CY54FCT240T, CY74FCT240T 8-BIT BUFFERS/LINE DRIVERS WITH 3-STATE OUTPUTS

SCCS017A - MAY 1994 - REVISED OCTOBER 2001

- **Function, Pinout, and Drive Compatible** With FCT and F Logic
- Reduced V_{OH} (Typically = 3.3 V) Versions of Equivalent FCT Functions
- **Edge-Rate Control Circuitry for** Significantly Improved Noise Characteristics
- I_{off} Supports Partial-Power-Down Mode Operation
- **ESD Protection Exceeds JESD 22**
 - 2000-V Human-Body Model (A114-A)
 - 200-V Machine Model (A115-A)
 - 1000-V Charged-Device Model (C101)
- **Matched Rise and Fall Times**
- Fully Compatible With TTL Input and **Output Logic Levels**
- CY54FCT240T
 - 48-mA Output Sink Current 12-mA Output Source Current
- CY74FCT240T
 - 64-mA Output Sink Current 32-mA Output Source Current
- 3-State Outputs

description

The 'FCT240T devices are octal buffers and line

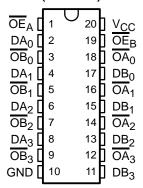
drivers designed to be employed as memory address drivers, clock drivers, and bus-oriented transmitters/receivers. These devices provide speed and drive capabilities equivalent to their

These devices are fully specified for partial-power-down applications using Ioff. The Ioff circuitry disables the outputs, preventing damaging current backflow through the device when it is powered down.

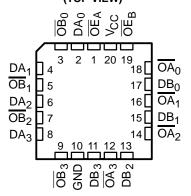
fastest bipolar logic counterparts, while reducing power consumption. The input and output voltage levels allow

direct interface with TTL, NMOS, and CMOS devices without external components.

CY54FCT240T . . . D PACKAGE CY74FCT240T...Q OR SO PACKAGE (TOP VIEW)



CY54FCT240T . . . L PACKAGE (TOP VIEW)





testing of all parameters.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include



Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of

ORDERING INFORMATION

TA	PACI	KAGE†	SPEED (ns)	ORDERABLE PART NUMBER	TOP-SIDE MARKING
	SOIC - SO	Tube	4.3	CY74FCT240CTSOC	FCT240C
	3010 - 30	Tape and reel	4.3	CY74FCT240CTSOCT	FC1240C
	QSOP - Q	Tape and reel	4.3	CY74FCT240CTQCT	FCT240C
	SOIC - SO	Tube	4.8	CY74FCT240ATSOC	FCT240A
−40°C to 85°C	3010 - 30	Tape and reel	4.8	CY74FCT240ATSOCT	FC1240A
	QSOP - Q	Tape and reel	4.8	CY74FCT240ATQCT	FCT240A
	SOIC - SO	Tube	8	CY74FCT240TSOC	FCT240
		Tape and reel	8	CY74FCT240TSOCT	FC1240
	QSOP - Q	Tape and reel	8	CY74FCT240TQCT	FCT240
	CDIP – D	Tube	4.7	CY54FCT240CTDMB	
FF00 1- 40F00	CDIP – D	Tube	5.1	CY54FCT240ATDMB	
–55°C to 125°C	LCC – L	Tube	5.1	CY54FCT240ATLMB	
	CDIP – D	Tube	9	CY54FCT240TDMB	

 $^{\ ^{\}dagger} \ Package \ drawings, standard \ packing \ quantities, thermal \ data, symbolization, and \ PCB \ design \ guidelines \ are \ available$ at www.ti.com/sc/package.

FUNCTION TABLE

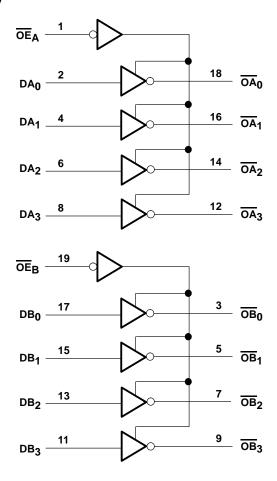
	INPUTS	ОИТРИТ	
ΘE _A	OE _B	D	ō
L	L	L	Н
L	L	Н	L
Н	Н	Χ	z

H = High logic level, L = Low logic level, X = Don't care, Z = High-impedance state



SCCS017A - MAY 1994 - REVISED OCTOBER 2001

logic diagram (positive logic)



absolute maximum rating over operating free-air temperature range (unless otherwise noted)†

Supply voltage range to ground potential	–0.5 V to 7 V
DC input voltage range	0.5 V to 7 V
DC output voltage range	0.5 V to 7 V
DC output current (maximum sink current/pin)	120 mA
Package thermal impedance, θ _{JA} (see Note 1): Q package	68°C/W
SO package	58°C/W
Ambient temperature range with power applied, T _A	–65°C to 135°C
Storage temperature range, T _{stq}	–65°C to 150°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: The package thermal impedance is calculated in accordance with JESD 51-7.



CY54FCT240T, CY74FCT240T 8-BIT BUFFERS/LINE DRIVERS WITH 3-STATE OUTPUTS SCCS017A - MAY 1994 - REVISED OCTOBER 2001

recommended operating conditions (see Note 2)

		CY	54FCT24	0T	CY74FCT240T		0T	UNIT
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
V_{IL}	Low-level input voltage			8.0			0.8	V
loh	High-level output current			-12			-32	mA
lOL	Low-level output current			48			64	mA
TA	Operating free-air temperature	-55		125	-40		85	°C

NOTE 2: All unused inputs of the device must be held at $V_{\hbox{CC}}$ or GND to ensure proper device operation.



SCCS017A - MAY 1994 - REVISED OCTOBER 2001

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

DADAMETER	TEST COMPITIONS			CY	54FCT24	0T	CY74FCT240T			UNIT
PARAMETER		TEST CONDITION	V 5	MIN	TYP [†]	MAX	MIN	TYP [†]	MAX	UNII
V.,,	$V_{CC} = 4.5 V$,	$I_{IN} = -18 \text{ mA}$			-0.7	-1.2				V
VIK	$V_{CC} = 4.75 \text{ V},$	$I_{IN} = -18 \text{ mA}$						-0.7	-1.2	V
	$V_{CC} = 4.5 \text{ V},$	$I_{OH} = -12 \text{ mA}$		2.4	3.3					
Voн	V _{CC} = 4.75 V	$I_{OH} = -32 \text{ mA}$					2			V
	VCC = 4.75 V	$I_{OH} = -15 \text{ mA}$					2.4	3.3		
V _{OL}	$V_{CC} = 4.5 \text{ V},$	$I_{OL} = 48 \text{ mA}$			0.3	0.55				V
VOL	$V_{CC} = 4.75 \text{ V},$	$I_{OL} = 64 \text{ mA}$						0.3	0.55	V
V _{hys}	All inputs				0.2			0.2		V
1.	$V_{CC} = 5.5 \text{ V},$	VIN = VCC				5				μΑ
1 ₁	$V_{CC} = 5.25 \text{ V},$	VIN = VCC							5	μΑ
1	$V_{CC} = 5.5 \text{ V},$	$V_{IN} = 2.7 \text{ V}$			±1	±1				μΑ
	$V_{CC} = 5.25 \text{ V},$	$V_{IN} = 2.7 \text{ V}$							±1	
1	$V_{CC} = 5.5 \text{ V},$	$V_{IN} = 0.5 V$				±1				μА
IΙL	$V_{CC} = 5.25 \text{ V},$	$V_{IN} = 0.5 V$							±1	μΑ
10711	$V_{CC} = 5.5 \text{ V},$	V _{OUT} = 2.7 V				10				μА
lozh	$V_{CC} = 5.25 \text{ V},$	V _{OUT} = 2.7 V							10	μΛ
lozu	$V_{CC} = 5.5 \text{ V},$	V _{OUT} = 0.5 V				-10				μА
lozL	$V_{CC} = 5.25 \text{ V},$	V _{OUT} = 0.5 V							-10	μΛ
los‡	$V_{CC} = 5.5 \text{ V},$	$V_{OUT} = 0 V$		-60	-120	-225				mA
105+	$V_{CC} = 5.25 \text{ V},$	V _{OUT} = 0 V					-60	-120	-225	IIIA
l _{off}	$V_{CC} = 0 V$,	V _{OUT} = 4.5 V				±1			±1	μΑ
laa	$V_{CC} = 5.5 \text{ V},$	$V_{IN} \le 0.2 V$	$V_{IN} \ge V_{CC} - 0.2 \text{ V}$		0.1	0.2				mA
Icc	$V_{CC} = 5.25 \text{ V},$	$V_{IN} \le 0.2 V$	$V_{IN} \ge V_{CC} - 0.2 V$					0.1	0.2	ША
Aloo	$V_{CC} = 5.5 \text{ V}, V_{IN} =$	3.4 V§, f ₁ = 0, Out	puts open		0.5	2				mA
∆lCC	$V_{CC} = 5.25 \text{ V}, V_{IN} =$	= 3.4 V§, f ₁ = 0, Ou	ıtputs open					0.5	2	ША
	$V_{CC} = 5.5 \text{ V}, O_{\underline{ne}} \text{ in}$		0% duty cycle,							
	Outputs open, OE _A V _{IN} ≤ 0.2 V or V _{IN} ≥		0.06	0.12				mA/		
^I CCD [¶]	$V_{CC} = 5.25 \text{ V, One}$		50% duty cycle.			+				MHz
	Outputs open, OEA	$= \overline{OE}_B = GND,$	to to daily of oile,					0.06	0.12	
	$V_{IN} \le 0.2 \text{ V or } V_{IN} \ge$	2 V _{CC} – 0.2 V								

[†] Typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$.



Not more than one output should be shorted at a time. Duration of short should not exceed one second. The use of high-speed test apparatus and/or sample-and-hold techniques are preferable to minimize internal chip heating and more accurately reflect operational values. Otherwise, prolonged shorting of a high output can raise the chip temperature well above normal and cause invalid readings in other parametric tests. In any sequence of parameter tests, Ios tests should be performed last.

[§] Per TTL-driven input (V_{IN} = 3.4 V); all other inputs at V_{CC} or GND

This parameter is derived for use in total power-supply calculations.

CY54FCT240T, CY74FCT240T 8-BIT BUFFERS/LINE DRIVERS WITH 3-STATE OUTPUTS

SCCS017A - MAY 1994 - REVISED OCTOBER 2001

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted) (continued)

DADAMETER	TEST CONDITIONS			CY	54FCT24	IOT	CY74FCT240T			UNIT
PARAMETER		TEST CONDITIONS			TYP [†]	MAX	MIN	TYP [†]	MAX	UNIT
		One bit switching at f ₁ = 10 MHz	$V_{IN} \le 0.2 \text{ V or}$ $V_{IN} \ge V_{CC} - 0.2 \text{ V}$		0.7	1.4				
	$V_{CC} = 5.5 V$,	at 50% duty cycle	$V_{IN} = 3.4 \text{ V or GND}$		1	2.4				
Outputs op OEA = OEI	Outputs open, OE _A = OE _B = GND	Eight bits switching at f ₁ = 2.5 MHz at 50% duty cycle	$V_{IN} = 0.2 \text{ V or}$ $V_{IN} \ge V_{CC} - 0.2 \text{ V}$		1.3	2.6				
			$V_{IN} = 3.4 \text{ V or GND}$		3.3	10.6				mA
		One bit switching at f ₁ = 10 MHz at 50% duty cycle	$V_{IN} \le 0.2 \text{ V or}$ $V_{IN} \ge V_{CC} - 0.2 \text{ V}$					0.7	1.4	IIIA
	$V_{CC} = 5.25 \text{ V},$ Outputs open, $\overline{OE}_A = \overline{OE}_B = \text{GND}$		$V_{IN} = 3.4 \text{ V or GND}$					1	2.4	
		Eight bits switching at f ₁ = 2.5 MHz at 50% duty cycle	$V_{IN} = 0.2 \text{ V or}$ $V_{IN} \ge V_{CC} - 0.2 \text{ V}$					1.3	2.6	
			$V_{IN} = 3.4 \text{ V or GND}$					3.3	10.6	
Ci					5	10		5	10	pF
Co					9	12		9	12	pF

 $[\]overline{\dagger}$ Typical values are at V_{CC} = 5 V, T_A = 25°C.

 $^{\#}$ IC = ICC + Δ ICC \times DH \times NT + ICCD (f₀/2 + f₁ \times N₁)

Where:

IC = Total supply current

ICC = Power-supply current with CMOS input levels

 ΔI_{CC} = Power-supply current for a TTL high input (V_{IN} = 3.4 V)

 D_H = Duty cycle for TTL inputs high N_T = Number of TTL inputs at D_H

I_{CCD} = Dynamic current caused by an input transition pair (HLH or LHL)

f₀ = Clock frequency for registered devices, otherwise zero

f₁ = Input signal frequency

N₁ = Number of inputs changing at f₁

All currents are in milliamperes and all frequencies are in megahertz.

|| Values for these conditions are examples of the I_{CC} formula.



CY54FCT240T, CY74FCT240T 8-BIT BUFFERS/LINE DRIVERS WITH 3-STATE OUTPUTS SCCS017A - MAY 1994 - REVISED OCTOBER 2001

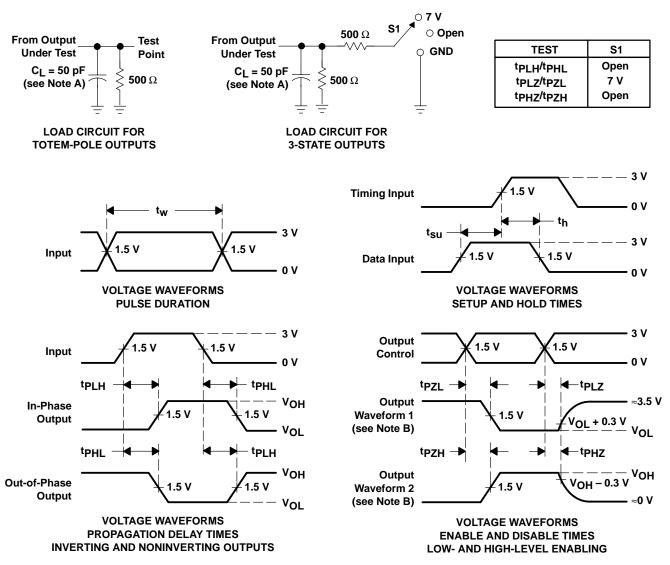
switching characteristics over operating free-air temperature range (see Figure 1)

PARAMETER	FROM	TO (OUTPUT)	CY54FC	CY54FCT240T		CY54FCT240AT		CY54FCT240CT		
	(INPUT)		MIN	MAX	MIN	MAX	MIN	MAX	UNIT	
t _{PLH}	D	ō	1.5	9	1.5	5.1	1.5	4.7	ne	
^t PHL		U	1.5	9	1.5	5.1	1.5	4.7	ns	
^t PZH	ŌĒ	ō	1.5	10.5	1.5	6.5	1.5	5.7	no	
t _{PZL}	OE		1.5	10.5	1.5	6.5	1.5	5.7	ns	
^t PHZ	ŌĒ	ō	1.5	10	1.5	5.9	1.5	4.6	no	
^t PLZ	OE .		1.5	10	1.5	5.9	1.5	4.6	ns	

switching characteristics over operating free-air temperature range (see Figure 1)

PARAMETER	FROM	TO (OUTPUT)	CY74FCT240T		CY74FCT240AT		CY74FCT	UNIT	
	(INPUT)		MIN	MAX	MIN	MAX	MIN	MAX	UNIT
^t PLH	D	ō	1.5	8	1.5	4.8	1.5	4.3	20
^t PHL		U	1.5	8	1.5	4.8	1.5	4.3	ns
^t PZH	ŌĒ	ō	1.5	10	1.5	6.2	1.5	5	no
t _{PZL}			1.5	10	1.5	6.2	1.5	5	ns
^t PHZ	ŌĒ	_	1.5	9.5	1.5	5.6	1.5	4.5	
^t PLZ	OE	0	1.5	9.5	1.5	5.6	1.5	4.5	ns

PARAMETER MEASUREMENT INFORMATION



NOTES: A. C_I includes probe and jig capacitance.

- B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
- C. The outputs are measured one at a time with one input transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms



IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third—party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Mailing Address:

Texas Instruments Post Office Box 655303 Dallas, Texas 75265

Copyright © 2001, Texas Instruments Incorporated