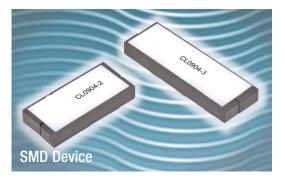
Effective June 2017 Supersedes January 2012

# CL0904 Multi-Phase power inductors



### **Product features**

- High current multi-phase inductor
- 50nH per phase coupled inductor
- Ferrite core material
- Patents pending
- Halogen free, lead free and RoHS compliant

#### Applications

For exclusive use with Maxim® Multi-phase controllers

#### **Environmental data**

- Storage temperature range (component): -40 °C to +125 °C
- Operating temperature range: -40 °C to +125 °C (ambient plus self-temperature rise)
- Solder reflow temperature: J-STD-020 (latest revision) compliant



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				Spe	cifications					
		Functio	nal					Test		
Part Number⁴	Inductor Phases	DCR (mΩ) ±10% @20°C	Rated Inductance per Phase (nH)	I Rated per Phase (Adc) <sup>3</sup>	I <sub>max</sub> Peak per Phase (Adc) <sup>3</sup>	Pin Number	OCL (nH) <sup>1, 2</sup>	Pin Number	OCL (nH) <sup>1, 2</sup>	Magnetized Inductance (nH) @ 5Adc (25°C)
CL0904-2-50TR-R	2	0.35	$50 \pm 20\%$	35	80	(1-2)	320±20%	(3-4)	320±20%	245
CL0904-3-50TR-R	3	0.35	$50 \pm 20\%$	35	50	(3-4)	400±20%	(1-2), (5-6)	380±20%	250

other than Maxim.

1. Open Circuit Inductance (OCL)

2. Test Parameters: 1MHz, 0.1Vrms, 0.0Adc.

3. The rated current, Imax peak current, and rated inductance per phase is determined by Volterra's testing and circuit design. Additional information can be provided by contacting Volterra.

4. Part Number Definition: CL0904-x-50TR-R

- CL0904= Product code and size

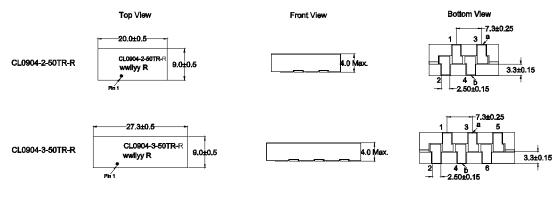
- "x" = number of phases

- "50" = inductance value per phase nH

- "TR" = Tape and Reel packaging

- "-R" suffix = RoHS compliant

## **Dimensions- mm**

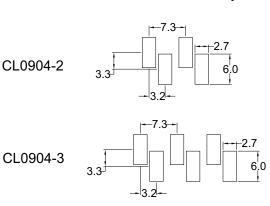


DCR measured from point 'a' to point "b"

Part Marking: CL0904= Product Code and Size -x (-2, -3)= Number of phases -50= inductance value per phase TR= Tape and Reel wwllyy= Date Code R=Revision Level

Soldering surfaces to be coplanar within 0.13 millimeters.

## Pad layout and schematics- mm



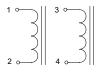
**Recommended Pad Layout** 

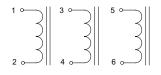
Schematic

A This device is licensed for use only when incorporated within a voltage regulator employing power

regulating devices manufactured by Maxim Integrated Devices. No license is granted expressly or

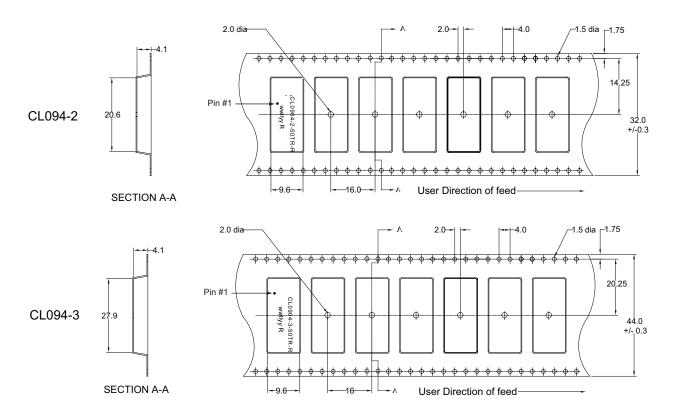
by implication to use this device with power regulating devices manufactured by any company





# CL0904 Multi-Phase power inductors

# Packaging information- mm



Supplied in tape and reel packaging, 1000 parts per 13" diameter reel.

# **Solder Reflow Profile**

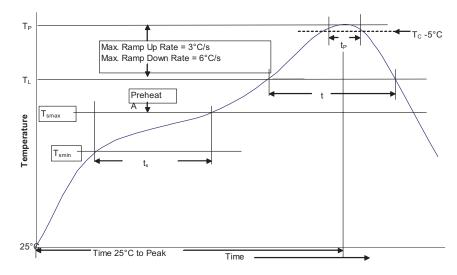


Table 1 - Sta	andard SnF	Pb Solder (T <sub>C</sub> )	
	Volume	Volume	
Package	mm <sup>3</sup>	mm <sup>3</sup>	
Thickness	<350	≥350	
<2.5mm	235°C	220°C	
≥2.5mm	220°C	220°C	
Table 2 - Le	ad (Pb) Fre	e Solder (T <sub>C</sub> )	
Table 2 - Lea	ad (Pb) Fre Volume	e Solder (T <sub>C</sub> ) Volume	Volume
Table 2 - Lea Package		• •	Volume mm <sup>3</sup>
	Volume	Volume	
Package	Volume mm <sup>3</sup>	Volume mm <sup>3</sup>	mm <sup>3</sup>

250°C

245°C

245°C

>2.5mm

# Reference JDEC J-STD-020

Profile Feature		Standard SnPb Solder	Lead (Pb) Free Solder	
Preheat and Soak	<ul> <li>Temperature min. (T<sub>smin</sub>)</li> </ul>	100°C	150°C	
	<ul> <li>Temperature max. (T<sub>smax</sub>)</li> </ul>	150°C	200°C	
	<ul> <li>Time (T<sub>smin</sub> to T<sub>smax</sub>) (t<sub>s</sub>)</li> </ul>	60-120 Seconds	60-120 Seconds	
Average ramp up rat	te T <sub>smax</sub> to T <sub>p</sub>	3°C/ Second Max.	3°C/ Second Max.	
Liquidous temperature (TL)		183°C	217°C	
Time at liquidous (t <sub>L</sub> )		60-150 Seconds	60-150 Seconds	
Peak package body temperature (T <sub>P</sub> )*		Table 1	Table 2	
Time $(t_p)^{**}$ within 5 °C of the specified classification temperature $(T_c)$		20 Seconds**	30 Seconds**	
Average ramp-down rate (Tp to Tsmax)		6°C/ Second Max.	6°C/ Second Max.	
Time 25°C to Peak Temperature		6 Minutes Max.	8 Minutes Max.	

 $^{\ast}$  Tolerance for peak profile temperature (T\_p) is defined as a supplier minimum and a user maximum.

\*\* Tolerance for time at peak profile temperature  $(t_p)$  is defined as a supplier minimum and a user maximum.

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