TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4538BP, TC4538BF

TC4538BP/TC4538BF Dual Precision

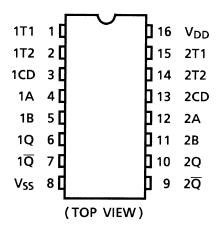
Retriggerable/Resettable Monostable Multivibrator

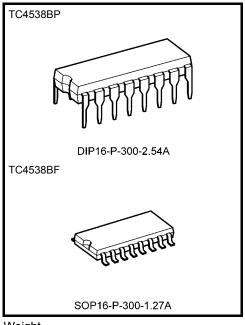
The TC4538BP/BF is the retriggerable/resettable monostable multivibrator and the trigger operation can be made at either the leading or trailing edge by 2 inputs of A and B. Since the output monostable pulse width is decided by time constant of the external resistor (Rx) and the external capacitor (Cx), it becomes possible to set a broad range of output pulse widths.

Features

• $t_{WOUT} = 10 \text{ ms} \pm 5\% \text{ (at } RX = 100 \text{ k}\Omega \text{ CX} = 0.1 \text{ }\mu\text{F}, \text{VDD} = 10 \text{ V)}$

Pin Assignment





Weight

DIP16-P-300-2.54A : 1.00 g (typ.) SOP16-P-300-1.27A : 0.18 g (typ.)

Truth Table (Note)

| | Inputs | | Out | puts | Note |
|---|--------|----|-----|------|---------------|
| Α | В | CD | Q | Q | Note |
| | Н | Н | П | П | Output Enable |
| | L | Н | L | Н | Inhibit |
| Н | \Box | Н | L | Н | Inhibit |
| L | \Box | Н | Л | Ţ | Output Enable |
| * | * | L | L | Н | Inhibit |

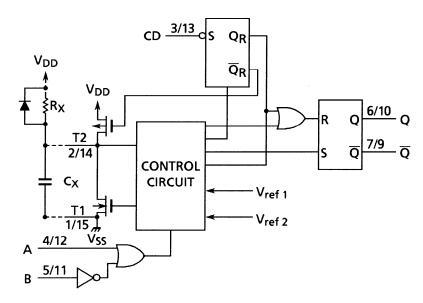
*: Don't care

Note: In the case of using only one circuit, CD should be tied to GND, T_2 , T_1 , Q, \overline{Q} should be tied to OPEN, and the other inputs should be tied to V_{CC} or GND.

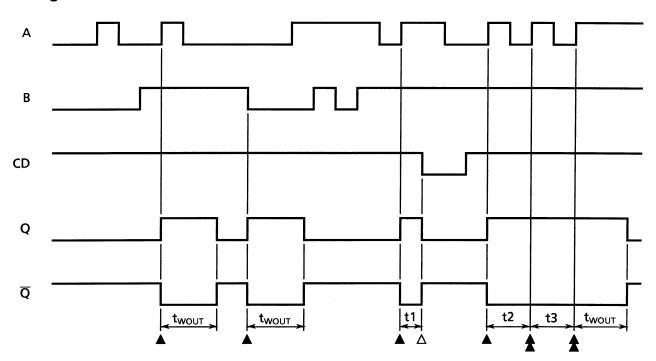
Start of commercial production 1978-04

Logic Diagram

1/2 TC4538BP/BF



Timing Chart



▲: TRIGGER

★: RETRIGGER

△: RESET

 $t_{\text{WOUT}} = C_X \cdot R_X$

2

 $t1\cdot t2\cdot t3\;;\quad t1\cdot t2\cdot t3\!<\!t_{WOUT}$

Absolute Maximum Ratings (Note)

| Characteristics | Symbol | Rating | Unit |
|-----------------------------|------------------|--|------|
| DC supply voltage | V_{DD} | V_{SS} – 0.5 to V_{SS} + 20 | V |
| Input voltage | V _{IN} | V _{SS} – 0.5 to V _{DD} + 0.5 | ٧ |
| Output voltage | V _{OUT} | V_{SS} – 0.5 to V_{DD} + 0.5 | V |
| DC input current | I _{IN} | ±10 | mA |
| Power dissipation | PD | 300 (DIP)/180 (SOIC) | mW |
| Operating temperature range | T _{opr} | −40 to 85 | °C |
| Storage temperature range | T _{stg} | -65 to 150 | °C |

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (V_{SS} = 0 V) (Note)

| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|----------------------|-----------------|----------------|-----------|------|----------|------|
| DC supply voltage | V_{DD} | _ | 3 | _ | 18 | V |
| Input voltage | V _{IN} | | 0 | _ | V_{DD} | V |
| External resistance | R _X | _ | 5 | _ | 1000 | kΩ |
| External capacitance | C _X | | No limits | | μF | |

3

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics ($V_{SS} = 0 V$)

| Characteristics | | Sym- | Test Condition | | -40°C | | 25°C | | | 85°C | | |
|-----------------------------|--------------------|------------------|--|------------------------|-------|------|-------|------------------|------|-------|------|------|
| | | bol | | V _{DD} (V) | Min | Max | Min | Тур. | Max | Min | Max | Unit |
| High-level output voltage | | | I _{OUT} < 1 μΑ V _{IN} = V _{SS} , V _{DD} | 5 | 4.95 | _ | 4.95 | 5.00 | _ | 4.95 | _ | |
| | | V _{OH} | | 10 | 9.95 | _ | 9.95 | 10.00 | _ | 9.95 | _ | V |
| | | | VIN - VSS, VDD | 15 | 14.95 | _ | 14.95 | 15.00 | _ | 14.95 | _ | |
| | | | I _{OUT} < 1 μA | 5 | _ | 0.05 | _ | 0.00 | 0.05 | _ | 0.05 | |
| Low-level voltage | output | V _{OL} | $V_{IN} = V_{SS}, V_{DD}$ | 10 | _ | 0.05 | _ | 0.00 | 0.05 | | 0.05 | V |
| | | | VIIV — V35, VDD | 15 | _ | 0.05 | _ | 0.00 | 0.05 | | 0.05 | |
| | | | V _{OH} = 4.6 V | 5 | -0.61 | _ | -0.51 | -1.0 | _ | -0.42 | _ | |
| | | | V _{OH} = 2.5 V | 5 | -2.50 | _ | -2.10 | -4.0 | _ | -1.70 | _ | mA |
| Output hig | gh current | IOH | V _{OH} = 9.5 V | 10 | -1.50 | _ | -1.30 | -2.2 | _ | -1.10 | _ | |
| | | | V _{OH} = 13.5 V | 15 | -4.00 | _ | -3.40 | -9.0 | _ | -2.80 | _ | |
| | | | $V_{IN} = V_{SS}, V_{DD}$ | | | | | | | | | |
| | | loL | V _{OL} = 0.4 V | 5 | 0.61 | _ | 0.51 | 1.5 | _ | 0.42 | _ | mA |
| Output lov | v current | | V _{OL} = 0.5 V | 10 | 1.50 | _ | 1.30 | 3.8 | _ | 1.10 | _ | |
| Output lov | Output low current | | V _{OL} = 1.5 V | 15 | 4.00 | _ | 3.40 | 15.0 | _ | 2.80 | _ | |
| | | | $V_{IN} = V_{SS}, V_{DD}$ | | | | | | | | | |
| | | VIH | V _{OUT} = 0.5 V, 4.5 V | 5 | 3.5 | _ | 3.5 | 2.75 | _ | 3.5 | _ | V |
| Input high | voltage | | V _{OUT} = 1.0 V, 9.0 V | 10 | 7.0 | _ | 7.0 | 5.50 | _ | 7.0 | _ | |
| input nign | voitage | VIH | V _{OUT} = 1.5 V, 13.5 V | 15 | 11.0 | _ | 11.0 | 8.25 | _ | 11.0 | _ | |
| | | | $ I_{OUT} < 1 \mu A$ | | | | | | | | | |
| | | V _{IL} | V _{OUT} = 0.5 V, 4.5 V | 5 | _ | 1.5 | _ | 2.25 | 1.5 | _ | 1.5 | V |
| Input low y | voltage | | V _{OUT} = 1.0 V, 9.0 V | 10 | _ | 3.0 | _ | 4.50 | 3.0 | _ | 3.0 | |
| Input low voltage | | VIL. | V _{OUT} = 1.5 V, 13.5 V | 15 | _ | 4.0 | _ | 6.75 | 4.0 | _ | 4.0 | v |
| | | | $ I_{OUT} < 1 \mu A$ | | | | | | | | | |
| Input | "H" level | l _{IH} | V _{IH} = 18 V | 18 | _ | 0.1 | | 10 ⁻⁵ | 0.1 | _ | 1.0 | μА |
| current | "L" level | I _Ι L | V _{IL} = 0 V | 18 | _ | -0.1 | _ | -10^{-5} | -0.1 | _ | -1.0 | μΛ |
| | | | | 5 | | 5 | _ | 0.005 | 5 | _ | 150 | |
| Quiescent supply current | | I _{DD} | $V_{IN} = V_{SS}, V_{DD}$ (Note) | 10 | _ | 10 | _ | 0.010 | 10 | _ | 300 | μА |
| | dirent | | | 15 | | 20 | _ | 0.015 | 20 | _ | 600 | |

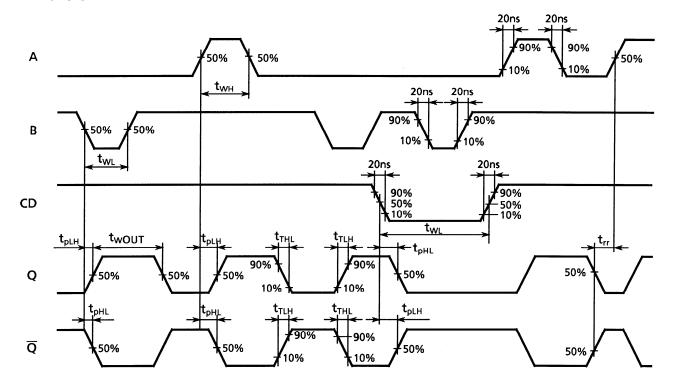
Note: All valid input combinations.

Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

| Characteristics | Symbol | Test Condition | | Min | Тур. | Max | Unit |
|--|----------------------------|--|---------------------|--------|-------|-------|------|
| Characteristics | Symbol | | V _{DD} (V) | IVIIII | | | |
| Output transition time | | | 5 | _ | 80 | 200 | |
| (low to high) | t _{TLH} | _ | 10 | _ | 50 | 100 | ns |
| (low to high) | | | 15 | _ | 40 | 80 | |
| Output transition time | | | 5 | _ | 80 | 200 | |
| (high to low) | t _{THL} | _ | 10 | _ | 50 | 100 | ns |
| (high to low) | | | 15 | _ | 40 | 80 | |
| Propagation delay time | t | | 5 | _ | 380 | 760 | |
| (A, B-Q, \overline{Q}) | t _{pLH} | _ | 10 | _ | 150 | 300 | ns |
| (A, B-Q, Q) | t _{pHL} | | 15 | _ | 100 | 220 | |
| Propagation delay time | + | | 5 | _ | 280 | 560 | |
| (CD-Q, \overline{Q}) | t _{pLH} | _ | 10 | _ | 110 | 250 | ns |
| (00-0, 0) | t _{pHL} | | 15 | _ | 75 | 190 | |
| Min input pulse width | tau | | 5 | _ | 60 | 120 | |
| (A, B) | t _{WH} | _ | 10 | _ | 30 | 60 | ns |
| (A, B) | tWL | | 15 | _ | 25 | 50 | |
| Min pulse width | tw∟ | | 5 | _ | 95 | 190 | ns |
| (CD) | | _ | 10 | _ | 45 | 90 | |
| (GD) | | | 15 | _ | 35 | 70 | |
| | t _{rr} | | 5 | _ | 0 | _ | |
| Min retrigger time | | _ | 10 | _ | 0 | _ | ns |
| | | | 15 | _ | 0 | _ | |
| | | R _X = 100 kΩ | 5 | _ | 206 | _ | |
| | t _{wOUT} | $C_X = 0.002 \mu F$ | 10 | _ | 204 | _ | μS |
| | | Oχ = 0.002 μι | 15 | _ | 205 | — | |
| | | R _X = 100 kΩ | 5 | 9.30 | 9.95 | 10.40 | |
| Output pulse width | | $C_X = 0.1 \mu F$ | 10 | 9.50 | 10.00 | 10.50 | ms |
| | | Oχ = 0.1 μι | 15 | 9.55 | 10.05 | 10.65 | |
| | | R _X = 100 kΩ | 5 | _ | 0.98 | _ | |
| | | $C_X = 100 \text{ kg}^2$ | 10 | _ | 1.00 | _ | s |
| | | - 10 μ | 15 | _ | 1.01 | _ | |
| D | cuits in Δt_{wOUT} | t av= (O2) + (O1) | 5 | _ | ±1 | _ | |
| Pulse width match between circuits in the same package | | $\frac{t_{WOUT}(Q2) - t_{W}(Q1)}{t_{WOUT}(Q1)} \times 100$ | 10 | _ | ±1 | _ | % |
| | | WOOT | 15 | _ | ±1 | _ | |
| Input capacitance | C _{IN} | _ | | _ | 5 | 7.5 | pF |

Waveform for Measurement of Dynamic Characteristics

Waveform

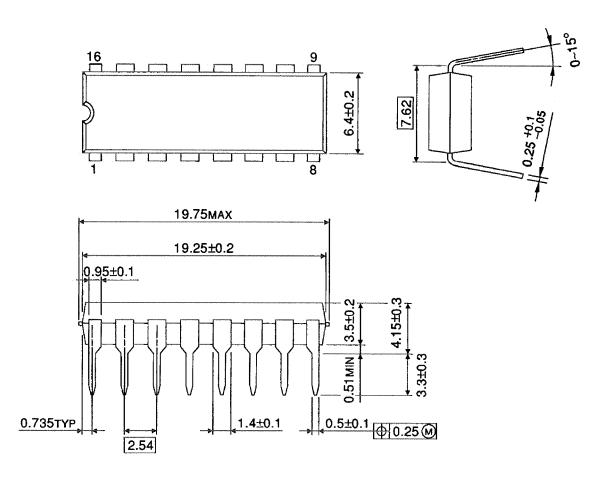


6 2014-03-01



Package Dimensions

DIP16-P-300-2.54A Unit: mm

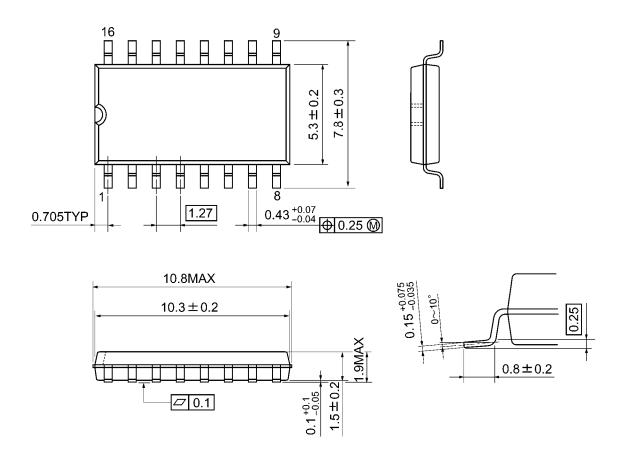


Weight: 1.00 g (typ.)



Package Dimensions

SOP16-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

RESTRICTIONS ON PRODUCT USE

- Toshiba Corporation, and its subsidiaries and affiliates (collectively "TOSHIBA"), reserve the right to make changes to the information in this document, and related hardware, software and systems (collectively "Product") without notice.
- This document and any information herein may not be reproduced without prior written permission from TOSHIBA. Even with TOSHIBA's written permission, reproduction is permissible only if reproduction is without alteration/omission.
- Though TOSHIBA works continually to improve Product's quality and reliability, Product can malfunction or fail. Customers are responsible for complying with safety standards and for providing adequate designs and safeguards for their hardware, software and systems which minimize risk and avoid situations in which a malfunction or failure of Product could cause loss of human life, bodily injury or damage to property, including data loss or corruption. Before customers use the Product, create designs including the Product, or incorporate the Product into their own applications, customers must also refer to and comply with (a) the latest versions of all relevant TOSHIBA information, including without limitation, this document, the specifications, the data sheets and application notes for Product and the precautions and conditions set forth in the "TOSHIBA Semiconductor Reliability Handbook" and (b) the instructions for the application with which the Product will be used with or for. Customers are solely responsible for all aspects of their own product design or applications, including but not limited to (a) determining the appropriateness of the use of this Product in such design or applications; (b) evaluating and determining the applicability of any information contained in this document, or in charts, diagrams, programs, algorithms, sample application circuits, or any other referenced documents; and (c) validating all operating parameters for such designs and applications. TOSHIBA ASSUMES NO LIABILITY FOR CUSTOMERS' PRODUCT DESIGN OR APPLICATIONS.
- PRODUCT IS NEITHER INTENDED NOR WARRANTED FOR USE IN EQUIPMENTS OR SYSTEMS THAT REQUIRE
 EXTRAORDINARILY HIGH LEVELS OF QUALITY AND/OR RELIABILITY, AND/OR A MALFUNCTION OR FAILURE OF WHICH
 MAY CAUSE LOSS OF HUMAN LIFE, BODILY INJURY, SERIOUS PROPERTY DAMAGE AND/OR SERIOUS PUBLIC IMPACT
 ("UNINTENDED USE"). Except for specific applications as expressly stated in this document, Unintended Use includes, without
 limitation, equipment used in nuclear facilities, equipment used in the aerospace industry, medical equipment, equipment used for
 automobiles, trains, ships and other transportation, traffic signaling equipment, equipment used to control combustions or explosions,
 safety devices, elevators and escalators, devices related to electric power, and equipment used in finance-related fields. IF YOU USE
 PRODUCT FOR UNINTENDED USE, TOSHIBA ASSUMES NO LIABILITY FOR PRODUCT. For details, please contact your
 TOSHIBA sales representative.
- Do not disassemble, analyze, reverse-engineer, alter, modify, translate or copy Product, whether in whole or in part.
- Product shall not be used for or incorporated into any products or systems whose manufacture, use, or sale is prohibited under any
 applicable laws or regulations.
- The information contained herein is presented only as guidance for Product use. No responsibility is assumed by TOSHIBA for any infringement of patents or any other intellectual property rights of third parties that may result from the use of Product. No license to any intellectual property right is granted by this document, whether express or implied, by estoppel or otherwise.
- ABSENT A WRITTEN SIGNED AGREEMENT, EXCEPT AS PROVIDED IN THE RELEVANT TERMS AND CONDITIONS OF SALE
 FOR PRODUCT, AND TO THE MAXIMUM EXTENT ALLOWABLE BY LAW, TOSHIBA (1) ASSUMES NO LIABILITY
 WHATSOEVER, INCLUDING WITHOUT LIMITATION, INDIRECT, CONSEQUENTIAL, SPECIAL, OR INCIDENTAL DAMAGES OR
 LOSS, INCLUDING WITHOUT LIMITATION, LOSS OF PROFITS, LOSS OF OPPORTUNITIES, BUSINESS INTERRUPTION AND
 LOSS OF DATA, AND (2) DISCLAIMS ANY AND ALL EXPRESS OR IMPLIED WARRANTIES AND CONDITIONS RELATED TO
 SALE, USE OF PRODUCT, OR INFORMATION, INCLUDING WARRANTIES OR CONDITIONS OF MERCHANTABILITY, FITNESS
 FOR A PARTICULAR PURPOSE, ACCURACY OF INFORMATION, OR NONINFRINGEMENT.
- Do not use or otherwise make available Product or related software or technology for any military purposes, including without
 limitation, for the design, development, use, stockpiling or manufacturing of nuclear, chemical, or biological weapons or missile
 technology products (mass destruction weapons). Product and related software and technology may be controlled under the
 applicable export laws and regulations including, without limitation, the Japanese Foreign Exchange and Foreign Trade Law and the
 U.S. Export Administration Regulations. Export and re-export of Product or related software or technology are strictly prohibited
 except in compliance with all applicable export laws and regulations.
- Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.
 Please use Product in compliance with all applicable laws and regulations that regulate the inclusion or use of controlled substances, including without limitation, the EU RoHS Directive. TOSHIBA ASSUMES NO LIABILITY FOR DAMAGES OR LOSSES
 OCCURRING AS A RESULT OF NONCOMPLIANCE WITH APPLICABLE LAWS AND REGULATIONS.

9 2014-03-01