

STC03DE170HV

Hybrid emitter switched bipolar transistor ESBT® 1700V - 3A - 0.33 Ω

Features

| V _{CS(ON)} | Ic | R _{CS(ON)} |
|---------------------|-----|---------------------|
| 1 V | 3 A | 0.33 Ω |

- Low equivalent on resistance
- Very fast-switch, up to 150 kHz
- Squared RBSOA, up to 1700V
- Very low C_{ISS} driven by $R_G = 47 \Omega$



■ Aux SMPS for three phase mains



The STC03DE170HV is manufactured in a hybrid structure, using dedicated high voltage Bipolar and low voltage MOSFET technologies, aimed to providing the best performance in ESBT topology. The STC03DE170HV is designed for use in aux flyback smps for any three phase application.

Applications

■ Aux SMPS for three phase mains

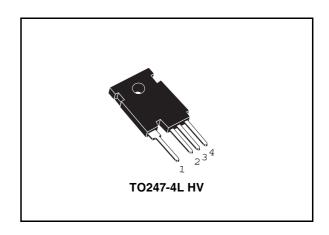


Figure 1. Internal schematic diagrams

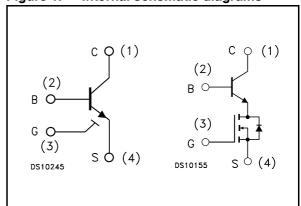


Table 1. Device summary

| Order code | Marking | Package | Packaging | |
|--------------|------------|-------------|-----------|--|
| STC03DE170HV | C03DE170HV | TO247-4L HV | Tube | |

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Electrical ratings STC03DE170HV

1 Electrical ratings

Table 2. Absolute maximum ratings

| Symbol | Parameter | Value | Unit |
|---------------------|---|------------|------|
| V _{CS(SS)} | Collector-source voltage (V _{BS} =V _{GS} =0V) | 1700 | V |
| V _{BS(OS)} | Base-source voltage (I _C =0, V _{GS} =0V) | 30 | V |
| V _{SB(OS)} | Source-base voltage (I _C =0, V _{GS} =0V) | 9 | V |
| V _{GS} | Gate-source voltage | ±20 | V |
| I _C | Collector current | 3 | Α |
| I _{CM} | Collector peak current (t _P < 5ms) | 6 | Α |
| I _B | Base current | 1 | Α |
| I _{BM} | Base peak current (t _P < 1ms) | 3 | Α |
| P _{tot} | Total dissipation at T _c ≤ 25°C | 100 | W |
| T _{stg} | Storage temperature | -40 to 150 | °C |
| TJ | Max. operating junction temperature | 125 | °C |

Table 3. Thermal data

| Symbol | Parameter | | Value | Unit |
|-----------------------|--------------------------------------|--|-------|------|
| R _{thj-case} | Thermal resistance junction-case max | | 1 | °C/W |

2 Electrical characteristics

 $(T_{case} = 25^{\circ}C \text{ unless otherwise specified})$

Table 4. Electrical characteristics

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|----------------------|---|---|------|------------|------------|----------|
| I _{CS(SS)} | Collector-source current (V _{BS} =V _{GS} =0V) | V _{CS(SS)} =1700V | | | 100 | μΑ |
| I _{BS(OS)} | Base-source current (I _C =0, V _{GS} =0V) | V _{BS(OS)} =30V | | | 10 | μΑ |
| I _{SB(OS)} | Source-base current (I _C =0, V _{GS} =0V) | V _{SB(OS)} =9V | | | 100 | μΑ |
| I _{GS(OS)} | Gate-source leakage (V _{BS} =0V) | V _{GS} = ± 20V | | | 500 | nA |
| V _{CS(ON)} | Collector-source ON voltage | V _{GS} =10V I _C =3A I _B =0.6A V _{GS} =10V I _C =1A I _B =100mA | | 1 0.3 | 1.2 0.6 | V V |
| h _{FE} | DC current gain | V _{GS} =10V V _{CS} =1V I _C =3A V _{GS} =10V V _{CS} =1V I _C =1A | 10 | 5 14 | | |
| V _{BS(ON)} | Base-source ON voltage | V _{GS} =10V I _C =3A I _B =0.6A V _{GS} =10V I _C =1A I _B =100mA | | 1 | 1.2 | V V |
| V _{GS(th)} | Gate threshold voltage | V _{BS} =V _{GS} I _B =250μA | 1.5 | | 3 | V |
| C _{iss} | Input capacitance | V _{CS} =25V f =1MHz V _{GS} =0V | | 750 | | pF |
| Q _{GS(tot)} | Gate-source Charge | V _{CS} =15V V _{GS} =10V V _{CB} =0V I _C =4A | | 12.5 | | nC |
| t _s | INDUCTIVE LOAD Storage time Fall time | $V_{GS} = 10V \qquad R_G = 47\Omega \\ V_{Clamp} = 1360V \qquad t_p = 4\mu s \\ I_C = 3A \qquad I_B = 0.6A$ | | 1000 15 | | ns ns |
| t _s | INDUCTIVE LOAD Storage time Fall time | $V_{GS} = 10V$ $R_{G} = 47\Omega$ $V_{Clamp} = 1360V$ $t_{p} = 4\mu s$ $I_{C} = 3A$ $I_{B} = 0.3A$ | | 590 15 | | ns ns |
| V _{CS(dyn)} | Collector-source dynamic voltage (500ns) | $\begin{aligned} &V_{CC} = &V_{Clamp} = &400 V \\ &V_{GS} = &10 V & I_{C} = &1.5 A \\ &I_{B} = &0.1 A & R_{G} = &47 \Omega \\ &t_{peak} = &500 ns & I_{Bpeak} = &3 A \end{aligned}$ | | 9.5 | | ٧ |

Electrical characteristics STC03DE170HV

Table 4. **Electrical characteristics**

| Symbol | Parameter | Test Conditions | Min. | Тур. | Max. | Unit |
|----------------------|--|--|------|------|------|------|
| V _{CS(dyn)} | Collector-source dynamic voltage (1µs) | $\begin{aligned} &V_{CC} = &V_{Clamp} = &400 V \\ &V_{GS} = &10 V &I_{C} = &1.5 A \\ &I_{B} = &0.1 A &R_{G} = &47 \Omega \\ &t_{peak} = &500 ns &I_{Bpeak} = &3 A \end{aligned}$ | | 9.5 | | ٧ |
| V _{CSW} | Maximum collector- source voltage switched without snubber | $R_G = 47\Omega$ $h_{FE} = 5$ $I_C = 4A$ | 1700 | | | ٧ |

Figure 3.

Note (1) Pulsed duration = 300 μ s, duty cycle \leq 1.5%

Electrical characteristics (curves) 2.1

Figure 2. **Output characteristics**

saturation voltage DG17330 V_{CE(sat)dyr} (V) l_c(A) 1.2A T_J=25°C $h_{FE} = 5$ 1A $V_{CC} = V_{Clamp} = 400V$ 4.0 0.8A $V_{GS}\!=10V$ 0.6A $R_G = 47 \Omega$ $I_{Bpeack} = 2I_C$ 6 0.4A 3.0 $I_C = 1.5A$ $I_B = 0.2A$ 2.0 1.0 4 6 8 10 12 t (µs) 0.2 0.4 0.8 V_{cs}(V)

Figure 4. Reverse biased safe

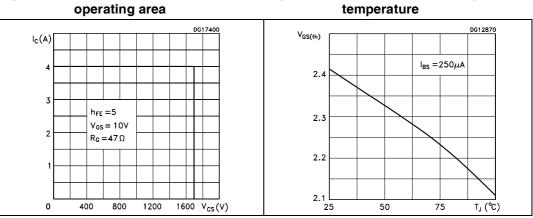


Figure 5. Gate threshold voltage vs

Dynamic collector-source

DG17390

Figure 6. DC current gain

Figure 7. Collector-source On voltage

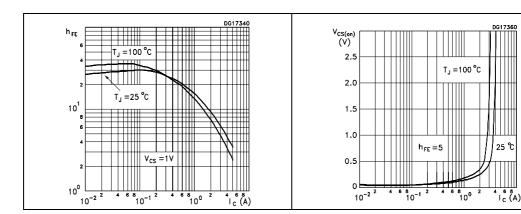


Figure 8. Collector-source On voltage Figure 9. Base-source On voltage

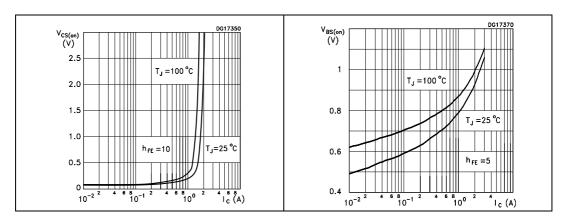
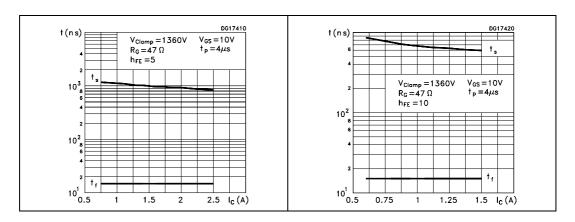


Figure 10. Inductive load switching time Figure 11. Inductive load switching time

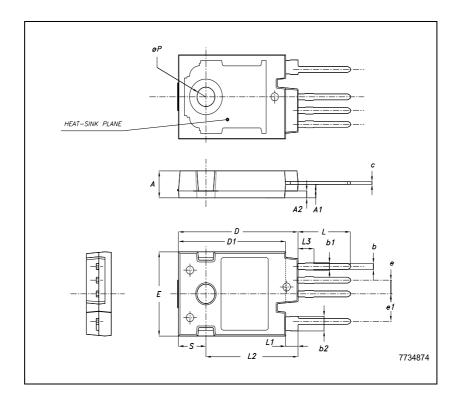


3 Package mechanical data

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com

TO247-4L HV MECHANICAL DATA

| DIM. | | mm. | |
|------|-------|-------|-------|
| DIW. | MIN. | TYP | MAX. |
| Α | 4.85 | | 5.15 |
| A1 | 2.20 | 2.50 | 2.60 |
| A2 | | 1.27 | |
| b | 0.95 | 1.10 | 1.30 |
| b2 | 2.50 | | 2.90 |
| С | 0.40 | | 0.80 |
| D | 23.85 | 24 | 24.15 |
| D1 | | 21.50 | |
| E | 15.45 | 15.60 | 15.75 |
| е | 2.54 | | |
| e1 | 5.08 | | |
| L | 10.20 | | 10.80 |
| L1 | 2.20 | 2.50 | 2.80 |
| L2 | | 18.50 | |
| L3 | | 3 | |
| øΡ | 3.55 | | 3.65 |
| S | | 5.50 | |



Revision history STC03DE170HV

4 Revision history

 Table 5.
 Revision history

| Date | Revision | Changes |
|-------------|----------|--|
| 26-Sep-2006 | 1 | First release. |
| 12-Jul-2007 | 2 | Improved electrical specification. Updated figures: 2,3,4,6,7,8,9,10 and 11. |

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