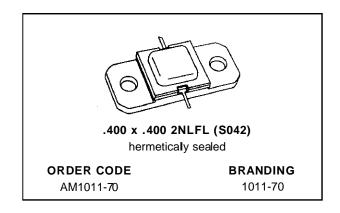


AM1011-070

RF & MICROWAVE TRANSISTORS L-BAND AVIONICS APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- Pout = 70 W MIN. WITH 6.7 dB GAIN

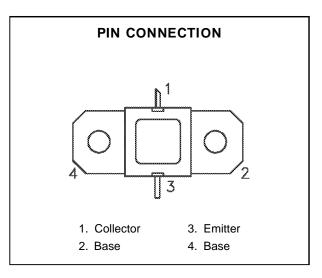


DESCRIPTION

The AM1011-070 device is a high power Class C transistor specifically designed for L-Band Avionics transponder/interrogator pulsed output and driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles and temperatures and is capable of withstanding severe output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM1011-070 is supplied in the AMPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
P _{DISS}	Power Dissipation* (T _C ≤ 100°C)	200	W
Ic	Device Current*	8.0	А
Vcc	Collector-Supply Voltage*	32	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

R _{TH(j-c)} Junction-Case Thermal Resistance*	0.68	°C/W
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^{*}Applies only to rated RF amplifier operation

September 1992

ELECTRICAL SPECIFICATIONS (T_{case} = 25°C)

STATIC

Symbol	Test Conditions	Value			IIn:4		
		Min.	Тур.	Max.	- Unit		
BV _{CBO}	I _C = 25mA	$I_{E} = 0mA$		55			V
BVEBO	I _E = 10mA	$I_C = 0mA$		3.5	_	_	V
BVcer	IC = 25mA	$R_{BE} = 10\Omega$		55		_	V
ICES	V _{CE} = 35V			_		20	mA
h _{FE}	V _{CE} = 5V	I _C = 2mA		20	_	200	_

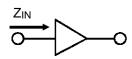
DYNAMIC

Symbol	Test Conditions			Value			Unit
Symbol		Test Conditions			Тур.	Max.	Oiiit
Роит	f = 1090 MHz	$P_{IN} = 15W$	Vcc = 28V	70	_	_	W
ης	f = 1090 MHz	P _{IN} = 15W	V _{CC} = 28V	45	_	_	%
G _P	f = 1090 MHz	P _{IN} = 15W	V _{CC} = 28V	6.7	_	_	dB

Note: Pulse Width = $100 \mu Sec$ Duty Cycle = 2%

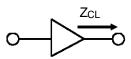
IMPEDANCE DATA

TYPICAL INPUT IMPEDANCE



TYPICAL COLLECTOR LOAD IMPEDANCE

FREQ.	$Z_{IN} (\Omega)$	$Z_{CL}\left(\Omega\right)$
L = 1025 MHz	4.7 + j 4.7	3.6 + j 4.3
H = 1090 MHz	4.7 + j 3.9	3.3 + j 4.4

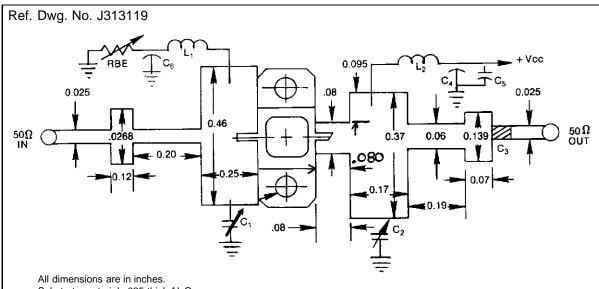


 $P_{IN} = 15 W$

 $V_{CC} = 28 \text{ V}$

Normalized to 50 ohms

TEST CIRCUIT



Substrate material: .025 thick Al₂O₃

: 0.3—3.5 pF Johanson Gigatrim Capacitor C2 : 0.3—3.5 pF Johanson Gigatrim Capacitor
C3 : 100 pF Chip Capacitor

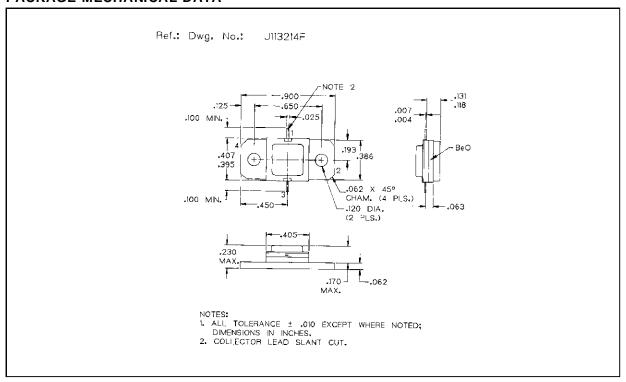
: 1500 pF Erie Feedthru, or Equiv.

: 100 MF Electrolytic Capacitor, 50V: 1500 pF Erie Feedthrough, or Equiv. C5 C6

: #32 Wire, 4 Turn .062 I.D. L1 L2 : #32 Wire, 4 Turn .062 I.D.

RBE : 0 - 1.0 Ohm

PACKAGE MECHANICAL DATA



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