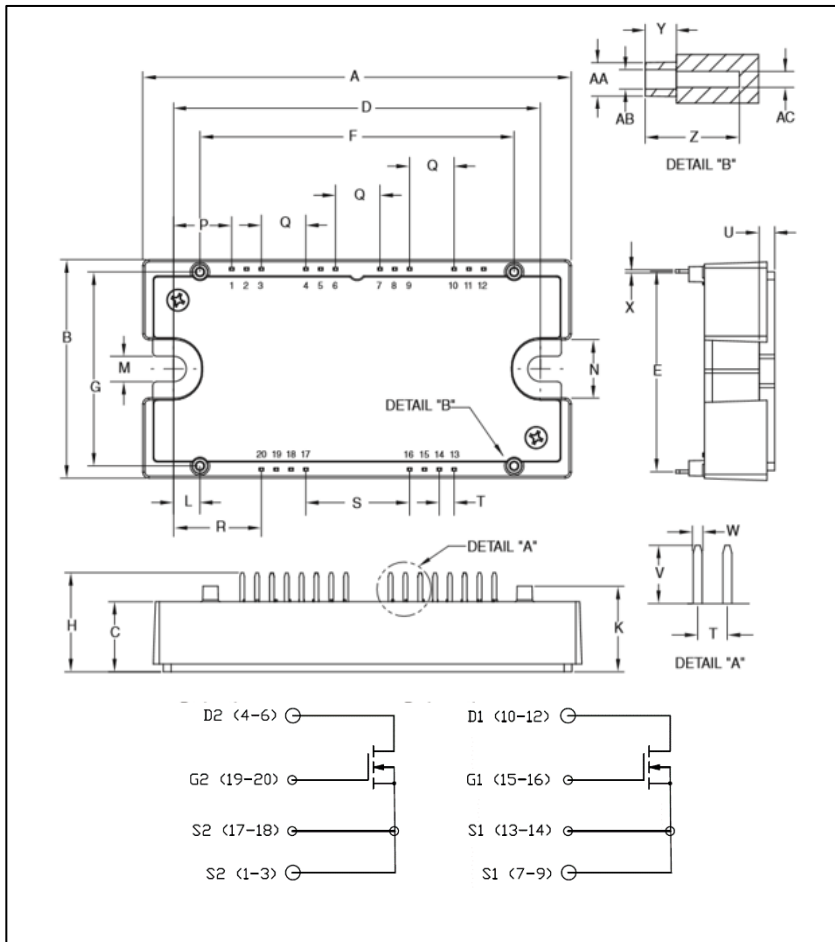


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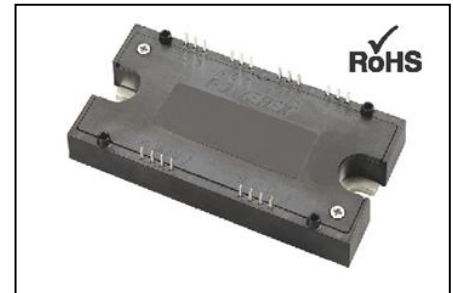
**Split Dual SiC
MOSFET Module**
100 Amperes/ 1200Volts/ 13mΩ



QJD1210SB2 (Outline Drawing)

Dimensions	Inches	Millimeters
A	4.32	109.8
B	2.21	56.1
C	0.71	18.0
D	3.70±0.02	94.0±0.5
E	2.026	51.46
F	3.17	80.5
G	1.96	49.8
H	1.00	25.5
K	0.87	22.0
L	0.266	6.75
M	0.26	6.5
N	0.59	15.0
P	0.586	14.89

Dimensions	Inches	Millimeters
Q	0.449	11.40
R	0.885	22.49
S	1.047	26.6
T	0.15	3.80
U	0.16	4.0
V	0.30	7.5
W	0.045	1.15
X	0.03	0.8
Y	0.16	4.0
Z	0.47	12.1
AA	0.17 Dia.	4.3 Dia.
AB	0.10 Dia.	2.5 Dia.
AC	0.08 Dia.	2.1 Dia.



Split Dual SiC MOSFET Module
100 Amperes/1200 Volts

Description:

Powerex Silicon Carbide MOSFET Modules are designed for use in high frequency applications. Each module consists of two MOSFET Silicon Carbide Transistors. All Components and interconnects are isolate from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- Junction Temperature: 175°C
- Low Internal Inductance
- Industry Leading $R_{ds(on)}$
- High Speed Switching
- Low Switching Losses
- Low Capacitance
- Low Drive Requirement
- High Power Density
- Isolated Baseplate
- Aluminum Nitride Isolation
- Two Individual Switches per Module
- Copper Baseplate
- RoHS Compliant
- Wolfspeed® 3rd Generation Chips

Applications:

- Energy Saving Power Systems
- High Frequency Type Power Systems
- High Temperature Power Systems

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Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	QJD1210SB2	Units
Drain-Source Voltage (G-S Short)	V_{DSS}	1200	Volts
Gate-Source Voltage	V_{GSS}	-4 / +15	Volts
Drain Current (Continuous) at $T_c=85^\circ\text{C}$	I_D	100	Amperes
Drain Current (Pulsed)*1	$I_{D(pulse)}$	200	Amperes
Maximum Power Dissipation ($T_c=25^\circ\text{C}$, $T_j < 175^\circ\text{C}$)	P_D	250	Watts
Junction Temperature	T_j	-40 to 175	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 to 150	$^\circ\text{C}$
Mounting Torque, M6 Mounting Screws	—	40	In-lb
Module Weight (Typical)	—	270	Grams
Isolation Voltage	V_{ISO}	3000	Volts

*1 Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(MAX)}$ rating.

DC Characteristics, $T_j=25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain Source Leakage Current	I_{DSS}	$V_{DS}=1200V$, $V_{GS}=0V$ $T_j=175^\circ\text{C}$			0.5	mA
					TBD	mA
Gate Source Leakage Current	I_{GSS}	$V_{DS}=0V$, $V_{GS}=15V$			1000	nA
Recommended Gate Source Voltage	V_{GS}			-4/15V		Volts
Maximum Gate Source Voltage	$V_{GS(max)}$	$V_{DS}=0V$			-8/19V	
Gate Source Threshold Voltage	V_{th}	$V_{GS}=V_{DS}$, $I_{DS}=26.5mA$	1.8	2.5	3.6	Volts
Drain Source On-Resistance	$R_{DS(on)}$	$V_{GS}=15V$ $I_{SD}=180A$ $T_j=175^\circ\text{C}$	10	13	18	mΩ
				21		mΩ
Internal Gate Source Series Resistance Per Upper or Lower MOSFET	$R_{gate(esr)}$	$V_{GS}=0V$, $f=1MHz$, Drain Floating		6.7		Ω

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Dynamic Characteristics, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C _{ISS}			7.6		nF
Output Capacitance	C _{OSS}	V _{GS} =0V, V _{DS} =1000V, f=1MHz		284		pF
Reverse Transfer Capacitance	C _{RSS}			18		pF
Turn-On Delay Time	t _{D(on)}			TBD		nS
Rise Time	t _R	V _{DD} =120V, V _{GS} = -4 to 15V,		TBD		nS
Turn-Off Delay Time	t _{D(off)}	I _D =100A, R _{G(max)} =0Ω		TBD		nS
Fall Time	t _F			TBD		nS
Turn-On Energy	E _{on}	V _{DD} =600V, V _{GS} =-4 to 15V,		2.6		mJ
Turn-Off Energy	E _{off}	I _D =100A, R _G =0Ω		2.4		mJ
Recovery Energy	E _{rr}	T _J =175°C		0.38		mJ
Total Gate Charge	Q _G	V _{DD} =800V, V _{GS} =-4 to 15V I _D =100A, T _J =25°C		260		nC

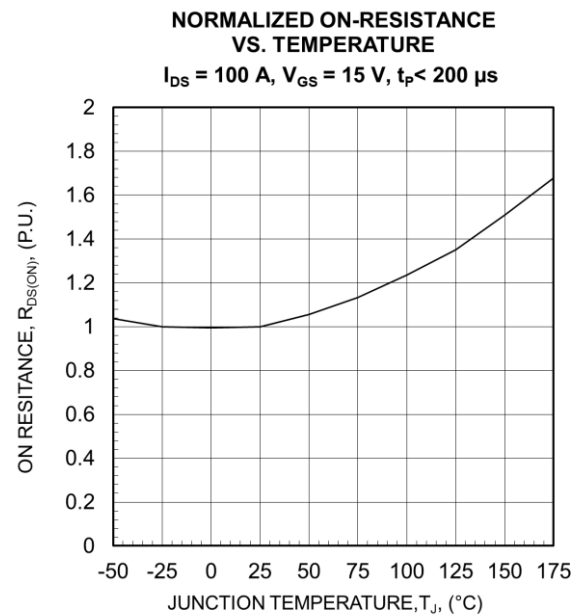
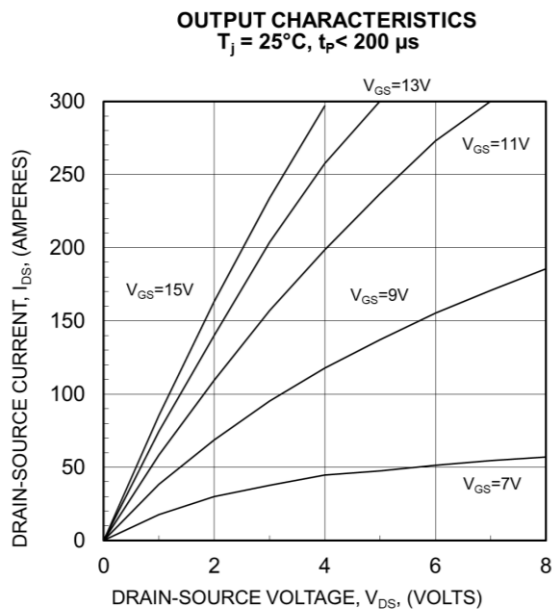
Body Diode, T_J=25°C unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Reverse Recovery Current, Peak	I _{RR}	V _{GS} =-4V, I _D =100A		65		Amps
Reverse Recovery Charge	Q _{RR}	V _R =800V, di/dt=3000A/μs		1.8		μC
Diode Forward Voltage	V _{SD}	V _{GS} =-4V I _{SD} =32.1A		4.6		V
		T _J =175°C		4.2		V

Thermal Resistance Characteristics

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	R _{th(j-c)}	Per MOSFET			0.045	°C/W
Contact Thermal Resistance	R _{th(c-s)}	Per ½ Module, Thermal Grease Applied		0.04		°C/W

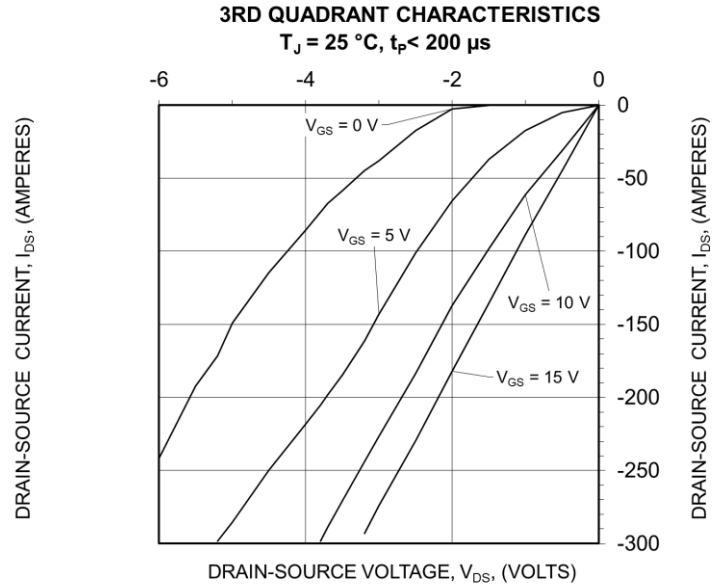
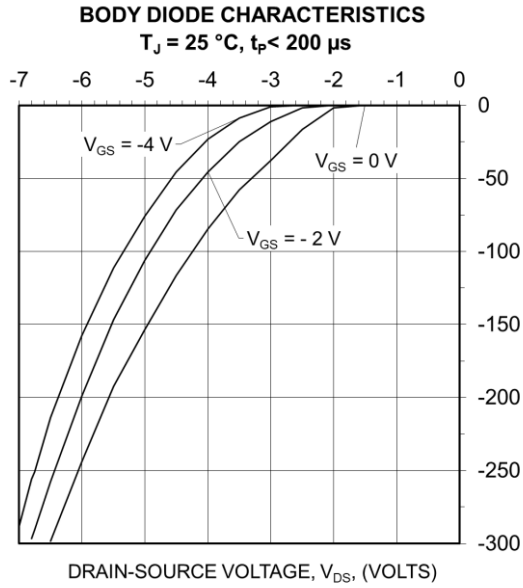
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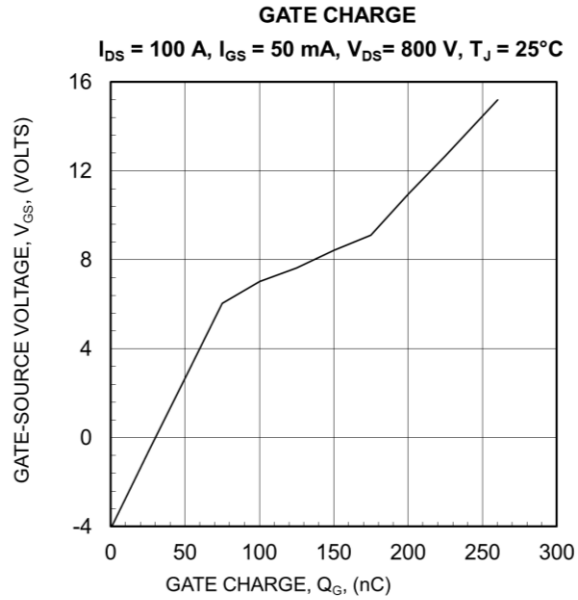
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