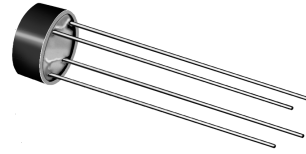


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## B40C800 – B500C800 0.8A SINGLE-PHASE BRIDGE RECTIFIER

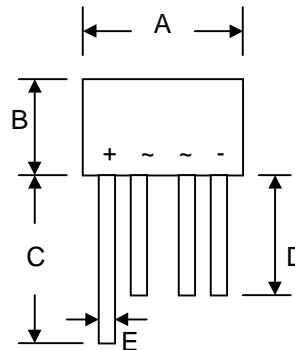


### Features

- Glass passivated chip junctions
- Typical IR less than 0.1uA
- High Current Capability
- High Reliability
- High Surge Current Capability
- Ideal for Printed Circuit Boards
- Excellent Case Dielectric Strength

### Mechanical Data

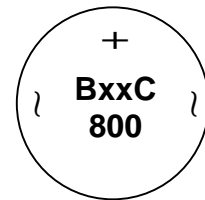
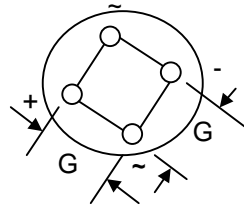
- Case: Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-750, Method 2026
- Polarity: As Marked on Body
- Weight: 1.1 grams (approx.)
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version,**



Case Style WOG

Dim	Min	Max
A	8.60	9.10
B	5.0	5.50
C	27.9	—
D	25.4	—
E	0.71	0.81
G	4.60	5.60

All Dimensions in mm



BxxC800 = Device Number  
 xx = 40, 80, 125, 250, 380, 500  
 Polarity = As Marked on Body

### Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

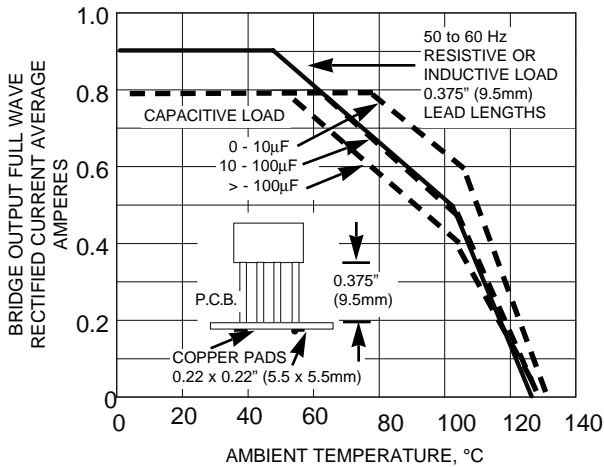
Single Phase, half wave, 60Hz, resistive or inductive load.  
 For capacitive load, derate current by 20%.

Characteristic	Symbol	B40C800	B80C800	B125C800	B250C800	B380C800	B500C800	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	$V_{RRM}$ $V_{RWM}$ $V_R$	100	200	300	600	900	1200	V
Input Voltage Recommended	$V_{R(RMS)}$	40	80	125	250	380	500	V
Average Rectified Output Current (Note 1) @ $T_A = 50^\circ\text{C}$	$I_o$	0.8						A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	45						A
Forward Voltage (per element) @ $I_F = 0.8\text{A}$	$V_{FM}$	1.0						V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 100^\circ\text{C}$	$I_{RM}$	5.0 500						$\mu\text{A}$
Operating Temperature Range	$T_j$	-55 to +125						$^\circ\text{C}$
Storage Temperature Range	$T_{STG}$	-55 to +150						$^\circ\text{C}$

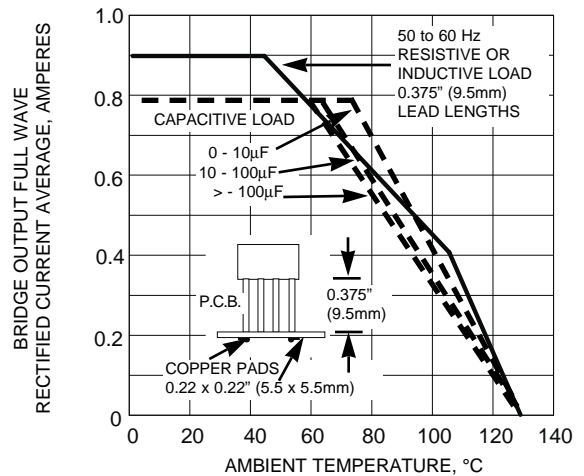
Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case.

## RATINGS AND CHARACTERISTICS CURVES B40C800G THRU B380C800G

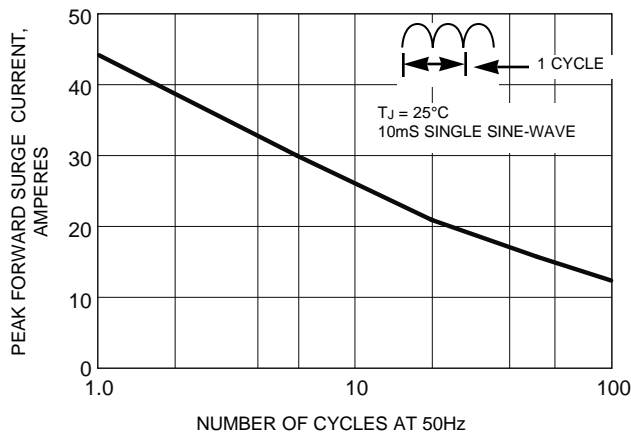
**FIG.1 - DERATING CURVES OUTPUT RECTIFIED CURRENT FOR B40C800G...B125C800G**



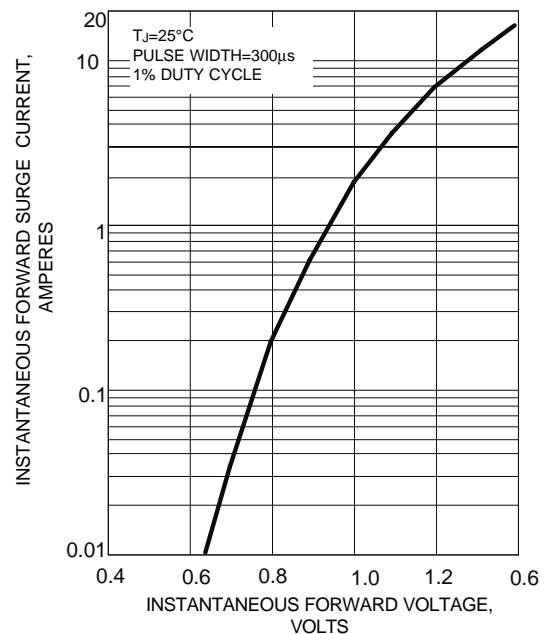
**FIG. 2 - DERATING CURVES FOR OUTPUT RECTIFIED CURRENT B250C800G...B380C800G**



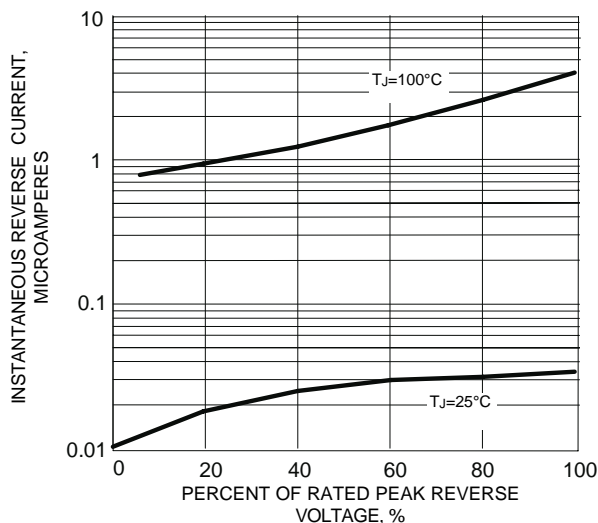
**FIG. 3 - MAXIMUM NON-REPETITIVE PEAK FORWARD CURRENT PER LEG**



**FIG. 4 - TYPICAL FORWARD CHARACTERISTICS PER LEG**



**FIG.5 - TYPICAL REVERSE CHARACTERISTICS PER LEG**



**FIG. 6 - TYPICAL JUNCTION CAPACITANCE PER LEG**

