



Low-Frequency General-Purpose Amplifier Driver, Muting Circuit Applications

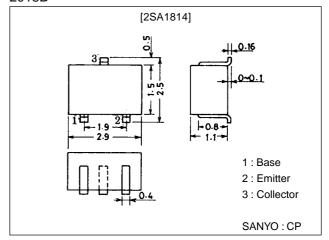
Features

- · Very small-sized package permitting 2SA1814-applied sets to be made smaller and slimmer.
- · Adoption of FBET process.
- · High DC current gain (h_{FE}=500 to 1200).
- · Low collector-to-emitter saturation voltage $(V_{CEO(sat)} \le 0.3V)$.
- · High V_{EBO} ($V_{EBO} \ge 15V$).

Package Dimensions

unit:mm

2018B



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CBO}		-30	V
Collector-to-Emitter Voltage	V _{CEO}		-25	V
Emitter-to-Base Voltage	V _{EBO}		-15	V
Collector Current	l _C		-150	mA
Collector Current (Pulse)	I _{CP}		-300	mA
Base Current	IB		-30	mA
Collector Dissipation	PC	Mounted on board	250	mW
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

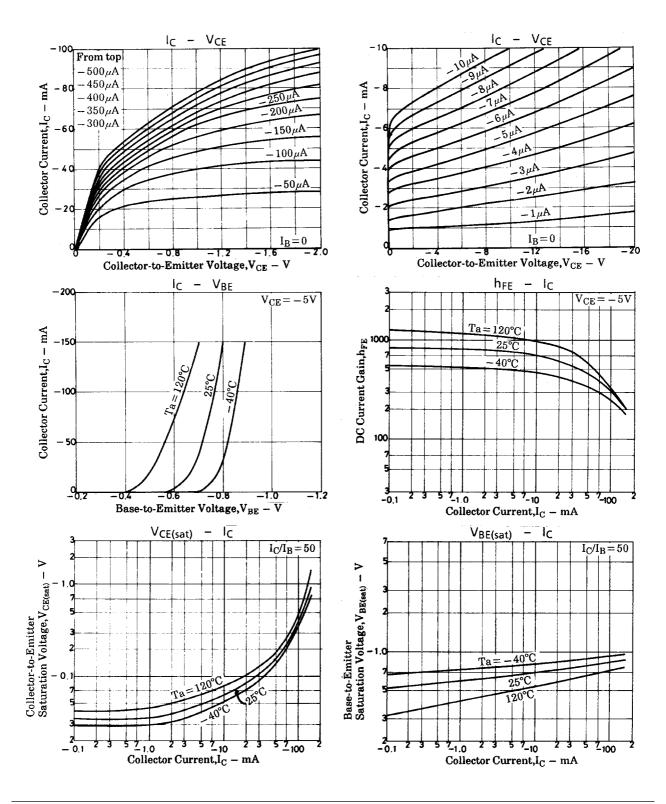
Electrical Characteristics at Ta = 25°C

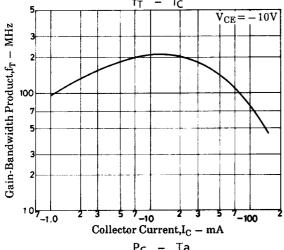
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector Cutoff Current	I _{CBO}	V _{CB} =-20V, I _E =0			-0.1	μA
Emitter Cutoff Current	I _{EBO}	V _{EB} =-10V, I _C =0			-0.1	μA
DC Current Gain	hFE	V_{CE} =-5V, I_{C} =-1mA	500	800	1200	
Gain-Bandwidth Product	fT	V _{CE} =-10V, I _C =-10mA		210		MHz
Output Capacitance	C _{ob}	V _{CB} =-10V, f=1MHz		2.6		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =-50mA, I _B =-1mA		-0.15	-0.3	V
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =-50mA, I _B =-1mA		-0.78	-1.1	V

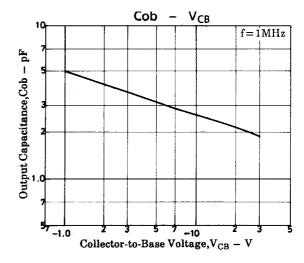
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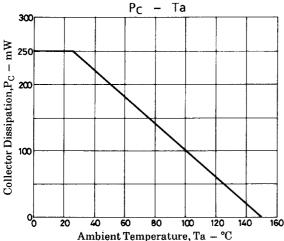
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Base Breakdown Voltage	V(BR)CBO	$I_{C}=(-)10\mu A, I_{E}=0$	-30			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	$I_C=(-)1mA$, $R_{BE}=\infty$	-25			V
Emitter-to-Base Breakdown Voltage	V(BR)EBO	I _E =(-)10μΑ, I _C =0	-15			V









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