DISCRETE SEMICONDUCTORS

DATA SHEET

BFQ19NPN 5 GHz wideband transistor

Product specification
File under Discrete Semiconductors, SC14

September 1995





NPN 5 GHz wideband transistor

BFQ19

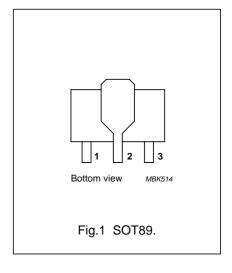
DESCRIPTION

NPN transistor in a SOT89 plastic envelope intended for application in thick and thin-film circuits. It is primarily intended for use in UHF and microwave amplifiers such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analyzers etc.

The transistor features very low intermodulation distortion and high power gain. Due to its very high transition frequency, it also has excellent wideband properties and low noise up to high frequencies.

PINNING

PIN DESCRIPTION				
Code: FB				
1	emitter			
2	collector			
3	base			



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
V _{CEO}	collector-emitter voltage	open base	_	15	V
I _C	DC collector current		_	100	mA
P _{tot}	total power dissipation	up to T _s = 145 °C (note 1)	_	1	W
f _T	transition frequency	$I_c = 50 \text{ mA}; V_{CE} = 10 \text{ V}; f = 500 \text{ MHz};$ $T_j = 25 ^{\circ}\text{C}$	5.5	_	GHz
C _{re}	feedback capacitance	$I_c = 10 \text{ mA}; V_{CE} = 10 \text{ V}; f = 1 \text{ MHz};$ $T_{amb} = 25 \text{ °C}$	1.3	_	pF
F	noise figure	$I_c = 50 \text{ mA}; V_{CE} = 10 \text{ V}; Z_s = \text{opt.};$ $f = 500 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	3.3	_	dB

LIMITING VALUES

In accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	PARAMETER CONDITIONS			
V _{CBO}	collector-base voltage	open emitter	_	20	V
V _{CEO}	collector-emitter voltage	open base	_	15	V
V _{EBO}	emitter-base voltage	open collector	_	3.3	V
I _C	DC collector current		_	100	mA
I _{CM}	peak collector current	f > 1 MHz	_	150	mA
P _{tot}	total power dissipation	up to T _s = 145 °C (note 1)	_	1	W
T _{stg}	storage temperature		-65	150	°C
T _j	junction temperature		_	175	°C

Note

1. T_{s} is the temperature at the soldering point of the collector tab.

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THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
R _{th j-s}	thermal resistance from junction to soldering point	up to T _s = 145 °C (note 1)	30 K/W

Note

1. $T_{\mbox{\scriptsize S}}$ is the temperature at the soldering point of the collector tab.

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I _{CBO}	collector cut-off current	I _E = 0; V _{CB} = 10 V	_	_	100	nA
h _{FE}	DC current gain	I _C = 70 mA; V _{CE} = 10 V	25	80	_	
C _c	collector capacitance	I _E = i _e = 0; V _{CB} = 10 V; f = 1 MHz	_	1.6	_	pF
C _e	emitter capacitance	I _C = i _c = 0; V _{EB} = 0.5 V; f = 1 MHz	_	5	_	pF
C _{re}	feedback capacitance	I_{C} = 10 mA; V_{CE} = 10 V; f = 1 MHz; T_{amb} = 25 °C	_	1.3	_	pF
f _T	transition frequency	I _C = 70 mA; V _{CE} = 10 V; f = 500 MHz	4.4	5.5	_	GHz
G _{UM}	maximum unilateral power gain (note 1)	I _C = 50 mA; V _{CE} = 10 V; f = 500 MHz; T _{amb} = 25 °C	_	11.5	_	dB
		I_{C} = 50 mA; V_{CE} = 10 V; f = 800 MHz; T_{amb} = 25 °C	_	7.5	_	dB
F	noise figure	$I_C = 50 \text{ mA}; V_{CE} = 10 \text{ V}; Z_s = \text{opt.};$ $f = 500 \text{ MHz}; T_{amb} = 25 ^{\circ}\text{C}$	_	3.3	_	dB

Note

1.
$$G_{UM}$$
 is the maximum unilateral power gain, assuming S_{12} is zero and
$$G_{UM} = 10 \log \frac{\left|S_{21}\right|^2}{\left(1-\left|S_{11}\right|^2\right)\left(1-\left|S_{22}\right|^2\right)} \, dB.$$

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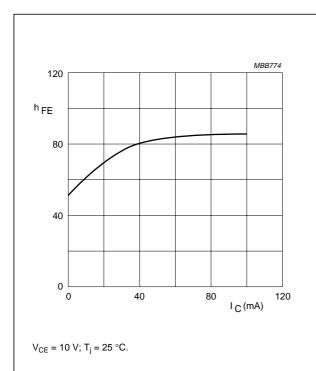
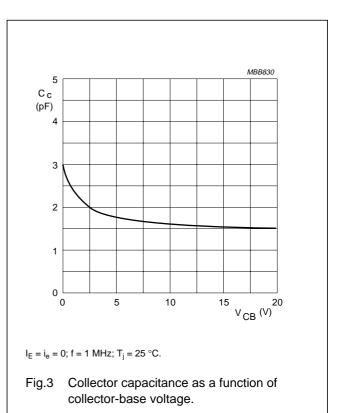
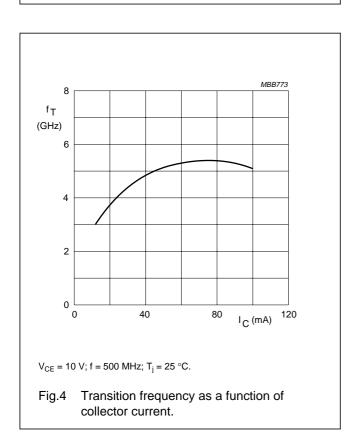


Fig.2 DC current gain as a function of collector current.





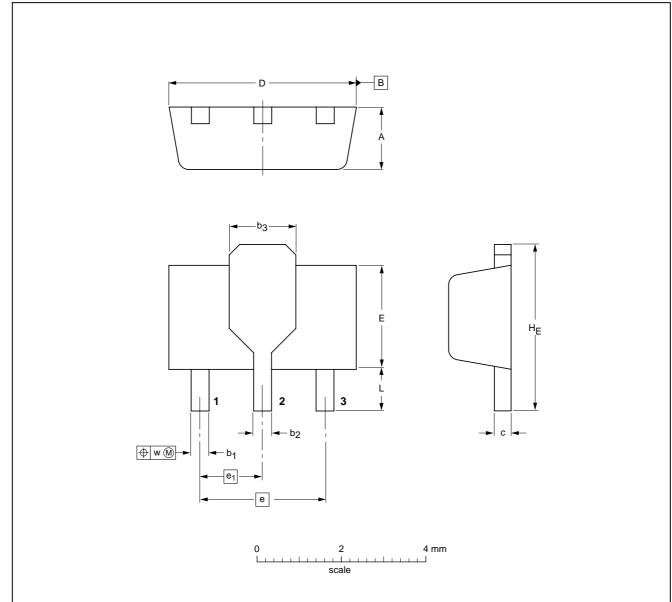
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PACKAGE OUTLINE

Plastic surface mounted package; collector pad for good heat transfer; 3 leads

SOT89



DIMENSIONS (mm are the original dimensions)

UNIT	A	b ₁	b ₂	b ₃	С	D	E	е	e ₁	HE	L min.	w
mm	1.6 1.4	0.48 0.35	0.53 0.40	1.8 1.4	0.44 0.37	4.6 4.4	2.6 2.4	3.0	1.5	4.25 3.75	0.8	0.13

OUTLINE	TLINE REFERENCES				EUROPEAN	ISSUE DATE
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT89						97-02-28

Product specification Philips Semiconductors

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DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

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