

6 – 18 GHz GaAs pHEMT MMIC Wideband LNA

Product Overview

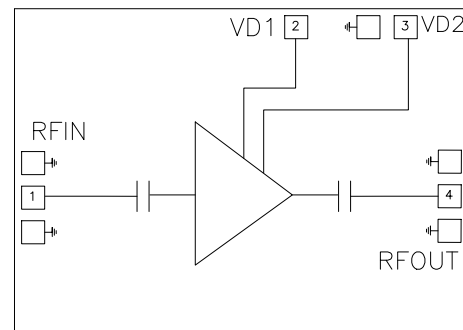
The MMA044AA is a gallium arsenide (GaAs) pseudomorphic high-electron mobility transistor (pHEMT) monolithic microwave integrated circuit (MMIC) low-noise wideband amplifier die that operates between 6 GHz and 18 GHz. The MMA044AA die provides 21 dB of small signal gain, 1.7 dB noise figure, and output IP3 of 30 dBm, while requiring only 102 mA from a 4 V supply. The P1dB output power of 16 dBm enables the LNA to function as an LO driver for balanced, in-phase quadrature (I/Q), or image reject mixers. The MMA044AA amplifier also features RF ports that are DC blocked and internally matched to 50Ω, which allows for easy integration into multi-chip modules (MCMs).

The following illustration shows the primary functional blocks of the MMA044AA device.

Key Features

- **Broadband performance: 6 to 18 GHz**
- **High Gain: 21 dB**
- **Low Noise figure: 1.7 dB**
- **High Output IP3: + 30 dBm**
- **Output P1dB power: + 16 dBm**
- **Maximum RF Input Power: + 24 dBm**
- **Single Positive Supply: + 4V @ 102 mA**
- **50Ω matched input/output**
- **Compact Die size: 1.12 mm X 1.35 mm X 0.1 mm**

Amplifier Functional Diagram



Applications

- Test & Measurement instrumentation
- Military and space
- Point-to-point and point-to-multi-point Radios
- VSAT

Performance Overview

Parameter	Typ.	Units
Frequency range	6 – 18	GHz
Gain	21	dB
Gain variation over Temp	0.02	dB/°C
NF	1.7	dB
Output IP3	+ 30	dBm
Output P1dB	+ 16	dBm

Export Classification: EAR99

Gain, NF & OIP3 Performances

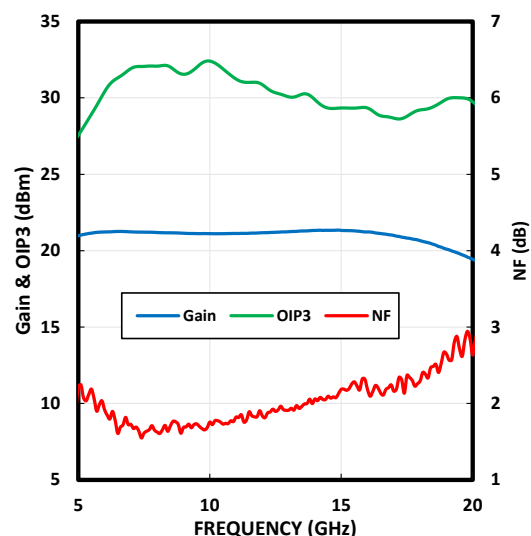


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1. Electrical Specifications

1.1 Typical Electrical Performance

Table 1-1. Typical Electrical Performance at 25 °C, V_{dd} = + 4V, I_{dd} = 102 mA (Unless otherwise mentioned)

Parameter	Min	Typ.	Max	Units
Frequency range	6		18	GHz
Gain	20.8	21	21.4	dB
Gain variation over Temperature		0.02		dB/°C
Noise Figure	1.5	1.7	2.2	dB
Output P1dB	+ 15	+ 16	+ 18	dBm
Output IP3	+ 28	+ 30	+ 33	dBc
Input Return Loss	11.5	14		dB
Output Return Loss	12	14		dB
Drain Supply Voltage (V _{D1} & V _{D2})		+ 4	+ 4.5	V
Drain Supply Current (I _{DD} = I _{D1} + I _{D2})		102		mA

1.2 Typical Performance Curves

The following graphs show the typical performance curves of the MMA0444A device at + 25 °C, + 4V and 102 mA unless otherwise indicated.

Figure 1-1. Gain vs. Temperature

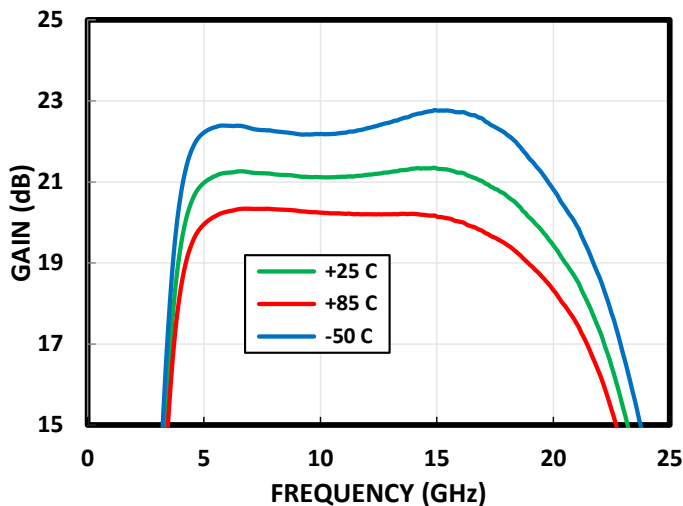


Figure 1-2. NF vs. Temperature

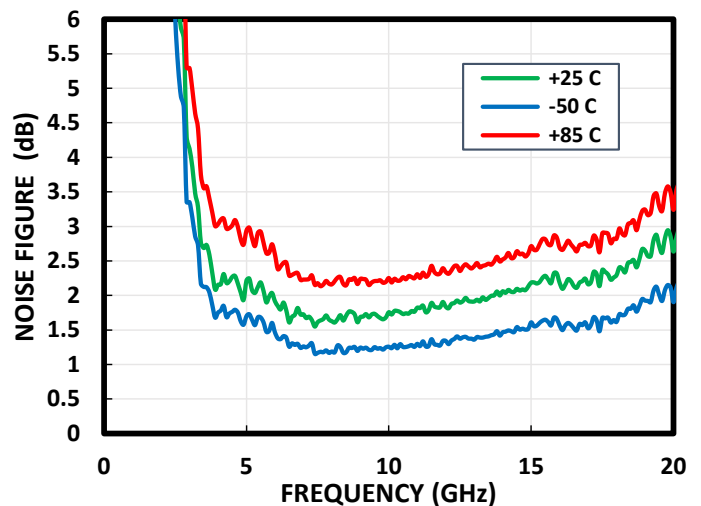


Figure 1-3. Input RL vs. Temperature

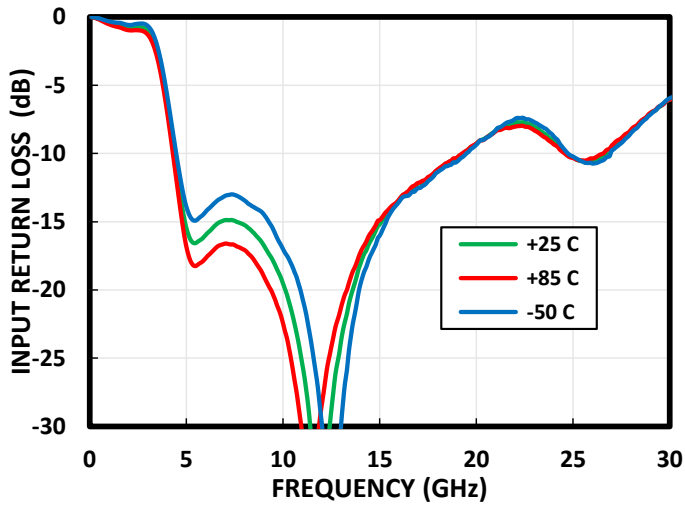


Figure 1-4. Output RL vs. Temperature

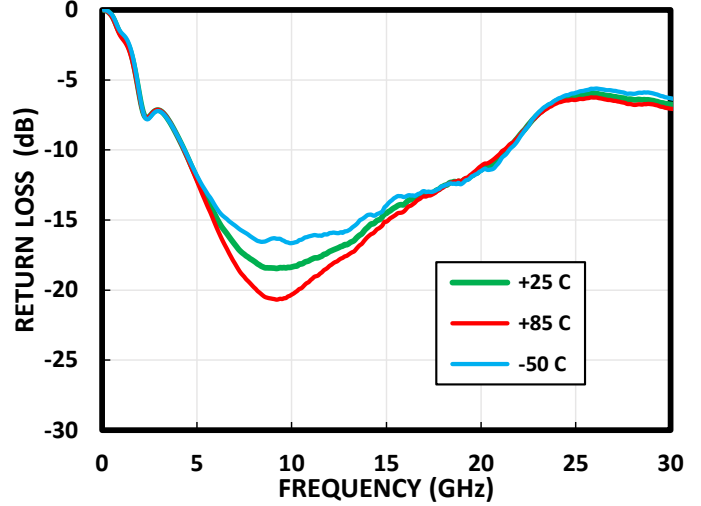


Figure 1-5. P1dB vs. Temperature

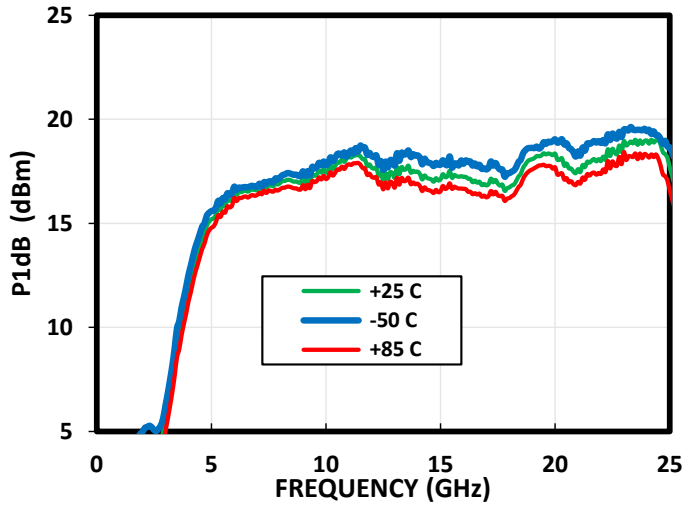
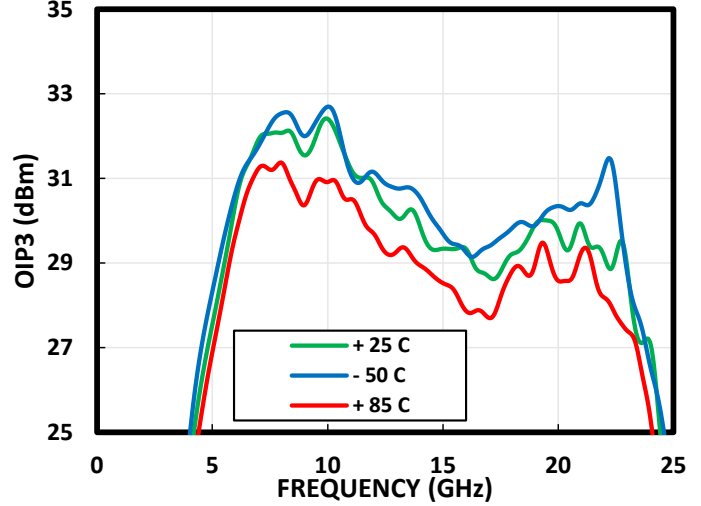


Figure 1-6. Output IP3 vs. Temperature



1.3 Absolute Maximum Ratings

The following table shows the absolute maximum ratings of the MMA044AA device at 25 °C, unless otherwise specified. Exceeding one or any of the maximum ratings potentially could cause damage or latent defects to the device.

Table 1-2. Absolute Maximum Ratings

Parameter	Rating
Drain bias voltage (V_{D1} and V_{D2})	+ 4.5V
RF input power (P_{IN})	+ 24 dBm
Channel Temperature (T_J)	+ 150 °C
Storage Temperature (T_S)	– 65 to + 150 °C
Operating Temperature (T_A)	– 55 to + 85 °C



ESD Sensitive Device

2. Die Specifications

The following illustration shows the chip outline of the MMA044AA device. Dimensions are shown in inches and millimeters. The minimum bond pad size is 100 μm \times 100 μm . Both the bond pad surface and the backside metal are 3 μm gold. The die thickness is 100 μm . The backside is the DC/RF ground. The airbridge keepout region is in crosshatch, and the unlabeled pads should not be bonded.

For additional packaging information, contact your Microchip sales representative.

Figure 2-1. Die Outline Drawing

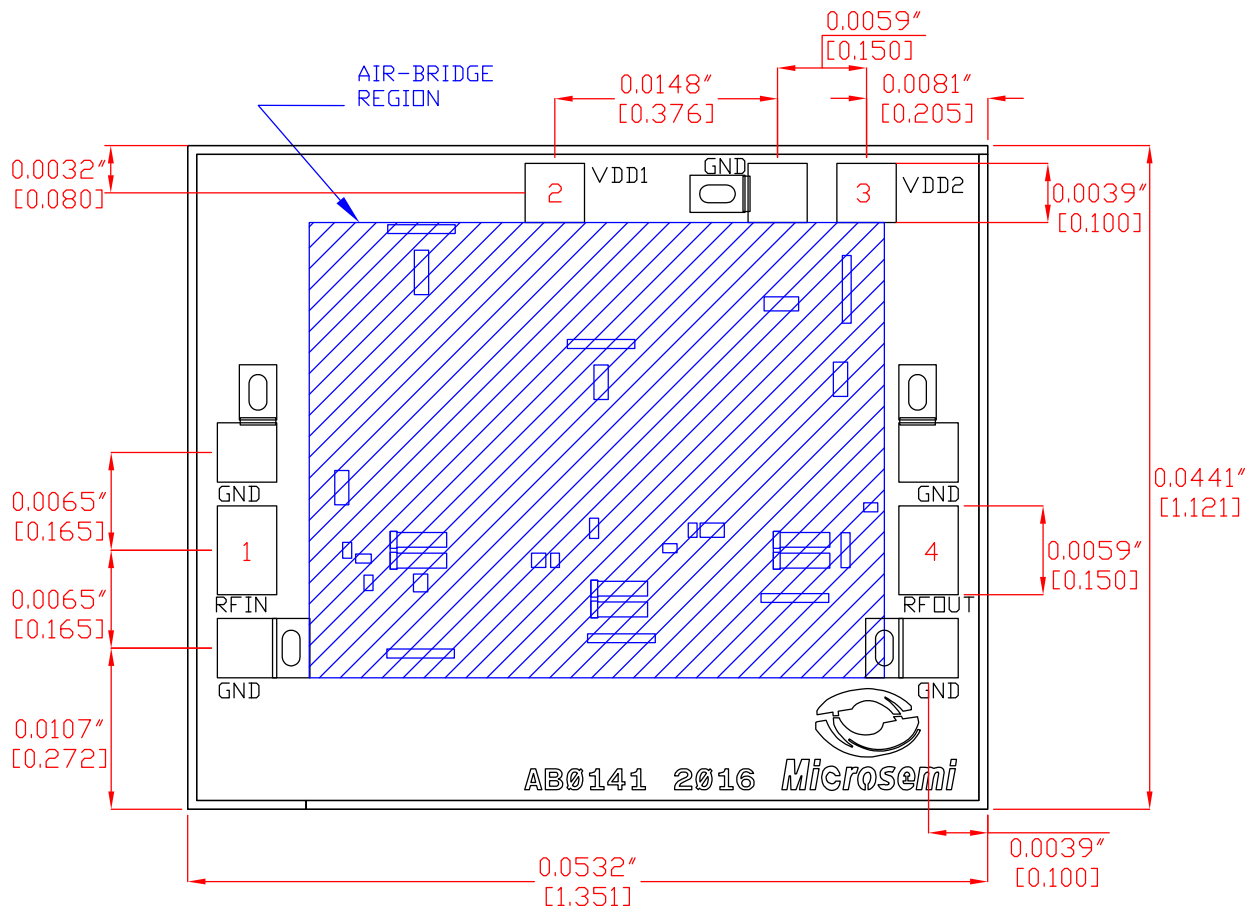


Table 2-1. I/O Pad Description

Pad Number	Pad Name	Pad Description
1	RFIN	AC coupled and matched to 50 Ω .
4	RFOUT	AC coupled and matched to 50 Ω
2, 3	VDD1, VDD2	Drain Supply Voltage for the Amplifier. See Assembly Diagram for required external components.
Backside Paddle	RF/DC GND	Must be connected to RF/DC Ground

3. Application Circuits

The following illustration shows the assembly diagram of the MMA044AA device. The carrier plate is gold plated. It is necessary to attach components using thermally conductive epoxy. The bypass chip caps are ceramic and must be assembled within 10 mils of the die. Use 1 mil Au bond wires

Figure 3-1. Assembly Diagram

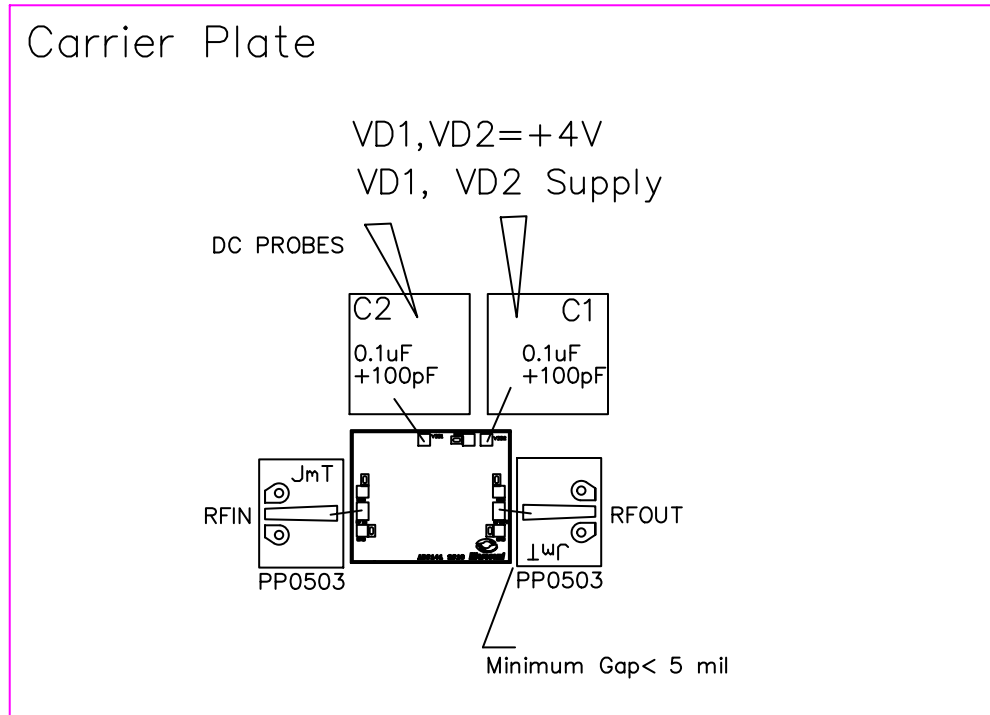


Table 3-1. Bill of material (BOM)

Component	Value	Part Number	Description
C1 & C2	0.1uF +/- 100 pF	MVB4040X104MEK5C1B	Presidio VB series dual caps. 40 mils X 40mils X17 mils

4. Ordering, Shipping and Handling

4.1 Handling Recommendations

Gallium arsenide integrated circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. It is recommended to follow all procedures and guidelines outlined in the Microsemi application note AN01: GaAs MMIC Handling and Die Attach Recommendations.

4.2 Ordering Information

For additional ordering information, contact your Microchip sales representative.

Part Number	Package
MMA044AA	Die

4.3 Packing Information

Standard Format
Gel Pack

Note: Contact your Microchip sales representative for the minimum quantity order

5. Revision History

Table 5-1. Revision History

Revision	Date	Description
A	08/2021	Document created.

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