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## SDR950M & Z thru SDR952M & Z

50 A, 35 nsec typ., 100 - 200 V Hyperfast Rectifier

### **Designer's Data Sheet**

Part Number/Ordering Information <sup>1</sup>/

SDR950

<sup>T</sup> Screening <sup>⊉</sup>

\_\_ = Not Screened TX = TX Level TXV = TXV Level S = S Level

Leg Bend Option (See Figure 1)

**Package** M = TO-254, Z = TO-254Z

### Features:

- Hyperfast Recovery: 50 nsec Maximum <sup>3/</sup>
- High Surge Rating
- Low Reverse Leakage Current
- Low Junction Capacitance
- Hermetically Sealed Package
- Gold Eutectic Die Attach
- Ultrasonic Aluminum Wire Bonds
- Higher Voltages and Faster Recovery Times Available, Contact Factory
- Ceramic Seal for Improved Hermeticity Available
- TX, TXV, and S-Level Screening Available <sup>2/</sup>

Maximum Ratings 4/		Symbol	Value	Units
Peak Repetitive Reverse Voltage	SDR950M & Z SDR951M & Z SDR952M & Z	$egin{array}{c} oldsymbol{V_{RWM}} \ oldsymbol{V_{R}} \end{array}$	100 150 200	Volts
Average Rectified Forward Current (Resistive Load, 60 Hz Sine Wave, T <sub>A</sub> = 25°C) <sup>5</sup> /		lo	50	Amps
Peak Surge Current (8.3 ms Pulse, Half Sine Wave, or equivalent DC)		I <sub>FSM</sub>	350	Amps
Operating & Storage Temperature		T <sub>OP</sub> & T <sub>STG</sub>	-65 to +200	°C
Maximum Total Thermal Resistance Junction to Case		R <sub>eJC</sub>	0.85	°C/W

#### Notes:

1/ For ordering information, price, operating curves, and availability- Contact factory.

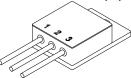
2/ Screening based on MIL-PRF-19500. Screening flows available on request.

3/ Recovery conditions:  $I_F = 10$  Amp, di/dt = 200A/ $\mu$ s

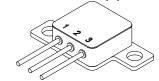
4/ Pins 2 and 3 tied together.

 $5/T_{C} = 150^{\circ}C$ , derate to 0 A @ 200°C.

TO-254 (M)



TO-254Z (Z)



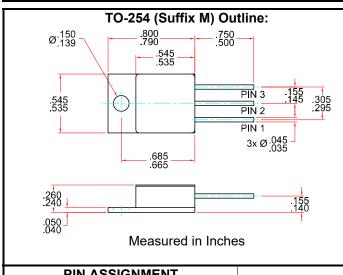


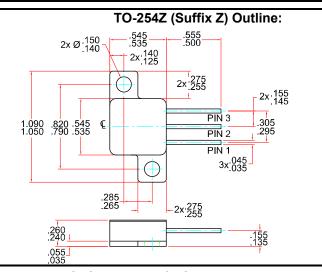
Solid State Devices, Inc.

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# SDR950M & Z thru SDR952M & Z

<b>Electrical Characteristics</b>		Symbol	Max	Units
Instantaneous Forward Voltage Drop (I <sub>F</sub> = 25 A, 300 - 500 µsec Pulse) (I <sub>F</sub> = 50 A, 300 - 500 µsec Pulse)	T <sub>A</sub> = 25°C T <sub>A</sub> = 25°C	$V_{F1} \ V_{F2}$	1.00 1.25	V <sub>DC</sub>
Instantaneous Forward Voltage Drop (I <sub>F</sub> = 50 Adc, 300 - 500 µsec Pulse)	T <sub>A</sub> = -55°C	$V_{F3}$	1.35	$V_{DC}$
Reverse Leakage Current (300 µsec Pulse Minimum)	$T_A$ = 25°C, Rated $V_R$ $T_C$ = 100°C, 80% of Rated $V_R$	I <sub>R1</sub> I <sub>R2</sub>	100 10	μA mA
Reverse Recovery Time (I <sub>F</sub> =10 Amp, dI <sub>F</sub> /dt = 200 A/µs)		t <sub>RR</sub>	55	nsec
Junction Capacitance $(V_R = 10 V_{DC}, T_A = 25^{\circ}C, f = 1MHz)$		CJ	900	pF





PIN ASSIGNIVIEN I						
PIN 1	PIN 2	PIN 3				
Cathode	Anode	Anode				
Figure 1- Optional Lead Bends						

