

## SR140L THRU SR1200L

1.0 AMP. LOW VF Schottky Barrier Rectifiers

#### **Features**

•Plastic package has Underwriters Laboratory Flammability Classification 94V-0 utilizing Flame Retardant Epoxy Molding Compound.

- Guard ring for overvoltage protection
- High current capability, low forward voltage drop
- · Low power loss, high efficiency
- · High surge capability

### **Mechanical Data**

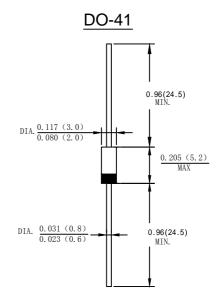
· Case: Molded plastic DO-41

 Terminals: Plated leads solderable per MIL-STD-202, Method 208 guaranteed

· Polarity: Color band dentes cathode end

Mounting Position: AnyMaking: Type Number

· Lead Free: For RoHS/Lead Free Version



Dimensions in inches and (millimeters)

### **Maximum Ratings and Electrical Characteristics**

Rating at 25°C ambient temperature unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	SR 140L	SR 145L	SR 150L	SR 160L	SR 180L	SR 1100L	SR 1150L	SR 1200L	Unit
Maximum Recurrent Peak Reverse Voltage	$V_{RRM}$	40	45	50	60	80	100	150	200	V
Maximum RMS Voltage	VRMS	28	31.5	35	42	56	70	105	140	V
Maximum DC Blocking Voltage	V <sub>DC</sub>	40	45	50	60	80	100	150	200	V
Average Rectified Output Current (Note 1)  @T <sub>L</sub> =10℃	IF(AV)	1.0								А
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	Ігѕм	40								Α
I <sup>2</sup> t Rating for Fusing (t < 8.3ms)	l²t	6.64								A <sup>2</sup> s
Forward Voltage @IF=1.0A	V <sub>FM</sub>	0.45 0.5				0.75		0.85	V	
Peak Reverse Current @T <sub>A</sub> =25°C	1-	0.1 0.05							mA	
At Rated DC Blocking Voltage @T <sub>A</sub> =100°C	· IR	10.0					5.0			
Typical Junction Capacitance (Note 2)	CJ	110								pF
Typical Thermal Resistance Junction to Ambient(Note 1)	RөJA	25								°C/W
Operating Temperature Range	Тл	-55 to + 150								$^{\circ}$
Storage Temperature Range	Тѕтс	-55 to + 150								$^{\circ}$

Note: 1. Leads maintained at ambient temperature at a distance of 9.5mm from the case

2. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C

version:02 1of3 www.dyelec.com



PEAK FORWARD SURGE CURRENT,(A)

# SR140L THRU SR1200L

FIG. 1 - FORWARD CURRENT DERATING CURVE

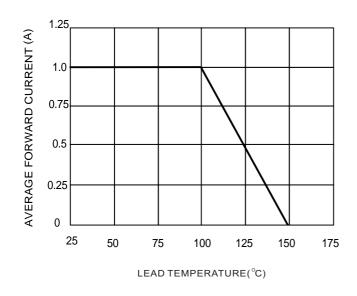


FIG.2-TYPICAL FORWARD CHARACTERISTICS

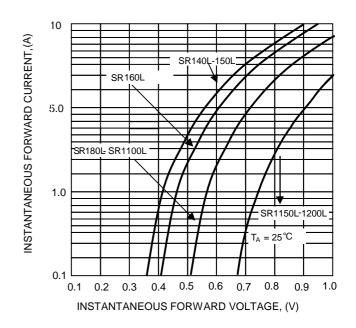


FIG. 3 MAXIMUM NON-REPETITIVE SURGE CURRENT

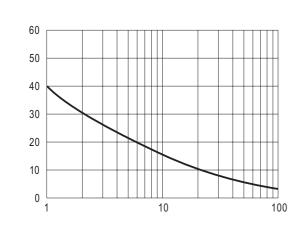
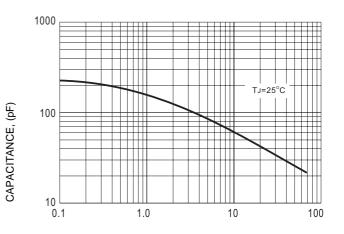


FIG.4 TYPICAL JUNCTION CAPACITANCE



NUMBER OF CYCLES AT 60Hz

REVERSE VOLTAGE (V)



## SR140L THRU SR1200L

### **Important Notice and Disclaimer**

- Reproducing and modifying information of the document is prohibited without permission from DIYI.
- DIYI reserves the right to make changes to this document and its products and specifications at any time without notice. Customers should obtain and confirm the latest product information and specifications before final design, purchase or use.
- DIYI disclaims any and all liability arising out of the application or use of any product including damages incidentally and consequentially occurred.
- DIYI does not assume any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.
- Applications shown on the here in document are examples of standard use and operation. Customers are responsible in comprehending the suitable use in particular applications.
  - DIYI makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.
- The products shown here in are not designed and authorized for equipments requiring high level of reliability or relating to human life and for any applications concerning life-saving or life-sustaining, such as medical instruments, transportation equipment, aerospace machinery et cetera. Customers using or selling these products for use in such applications do so at their own ris k andagree to fully indemnify DIYI for any damages resulting from such improper use or sale.
- Since DIYI uses lot number as the tracking base, please provide the lot number for tracking when complaining.