RT3NNNM

Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

DESCRIPTION

RT3NNNM is composite transistor built with two RT1N44H chips in SC-88 package.

FEATURE

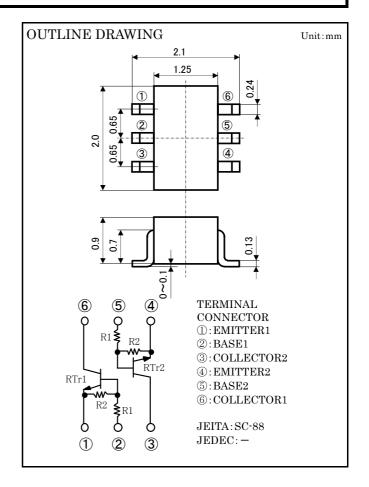
Silicon epitaxial type

Each transistor elements are independent.

Mini package for easy mounting

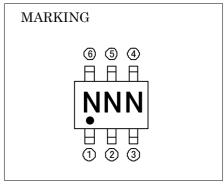
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	10	V
V_{CEO}	Collector to Emitter voltage	50	V
V_{IN}	Input voltage	40	V
Ic	Collector current	100	mA
Icm	Peak Collector current	200	mA
PT	Total dissipation	200	mW
Tj	Junction temperature	+150	°C
$T_{ m stg}$	Storage temperature	-55~+150	°C



$ELECTRICAL\ CHARACTERISTICS (Ta=25^{\circ}C) (RTr1,\ RTr2\ COMMON)$

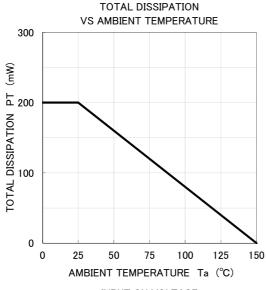
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter break down voltage	I c=100μA, R _{BE} =∞	50	_	_	V
ICBO	Collector cut off current	V _{CB} =50V, I _E =0	_	_	0.1	μA
IEBO	Emitter cut off current	V_{EB} =5V, I $_{C}$ =0	54	72	105	μA
$_{ m hFE}$	DC forward current gain	V_{CE} =5 V , I_{C} =5 mA	56	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	I $_{\rm C}$ =10mA, I $_{\rm B}$ =0.5mA	_	_	0.3	V
$V_{\rm I(ON)}$	Input on voltage	V_{CE} =0.2 V , I_{C} =5 mA	_	2.6	6.3	V
$V_{\rm I(OFF)}$	Input off voltage	V_{CE} =5 V , I_{C} =100 μA	1.3	1.7	_	V
R_1	Input resistor	_	33	47	61	kΩ
R_2/R_1	Resistor ratio	_	0.37	0.47	0.57	_
f_{T}	Gain band width product	V _{CE} =6V, I _E =-10mA	_	200	_	MHz

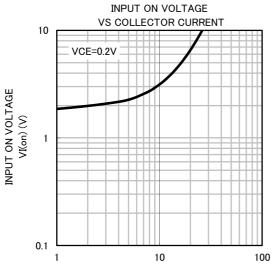
RT3NNNM

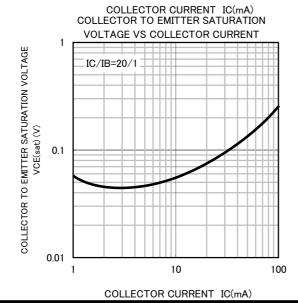
Composite Transistor With Resistor For Switching Application Silicon Epitaxial Type

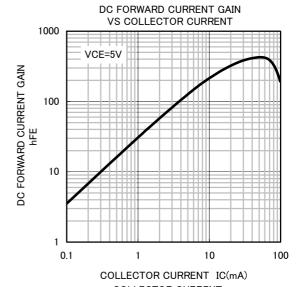
TYPICAL CHARACTERISTICS

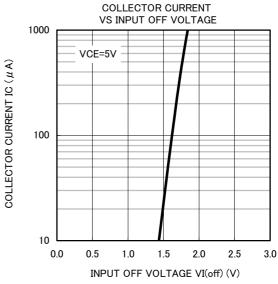
(Ta=25°C)(RTr1,RTr2 COMMON)













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