

**SiC Hybrid Modules** 

Power Module (V-series IGBT & SiC SBD Hybrid type) 1200V / 200A / 2-in-1 package

#### **■** Features

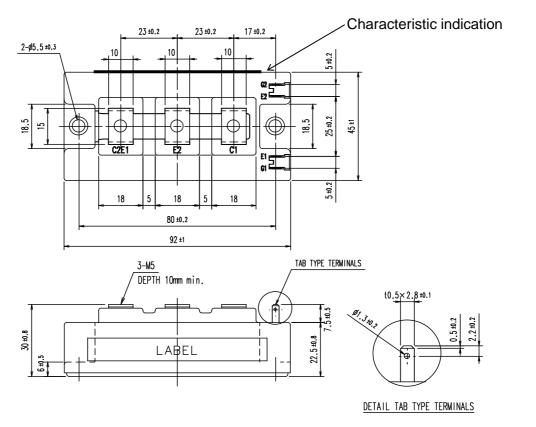
High speed switching Voltage drive Low switching loss Low inductance module structure

#### ■ Applications

Inverter for Motor Drive AC and DC Servo Drive Amplifier Uninterruptible Power Supply Active Front End

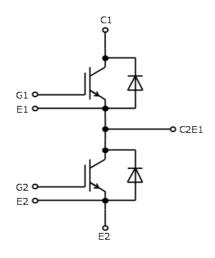
■ Outline drawing ( Unit : mm )





Weight: 270g (typ.)

### **■** Equivalent Circuit



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### ■ Absolute Maximum Ratings (at T<sub>C</sub>= 25°C unless otherwise specified)

Items	Symbols	Conditions	Maximum Ratings	Units
Collector-Emitter voltage	V <sub>CES</sub>		1200	V
Gate-Emitter voltage	$V_{GES}$		±20	V
Collector current	I <sub>C</sub>	Continuous $T_c = 100$ °C	200	
Collector current	I <sub>C</sub> pulse	1 msec	400	A
Forward current	I <sub>F</sub>	Continuous	200	] ^
Forward current	I <sub>F</sub> pulse	1 msec	400	
Collector power dissipation	Pc	1 device	1500	W
Junction temperature	T <sub>vj</sub>		175	
Operating junction temperature	$T_{\text{vjop}}$		150	
(under switching conditions)	, vjob		150	°C
Case temperature	T <sub>C</sub>		125	
Storage temperature	$T_{\rm stg}$		-40 ~ 125	
Isolation   between terminal and copper base		AC: 1 min.	2500	Vrms
voltage (*1)	V iso	AC. I IIIII.	2500	VIIIIS
Screw Mounting (*2)	-		3.5	N m
Torque Terminals (*2)	-		3.5	INIII

<sup>(\*1)</sup> All terminals should be connected together during the test.

<sup>(\*2)</sup> Recommendable Value: 2.5 - 3.5 Nm (M5)

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#### ■ Electrical characteristics (at $T_{vj}$ = 25°C unless otherwise specified)

#### NOTICE:

The external gate resistance ( $R_{\rm G}$ ) shown in below is one of our recommend value for the purpose of minimum switching loss. However the optimum  $R_{\rm G}$  depends on circuit configuration and/or environment. We recommend that the  $R_{\rm G}$  has to be carefully chosen based on consideration if IGBT module matches design criteria, for example, switching loss, EMC/EMI, spike voltage, surge current and no unexpected oscillation and so on. Especially, we recommend to choose  $R_{\rm G}$  value shown in below or more.

ltomo	Symbole	Condition		Ch	aracterist	lcs	Units
Items	Symbols	Condition	is	min.	typ.	max.	Units
Zero gate voltage Collector current	I <sub>CES</sub>	$V_{\rm GE} = 0 \text{ V}, \ V_{\rm CE} = 1200$	) V	-	-	2.5	mA
Gate-Emitter leakage current	I <sub>GES</sub>	$V_{CE} = 0 \text{ V}, \ V_{GE} = \pm 20$	V	ı	ı	400	nA
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{\rm CE} = 20 \text{ V}, I_{\rm C} = 200 \text{ r}$		6.0	6.5	7.0	V
	$V_{CE(sat)}$	$V_{GE} = 15 \text{ V},$	<i>T</i> <sub>∨j</sub> =25°C	-	1.95	2.40	
		$I_{\rm C} = 200 \text{ A}$	T <sub>vj</sub> =125°C	-	2.25	-	
Collector-Emitter	(terminal)	1°C = 200 A	T <sub>vj</sub> =150°C	•	2.30	-	V
saturation voltage	V	$V_{GE} = 15 \text{ V},$	T <sub>vj</sub> =25°C	-	1.75	2.20	
	V <sub>CE(sat)</sub>	$I_{\rm C} = 200  \text{A}$	<i>T</i> <sub>vj</sub> =125°C	-	2.05	-	
	(chip)	1°C - 200 A	<i>T</i> <sub>vj</sub> =150°C	-	2.10	-	
Internal gate resistance	$r_{\rm g(int)}$	-		-	3.75	-	Ω
Input capacitance	$C_{ies}$	$V_{CE} = 10 \text{ V}, V_{GE} = 0 \text{ V},$	f=100 kHz	-	17.0	-	nF
	$t_{on}$			-	690	-	
Turn-on time	$t_{r}$	$V_{\rm CC} = 600 \text{ V}, I_{\rm C} = 200 \text{ A},$ - 230		-			
	$t_{r(i)}$	$V_{GE} = \pm 15 \text{ V}, R_G = 2.7$	$V, R_{\rm G} = 2.7\Omega,$ - 50		-	nsec	
Turn-off time	$t_{ m off}$	$T_{vi} = 150^{\circ}\text{C}, L_s = 70\text{nl}$	Н	-	720	-	
rum-on ume	$t_{\mathrm{f}}$	] '		-	90	-	1
	W		T <sub>vj</sub> =25°C	-	1.75	2.10	
	V <sub>F</sub>	$V_{GE} = 0 \text{ V}, I_F = 200 \text{ A}$	T <sub>vj</sub> =125°C	-	2.25	-	
Forward on voltage	(terminal)		T <sub>vj</sub> =150°C	-	2.45	-	$\rfloor$ $_{ m V}$ $\mid$
Forward on voltage	V		T <sub>vj</sub> =25°C	-	1.60	1.90	7 V
	V <sub>F</sub>	$V_{GE} = 0 \text{ V}, I_F = 200 \text{ A}$	T <sub>vj</sub> =125°C	-	2.05	-	]
	(chip)		T <sub>vj</sub> =150°C	-	2.25	-	1
Reverse recovery time	$t_{\rm rr}$	I <sub>F</sub> = 200A		-	40	-	nsec

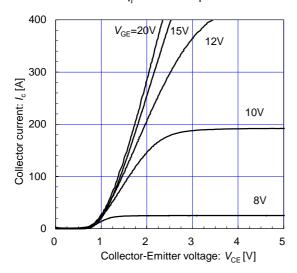
#### 5. Thermal resistance characteristics

Items	Symbole	Conditions Characteristic			ics	Linita
	Symbols	Conditions	min.	typ.	max.	Units
Thermal resistance(1device)	D	IGBT	-	-	0.100	
	$R_{ m th(j-c)}$	FWD (SiC-SBD)	-	-	0.120	°C/W
Contact thermal resistance (1device) (*1)	$R_{\text{th(c-f)}}$	with Thermal Compound	-	0.025	-	C/VV

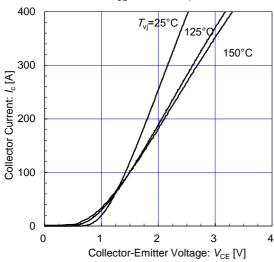
<sup>(\*1)</sup> This is the value which is defined mounting on the additional cooling fin with thermal compound.

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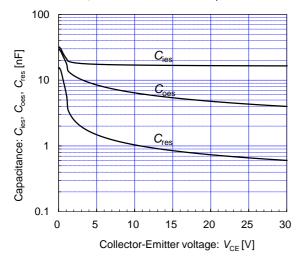
Collector current vs. Collector-Emitter voltage  $T_{vi} = 25^{\circ}\text{C} / \text{chip}$ 



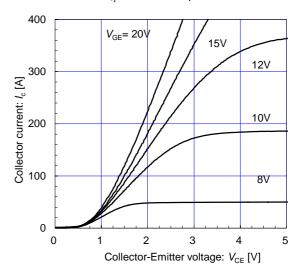
Collector current vs. Collector-Emitter voltage  $V_{\rm GE} = 15 \rm V \ / \ chip$ 



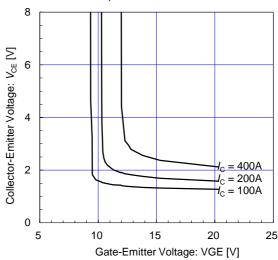
Gate Capacitance vs. Collector-Emitter Voltage  $V_{GE} = 0 \text{ V}, f = 100 \text{kHz}, T_{vi} = 25^{\circ}\text{C}$ 



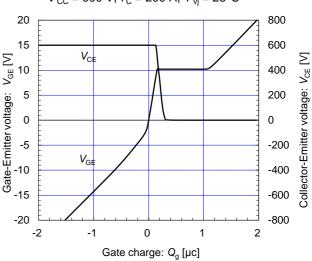
Collector current vs. Collector-Emitter voltage (typ.)  $T_{vi} = 150$ °C / chip



Collector-Emitter voltage vs. Gate-Emitter voltage  $T_{vj}$ = 25°C / chip



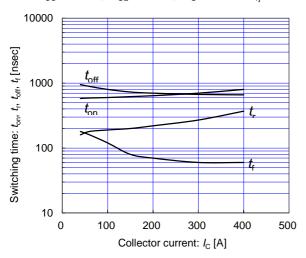
Dynamic Gate Charge (typ.)  $V_{CC} = 600 \text{ V}, I_{C} = 200 \text{ A}, T_{vi} = 25^{\circ}\text{C}$ 



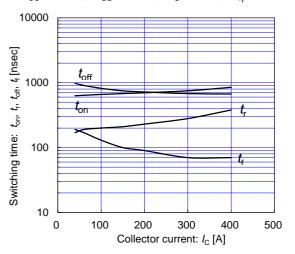


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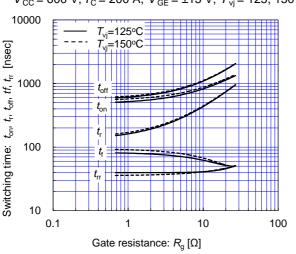
Switching time vs. Collector current (typ.)  $V_{CC} = 600 \text{ V}, V_{GE} = \pm 15 \text{ V}, R_G = 2.7 \Omega, T_{vj} = 125 ^{\circ}\text{C}$ 



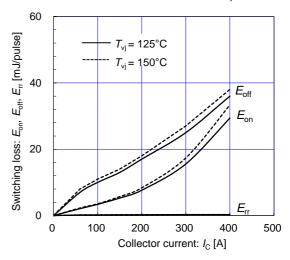
Switching time vs. Collector current (typ.)  $V_{\rm CC} = 600 \text{ V}, V_{\rm GE} = \pm 15 \text{ V}, R_{\rm G} = 2.7 \Omega, T_{\rm Vi} = 150 ^{\circ} \text{C}$ 



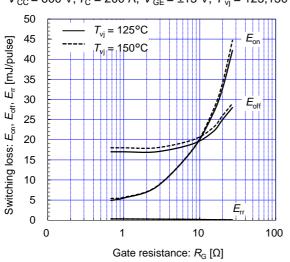
Switching time vs. Gate resistance (typ.)  $V_{\rm CC}$  = 600 V,  $I_{\rm C}$  = 200 A,  $V_{\rm GE}$  = ±15 V,  $T_{\rm vj}$  = 125, 150°C



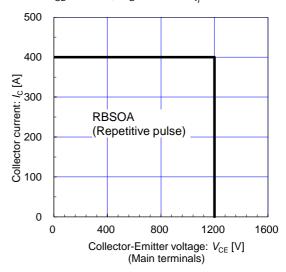
Switching loss vs. Collector current (typ.)  $V_{\rm CC}$  = 600 V,  $V_{\rm GE}$  = ±15 V,  $R_{\rm G}$  = 2.7  $\Omega$ ,  $T_{\rm vj}$ =125, 150°C



Switching loss vs. Gate resistance (typ.)  $V_{CC} = 600 \text{ V}, I_C = 200 \text{ A}, V_{GE} = \pm 15 \text{ V}, T_{vi} = 125,150^{\circ}\text{C}$ 



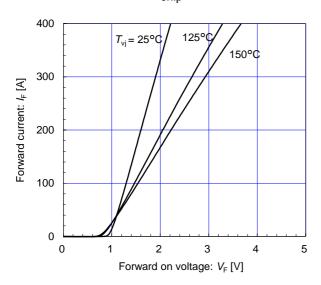
Reverse bias safe operating area (max.)  $V_{GF} = \pm 15 \text{ V}, R_G = 2.7 \Omega, T_{vi} = 150^{\circ}\text{C}$ 



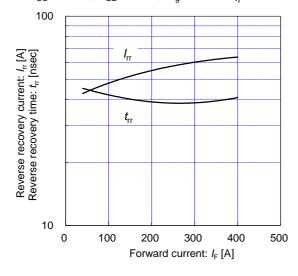


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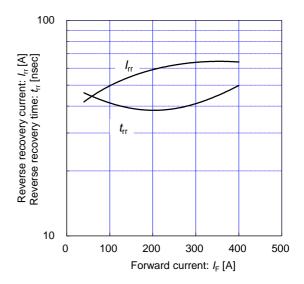
Forward Current vs. Forward Voltage (typ.) chip



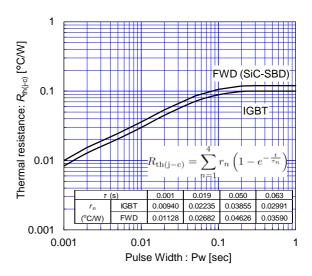
Reverse Recovery Characteristics (typ.)  $V_{CC} = 600V$ ,  $V_{GE} = \pm 15V$ ,  $R_{g} = 2.7\Omega$ ,  $T_{vi} = 150^{\circ}C$ 



Reverse Recovery Characteristics (typ.)  $V_{CC} = 600V$ ,  $V_{GE} = \pm 15V$ ,  $R_G = 2.7\Omega$ ,  $T_{vi} = 125^{\circ}C$ 



Transient Thermal Resistance (max.)





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