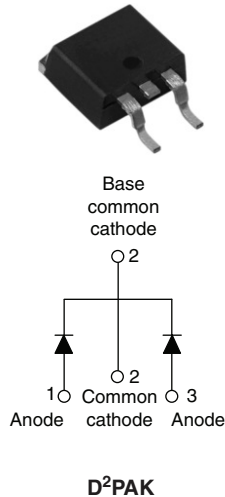


HEXFRED® Ultrafast Soft Recovery Diode, 2 x 8 A


FEATURES

- Ultrafast recovery
- Ultrasoft recovery
- Very low I_{RRM}
- Very low Q_{rr}
- Specified at operating conditions
- Designed and qualified for industrial level

BENEFITS

- Reduced RFI and EMI
- Reduced power loss in diode and switching transistor
- Higher frequency operation
- Reduced snubbing
- Reduced parts count

DESCRIPTION

HFA16TA60CS is a state of the art center tap ultrafast recovery diode. Employing the latest in epitaxial construction and advanced processing techniques it features a superb combination of characteristics which result in performance which is unsurpassed by any rectifier previously available. With basic ratings of 600 V and 8 A per leg continuous current, the HFA16TA60CS is especially well suited for use as the companion diode for IGBTs and MOSFETs. In addition to ultrafast recovery time, the HEXFRED® product line features extremely low values of peak recovery current (I_{RRM}) and does not exhibit any tendency to “snap-off” during the t_b portion of recovery. The HEXFRED features combine to offer designers a rectifier with lower noise and significantly lower switching losses in both the diode and the switching transistor. These HEXFRED advantages can help to significantly reduce snubbing, component count and heatsink sizes. The HEXFRED HFA16TA60CS is ideally suited for applications in power supplies and power conversion systems (such as inverters), motor drives, and many other similar applications where high speed, high efficiency is needed.

PRODUCT SUMMARY

V_R	600 V
V_F at 8 A at 25 °C	1.7 V
$I_{F(AV)}$	2 x 8 A
t_{rr} (typical)	18 ns
T_J (maximum)	150 °C
Q_{rr}	65 nC
$dl_{(rec)M}/dt$	240 A/ μ s

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Cathode to anode voltage	V_R		600	V
Maximum continuous forward current	I_F	$T_C = 100\text{ °C}$	8	A
			per leg	
per device				
Single pulse forward current	I_{FSM}		60	
Maximum repetitive forward current	I_{FRM}		24	
Maximum power dissipation	P_D	$T_C = 25\text{ °C}$	36	W
		$T_C = 100\text{ °C}$	14	
Operating junction and storage temperature range	T_J, T_{Stg}		- 55 to + 150	°C

ELECTRICAL SPECIFICATIONS PER LEG (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA	600	-	-	V
Maximum forward voltage	V _{FM}	I _F = 8.0 A	-	1.4	1.7	
		I _F = 16 A	-	1.7	2.1	
		I _F = 8.0 A, T _J = 125 °C	-	1.4	1.7	
Maximum reverse leakage current	I _{RM}	V _R = V _R rated	-	0.3	5.0	μA
		T _J = 125 °C, V _R = 0.8 x V _R rated	-	100	500	
Junction capacitance	C _T	V _R = 200 V	-	10	25	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH

DYNAMIC RECOVERY CHARACTERISTICS PER LEG (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Reverse recovery time See fig. 5, 6 and 16	t _{rr}	I _F = 1.0 A, dI _F /dt = 200 A/μs, V _R = 30 V	-	18	-	ns
	t _{rr1}	T _J = 25 °C	-	37	55	
	t _{rr2}	T _J = 125 °C	-	55	90	
Peak recovery current See fig. 7 and 8	I _{RRM1}	T _J = 25 °C	-	3.5	5.0	A
	I _{RRM2}	T _J = 125 °C	-	4.5	8.0	
Reverse recovery charge See fig. 9 and 10	Q _{rr1}	T _J = 25 °C	-	65	138	nC
	Q _{rr2}	T _J = 125 °C	-	124	360	
Peak rate of fall of recovery current during t _b See fig. 11 & 12	dI _{(rec)M} /dt1	T _J = 25 °C	-	240	-	A/μs
	dI _{(rec)M} /dt2	T _J = 125 °C	-	210	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Lead temperature	T _{lead}	0.063" from case (1.6 mm) for 10 s	-	-	300	°C
Junction to case, single leg conducting	R _{thJC}		-	-	3.5	K/W
Junction to case, both legs conducting			-	-	1.75	
Thermal resistance, junction to ambient	R _{thJA}	Typical socket mount	-	-	80	
Weight			-	2	-	g
			-	0.07	-	oz.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style D ² PAK	HFA16TA60CS			



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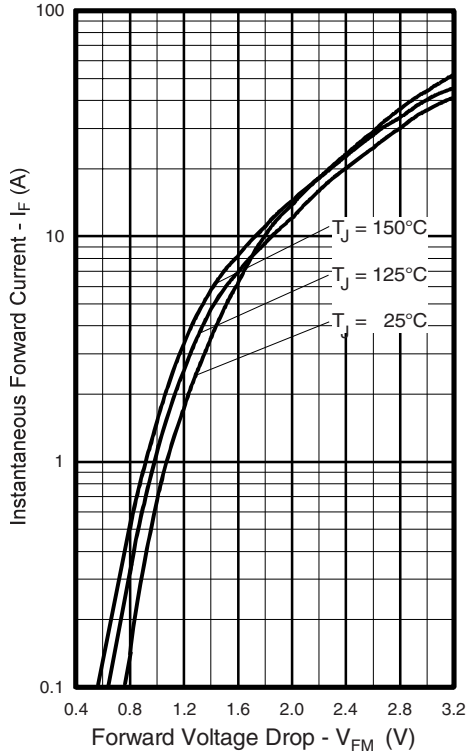


Fig. 1 - Maximum Forward Voltage Drop vs. Instantaneous Forward Current (Per Leg)

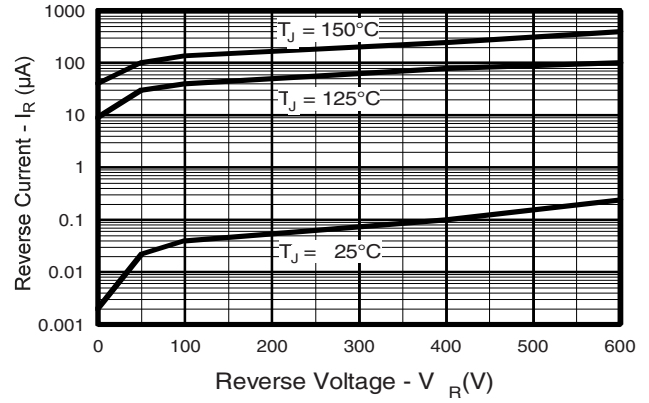


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

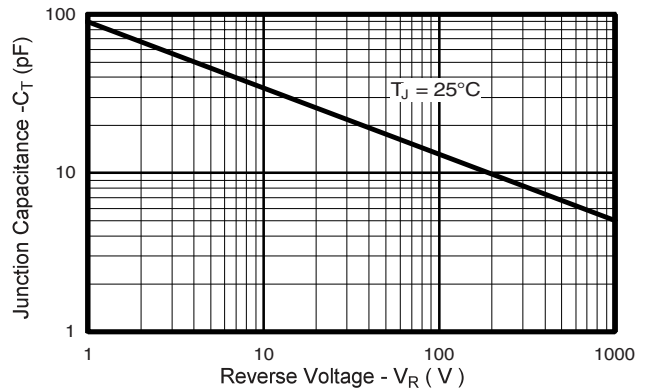


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

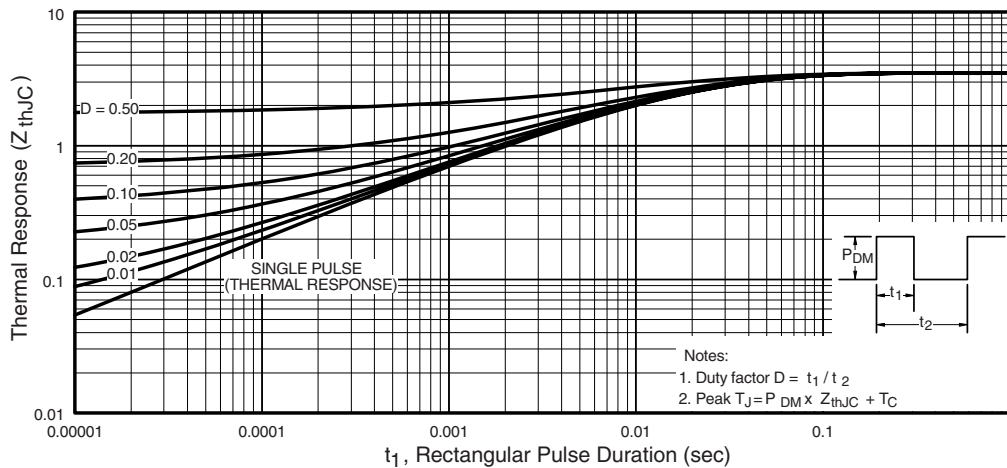


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

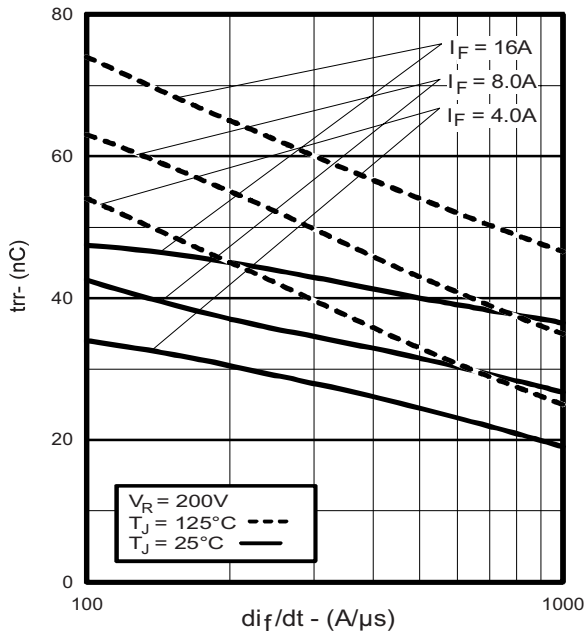


Fig. 5 - Typical Reverse Recovery Time vs. di_F/dt (Per Leg)

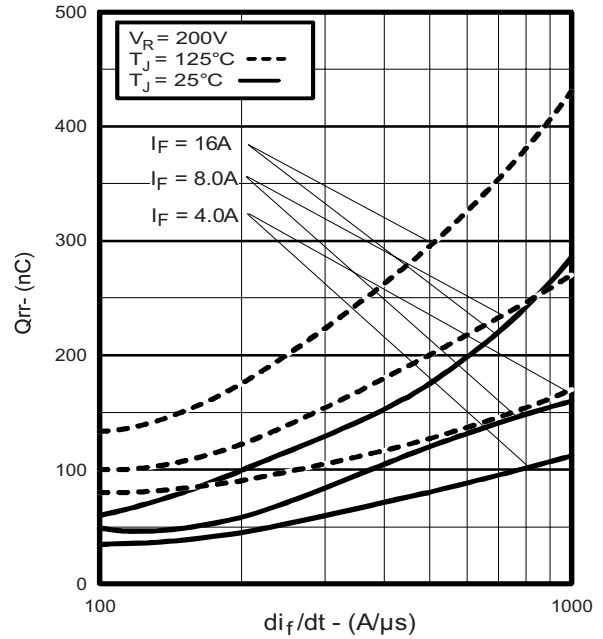


Fig. 7 - Typical Stored Charge vs. di_F/dt (Per Leg)

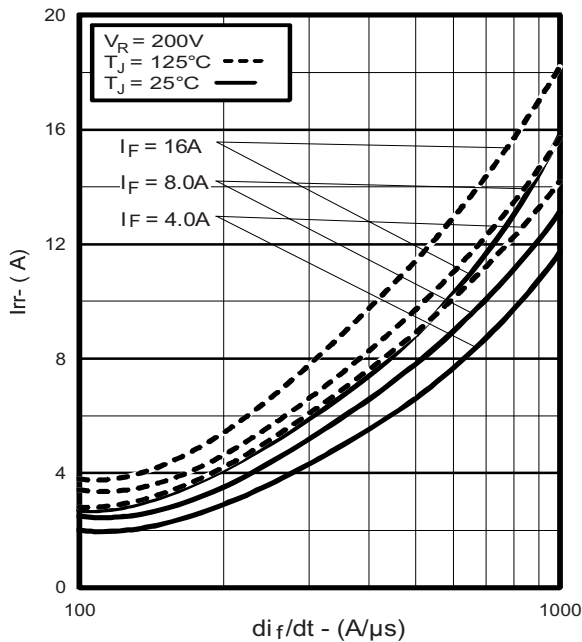


Fig. 6 - Typical Recovery Current vs. di_F/dt (Per Leg)

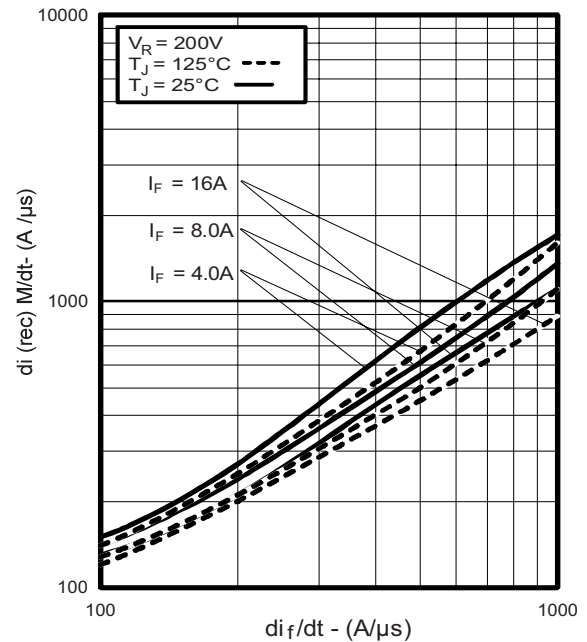


Fig. 8 - Typical $di_{(rec)M}/dt$ vs. di_F/dt (Per Leg)

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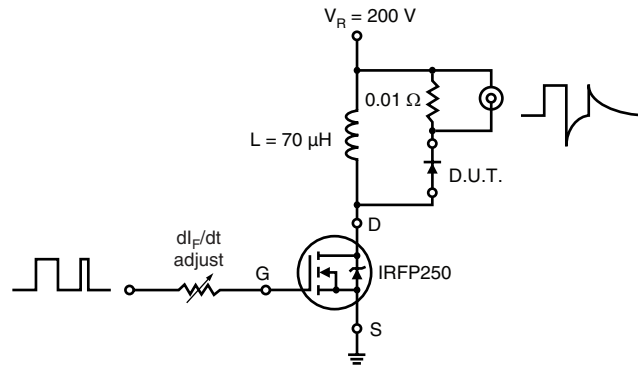
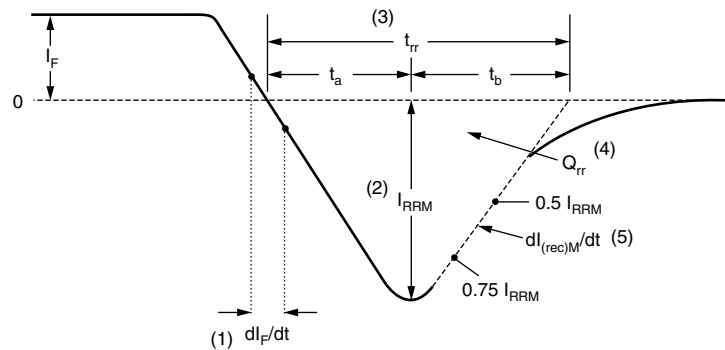


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) di_F/dt - rate of change of current through zero crossing
- (2) I_{RRM} - peak reverse recovery current
- (3) t_{rr} - reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through $0.75 I_{RRM}$ and $0.50 I_{RRM}$ extrapolated to zero current.
- (4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}
- (5) $di_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

Fig. 10 - Reverse Recovery Waveform and Definitions

LINKS TO RELATED DOCUMENTS	
Dimensions	http://www.vishay.com/doc?95046
Part marking information	http://www.vishay.com/doc?95054
Packaging information	http://www.vishay.com/doc?95032



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