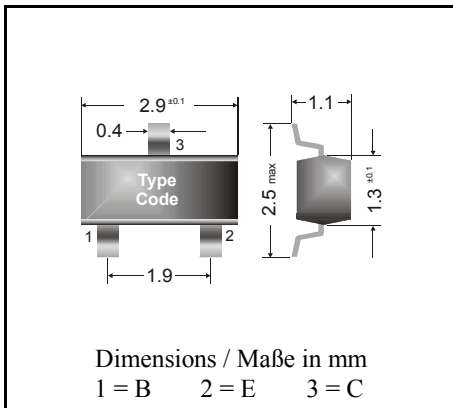


NPN

Surface mount Si-Epitaxial Planar Transistors
Si-Epitaxial Planar Transistoren für die Oberflächenmontage

NPN



Power dissipation – Verlustleistung 250 mW

Plastic case SOT-23
Kunststoffgehäuse (TO-236)

Weight approx. – Gewicht ca. 0.01 g

Plastic material has UL classification 94V-0
Gehäusematerial UL94V-0 klassifiziertStandard packaging taped and reeled
Standard Lieferform gegurtet auf Rolle**Maximum ratings ($T_A = 25^\circ\text{C}$)****Grenzwerte ($T_A = 25^\circ\text{C}$)**

			BSR 13	BSR 14
Collector-Emitter-voltage	B open	V_{CE0}	30 V	40 V
Collector-Base-voltage	E open	V_{CB0}	60 V	75 V
Emitter-Base-voltage	C open	V_{EB0}	5 V	6 V
Power dissipation – Verlustleistung		P_{tot}	250 mW ¹⁾	
Collector current – Kollektorstrom (dc)		I_C	800 mA	
Peak Collector current – Kollektor-Spitzenstrom		I_{CM}	800 mA	
Peak Base current – Basis-Spitzenstrom		I_{BM}	200 mA	
Junction temp. – Sperrschichttemperatur		T_j	150°C	
Storage temperature – Lagerungstemperatur		T_S	- 65...+ 150°C	

Characteristics ($T_j = 25^\circ\text{C}$)**Kennwerte ($T_j = 25^\circ\text{C}$)**

				Min.	Typ.	Max.
Collector-Base cutoff current – Kollektorreststrom						
$I_E = 0, V_{CB} = 50\text{ V}$	BSR 13	I_{CB0}		–	–	30 nA
			I_{CB0}	–	–	10 μA
$I_E = 0, V_{CB} = 60\text{ V}$	BSR 14	I_{CB0}		–	–	10 nA
			I_{CB0}	–	–	10 μA
Emitter-Base cutoff current – Emitterreststrom						
$I_C = 0, V_{EB} = 5\text{ V}$	BSR 13	I_{EB0}		–	–	30 nA
	BSR 14		I_{EB0}	–	–	10 nA

¹⁾ Mounted on P.C. board with 3 mm² copper pad at each terminal
 Montage auf Leiterplatte mit 3 mm² Kupferbelag (Löt-pad) an jedem Anschluß

Characteristics ($T_j = 25^\circ\text{C}$)Kennwerte ($T_j = 25^\circ\text{C}$)

		Min.	Typ.	Max.
DC current gain – Kollektor-Basis-Stromverhältnis ¹⁾				
$V_{CE} = 10\text{ V}, I_C = 0.1\text{ mA}$	BSR 13 BSR 14	h_{FE}	35	–
$V_{CE} = 10\text{ V}, I_C = 1\text{ mA}$		h_{FE}	50	–
$V_{CE} = 10\text{ V}, I_C = 10\text{ mA}$		h_{FE}	75	–
$V_{CE} = 10\text{ V}, I_C = 150\text{ mA}$		h_{FE}	100	–
$V_{CE} = 1\text{ V}, I_C = 150\text{ mA}$		h_{FE}	50	–
$V_{CE} = 10\text{ V}, I_C = 500\text{ mA}$	BSR 13 BSR 14	h_{FE} h_{FE}	30 40	– –
Collector saturation volt. – Kollektor-Sättigungsspg. ¹⁾				
$I_C = 150\text{ mA}, I_B = 15\text{ mA}$	BSR 13	V_{CEsat}	–	–
	BSR 14	V_{CEsat}	–	–
$I_C = 500\text{ mA}, I_B = 50\text{ mA}$	BSR 13	V_{CEsat}	–	–
	BSR 14	V_{CEsat}	–	–
Base saturation voltage – Basis-Sättigungsspannung ¹⁾				
$I_C = 150\text{ mA}, I_B = 15\text{ mA}$	BSR 13	V_{BEsat}	–	–
	BSR 14	V_{BEsat}	0.6 V	–
$I_C = 500\text{ mA}, I_B = 50\text{ mA}$	BSR 13	V_{BEsat}	–	–
	BSR 14	V_{BEsat}	–	–
Gain-Bandwidth Product – Transitfrequenz				
$V_{CE} = 20\text{ V}, I_C = 20\text{ mA},$ $f = 100\text{ MHz}$	BSR 13	f_T	250 MHz	–
	BSR 14	f_T	300 MHz	–
Collector-Base Capacitance – Kollektor-Basis-Kapazität				
$V_{CB} = 10\text{ V}, I_E = i_e = 0, f = 1\text{ MHz}$		C_{CB0}	–	8 pF
Switching times – Schaltzeiten				
turn-on time		t_{on}	–	–
delay time		t_d	–	–
rise time	$I_{Con} = 150\text{ mA}$	t_r	–	–
turn-off time	$I_{Bon} = 15\text{ mA}$ $- I_{Boff} = 15\text{ mA}$	t_{off}	–	–
storage time		t_s	–	–
fall time		t_f	–	–
Thermal resistance junction to ambient air Wärmewiderstand Sperrschicht – umgebende Luft		R_{thA}		420 K/W ²⁾
Recommended complementary PNP transistors Empfohlene komplementäre PNP-Transistoren			BSR 15, BSR 16	
Marking - Stempelung		BSR 13 = U7	BSR 14 = U8	

¹⁾ Tested with pulses $t_p = 300\text{ }\mu\text{s}$, duty cycle $\leq 2\%$ – Gemessen mit Impulsen $t_p = 300\text{ }\mu\text{s}$, Schaltverhältnis $\leq 2\%$

²⁾ Mounted on P.C. board with 3 mm^2 copper pad at each terminal
Montage auf Leiterplatte mit 3 mm^2 Kupferbelag (Lötpad) an jedem Anschluß

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