

# MGFC41V3642

## 3.6~4.2GHz BAND 12W INTERNALLY MATCHED GaAs FET

### DESCRIPTION

The MGFC41V3642 is an internally impedance-matched GaAs power FET especially designed for use in 3.6~4.2 GHz band amplifiers. The hermetically sealed metal-ceramic package guarantees high reliability.

### FEATURES

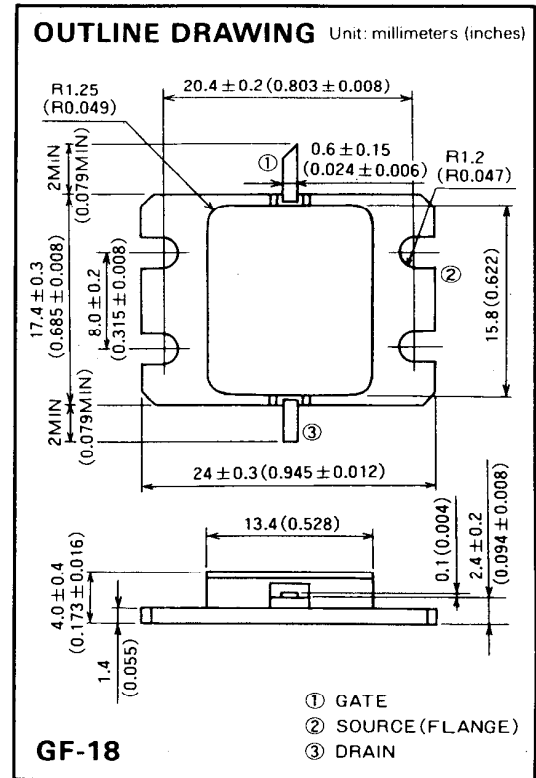
- Class A operation
- Internally matched to 50Ω system
- High output power  
 $P_{1dB} = 14W(TYP) @ 3.6\sim 4.2GHz$
- High power gain  
 $G_{LP} = 12.5dB(TYP) @ 3.6\sim 4.2GHz$
- High power added efficiency  
 $\eta_{add} = 40\%(TYP) @ 3.6\sim 4.2GHz, P_{1dB}$
- Hermetically sealed metal-ceramic package
- Low distortion [Item: -51]  
 $IM_3 = -45 dBc(TYP) @ P_o = 30(dBm) S.C.L.$

### APPLICATION

- Item-01: 3.6~4.2 GHz band power amplifier
- Item-51: Digital radio communication

### QUALITY GRADE

- IG



### ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Symbol	Parameter	Ratings	Unit
V <sub>GD0</sub>	Gate to drain voltage	-15	V
V <sub>GSO</sub>	Gate to source voltage	-15	V
I <sub>D</sub>	Drain current	12	A
I <sub>GR</sub>	Reverse gate current	-30	mA
I <sub>GF</sub>	Forward gate current	63	mA
P <sub>T</sub>	Total power dissipation *1	57.7	W
T <sub>ch</sub>	Channel temperature	175	°C
T <sub>stg</sub>	Storage temperature	-65 ~ +175	°C

\*1: T<sub>c</sub> = 25°C

### RECOMMENDED BIAS CONDITIONS

- V<sub>DS</sub> = 10V
- I<sub>D</sub> = 3.4A
- R<sub>g</sub> = 50Ω
- Refer to Bias Procedure

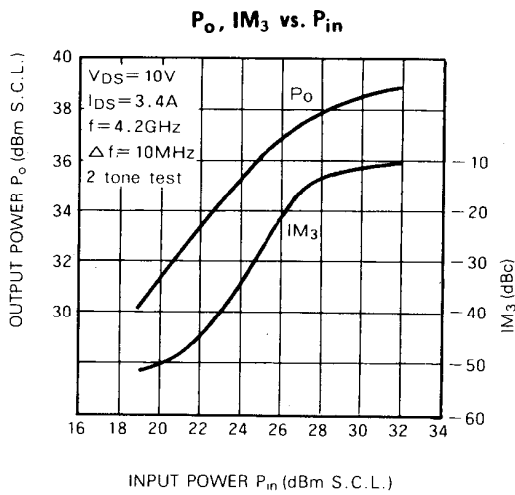
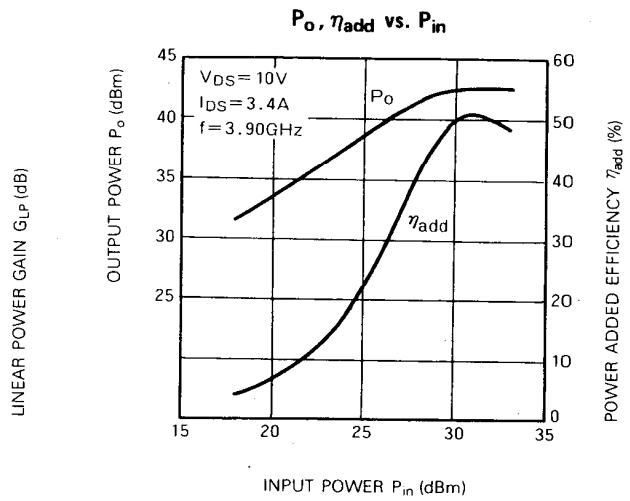
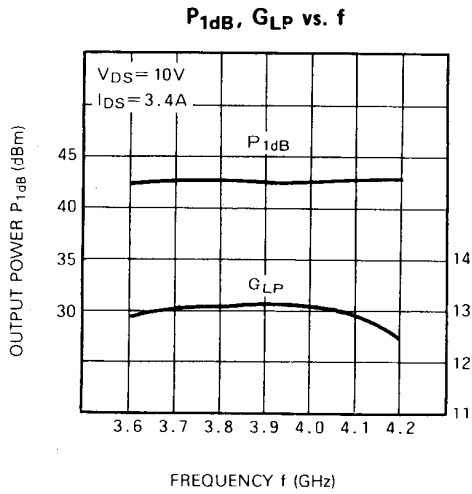
### ELECTRICAL CHARACTERISTICS (Ta = 25°C)

Symbol	Parameter	Test conditions	Limits			Unit	
			Min	Typ	Max		
I <sub>DSS</sub>	Saturated drain current	V <sub>DS</sub> = 3V, V <sub>GS</sub> = 0V	—	—	12	A	
g <sub>m</sub>	Transconductance	V <sub>DS</sub> = 3V, I <sub>D</sub> = 3.0A	—	3.0	—	S	
V <sub>GS(off)</sub>	Gate to source cut-off voltage	V <sub>DS</sub> = 3V, I <sub>D</sub> = 30mA	—	—	-5	V	
P <sub>1dB</sub>	Output power at 1dB gain compression	V <sub>DS</sub> = 10V, I <sub>D</sub> = 3.4A, f = 3.6~4.2GHz	40	41.5	—	dBm	
G <sub>LP</sub>	Linear power gain		11.0	12.5	—	dB	
I <sub>D</sub>	Drain current		—	3.3	—	A	
η <sub>add</sub>	Power added efficiency		—	40	—	%	
IM <sub>3</sub>	3rd order IM distortion *1		-42	-45	—	dBc	
R <sub>th(ch-c)</sub>	Thermal resistance *2		ΔV <sub>f</sub> method	—	—	2.6	°C/W

\*1: Item-51, 2-tone test P<sub>o</sub> = 30 dBm Single Carrier Level f = 4.2 GHz Δf = 10 MHz \*2: Channel to case

**3.6~4.2GHz BAND 12W INTERNALLY MATCHED GaAs FET**

**TYPICAL CHARACTERISTICS** (Ta=25°C)



**S PARAMETERS** (Ta=25°C, V<sub>DS</sub>=10V, I<sub>DS</sub>=3.4A)

f (GHz)	S Parameters (TYP.)							
	S <sub>11</sub>		S <sub>21</sub>		S <sub>12</sub>		S <sub>22</sub>	
	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)	Magn.	Angle (deg.)
3.6	0.48	-168	4.13	57	0.042	10	0.45	-23
3.7	0.47	155	4.29	32	0.052	-21	0.34	-45
3.8	0.44	121	4.45	6	0.052	-45	0.26	-73
3.9	0.40	86	4.39	-20	0.058	-75	0.23	-115
4.0	0.35	42	4.34	-46	0.060	-101	0.22	-160
4.1	0.31	-11	4.17	-73	0.066	-123	0.28	166
4.2	0.34	-70	3.70	-100	0.057	-150	0.29	140