

## NCE N&P-Channel complementary Power MOSFET

### Description

The NCE40NP2815G uses advanced trench technology and design to provide excellent  $R_{DS(ON)}$  with low gate charge. It can be used in a wide variety of applications.

### General Features

#### N channel

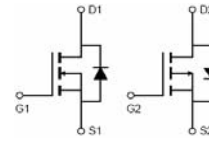
- $V_{DS} = 40V, I_D = 28A$   
 $R_{DS(ON)} < 18m\Omega @ V_{GS} = 10V$   
 $R_{DS(ON)} < 28m\Omega @ V_{GS} = 4.5V$

#### p channel

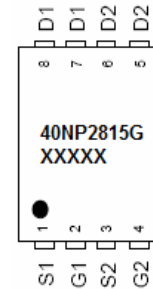
- $V_{DS} = -40V, I_D = -15A$   
 $R_{DS(ON)} < 35m\Omega @ V_{GS} = -10V$   
 $R_{DS(ON)} < 45m\Omega @ V_{GS} = -4.5V$
- High density cell design for ultra low  $R_{dson}$
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high  $E_{AS}$
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

### Application

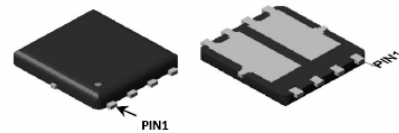
- H-bridge
- Inverters



Schematic diagram



Marking and pin assignment



Top View

Bottom View

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
40NP2815G	NCE40NP2815G	DFN5X6-8L	-	-	-

### Absolute Maximum Ratings ( $T_C = 25^\circ C$ unless otherwise noted)

Parameter	Symbol	N-Channel	P-Channel	Unit
Drain-Source Voltage	$V_{DS}$	40	-40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	$\pm 20$	V
Continuous Drain Current	$I_D$	$T_C = 25^\circ C$	-15	A
		$T_C = 100^\circ C$	-10.6	
Pulsed Drain Current <sup>(Note 1)</sup>	$I_{DM}$	70	-60	A
Maximum Power Dissipation	$P_D$	35		W
Operating Junction and Storage Temperature Range	$T_J, T_{STG}$	-55 To 150		$^\circ C$

### Thermal Characteristic

Thermal Resistance, Junction-to-Case <sup>(Note 2)</sup>	$R_{\theta JC}$	3.6	$^\circ C/W$
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## N-Channel Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =250μA	40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V	-	-	1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics</b> (Note 3)						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.0	1.4	2.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =15A	-	15	18	mΩ
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	22	28	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =5V, I <sub>D</sub> =15A	-	7	-	S
<b>Dynamic Characteristics</b> (Note 4)						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =20V, V <sub>GS</sub> =0V, F=1.0MHz	-	964	-	PF
Output Capacitance	C <sub>oss</sub>		-	109	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	96	-	PF
<b>Switching Characteristics</b> (Note 4)						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =20V, R <sub>L</sub> =2.5Ω V <sub>GS</sub> =10V, R <sub>G</sub> =3Ω	-	5.5	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	24	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =20V, I <sub>D</sub> =15A, V <sub>GS</sub> =10V	-	22.9	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	5.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =15A	-	-	1.2	V
Diode Forward Current (Note 2)	I <sub>S</sub>		-	-	28	A

### Notes:

1. Repetitive Rating: Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, t ≤ 10 sec.
3. Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2%.
4. Guaranteed by design, not subject to production
5. EAS condition: T<sub>J</sub>=25°C, V<sub>DD</sub>=20V, V<sub>G</sub>=10V, L=0.5mH, R<sub>G</sub>=25Ω

N- Channel Typical Electrical and Thermal Characteristics (Curves)

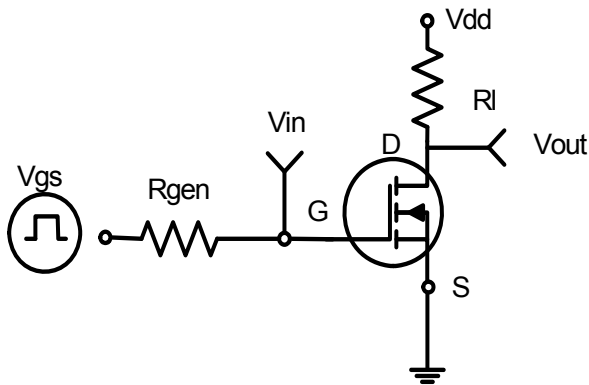


Figure 1: Switching Test Circuit

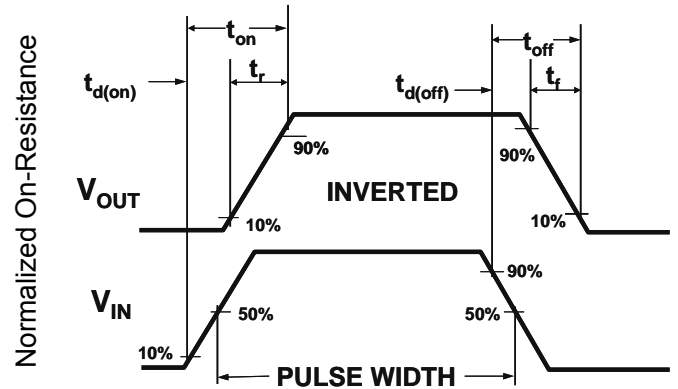


Figure 2: Switching Waveforms

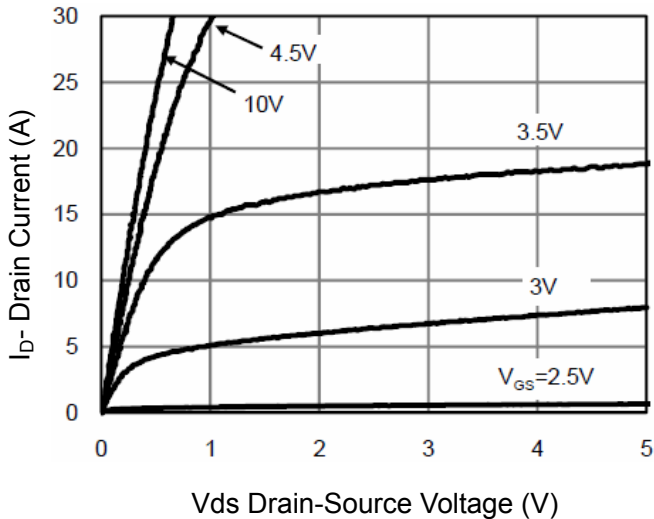


Figure 3 Output Characteristics

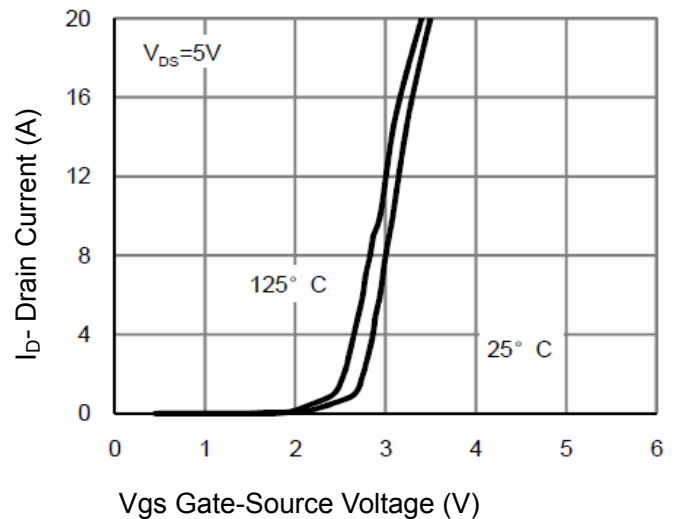


Figure 4 Transfer Characteristics

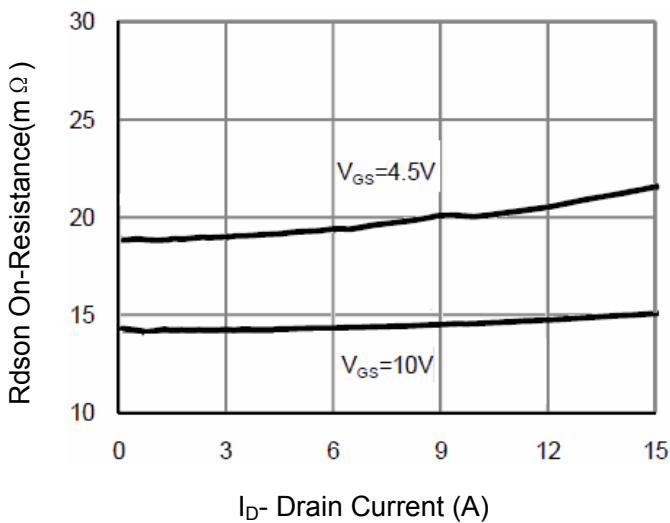


Figure 5 Drain-Source On-Resistance

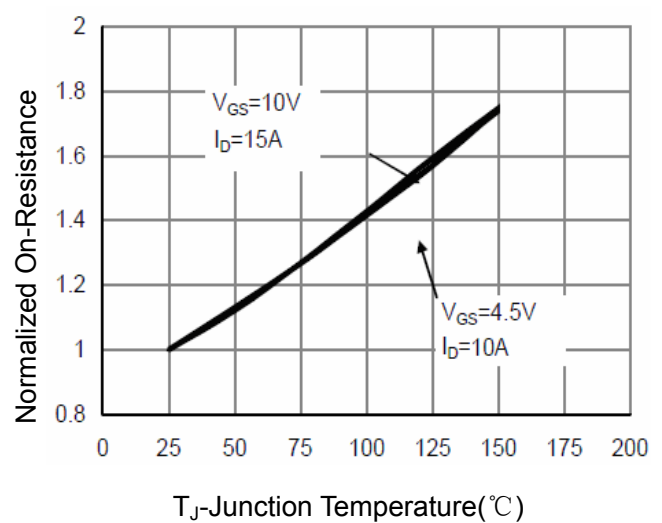
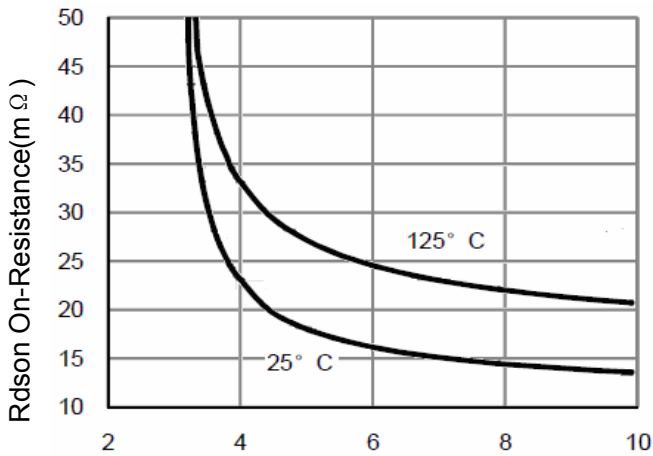
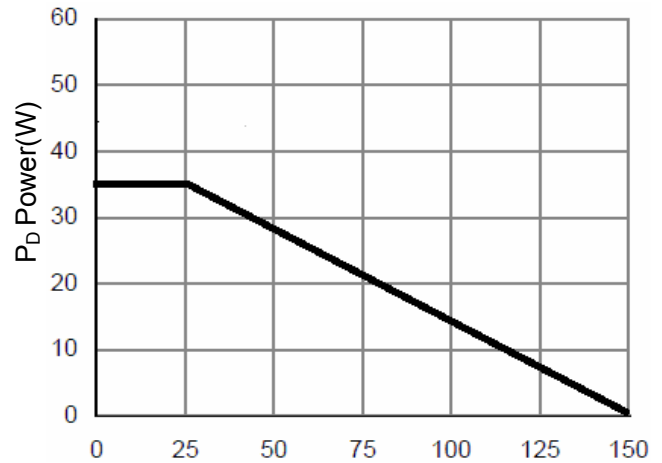


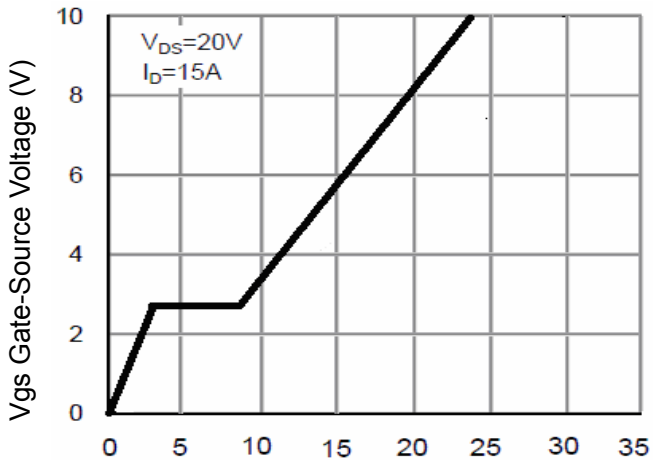
Figure 6 Drain-Source On-Resistance



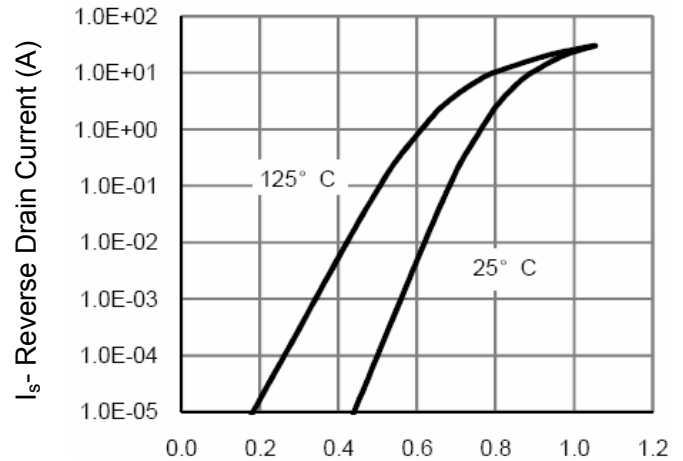
Vgs Gate-Source Voltage (V)  
**Figure 7 Rdson vs Vgs**



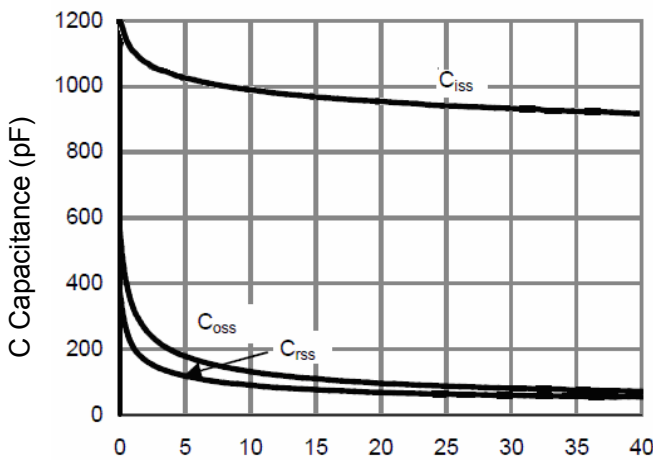
Tj Junction Temperature (°C)  
**Figure 8 Power Dissipation**



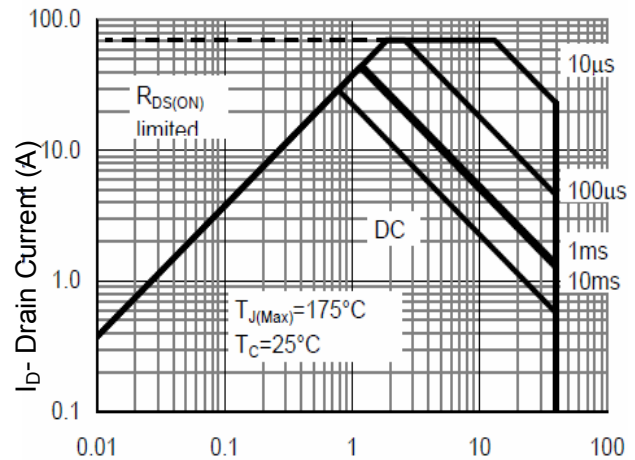
Qg Gate Charge (nC)  
**Figure 9 Gate Charge**



**Figure 10 Source- Drain Diode Forward**



Vds Drain-Source Voltage (V)  
**Figure 11 Capacitance vs Vds**



**Figure 12 Safe Operation Area**

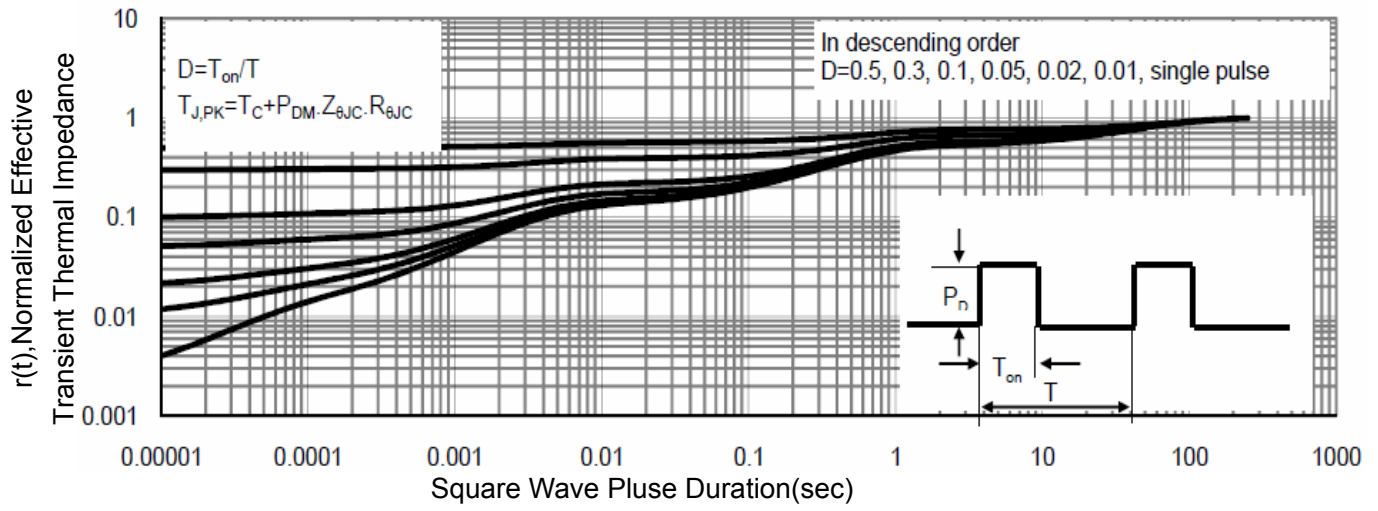
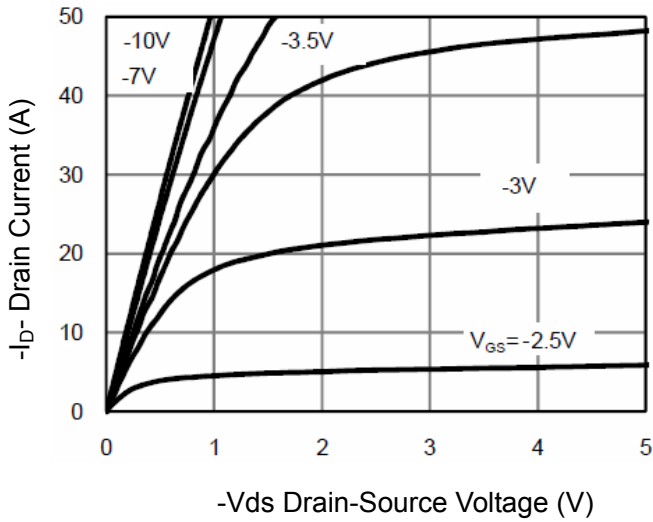


Figure 13 Normalized Maximum Transient Thermal Impedance

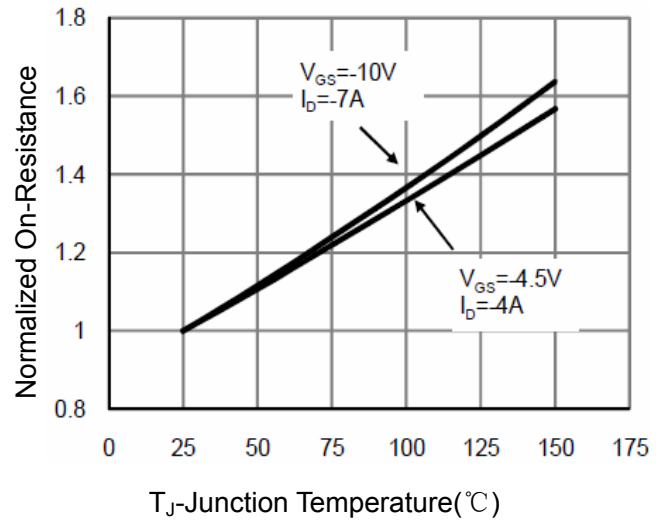
**P-Channel Electrical Characteristics (T<sub>C</sub>=25°C unless otherwise noted)**

Parameter	Symbol	Condition	Min	Typ	Max	Unit
<b>Off Characteristics</b>						
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> =0V, I <sub>D</sub> =-250μA	-40	-	-	V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =-40V, V <sub>GS</sub> =0V	-	-	-1	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>GS</sub> =±20V, V <sub>DS</sub> =0V	-	-	±100	nA
<b>On Characteristics (Note 3)</b>						
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =-250μA	-1.0	-1.5	-2.0	V
Drain-Source On-State Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =-10V, I <sub>D</sub> =-7A	-	29	35	mΩ
		V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-4A	-	34	45	mΩ
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =-5V, I <sub>D</sub> =-7A	20	-	-	S
<b>Dynamic Characteristics (Note 4)</b>						
Input Capacitance	C <sub>iss</sub>	V <sub>DS</sub> =-20V, V <sub>GS</sub> =0V, F=1.0MHz	-	964	-	PF
Output Capacitance	C <sub>oss</sub>		-	109	-	PF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	96	-	PF
<b>Switching Characteristics (Note 4)</b>						
Turn-on Delay Time	t <sub>d(on)</sub>	V <sub>DD</sub> =-20V, R <sub>L</sub> =2.3Ω V <sub>GS</sub> =-10V, R <sub>GEN</sub> =6Ω	-	5.5	-	nS
Turn-on Rise Time	t <sub>r</sub>		-	14	-	nS
Turn-Off Delay Time	t <sub>d(off)</sub>		-	24	-	nS
Turn-Off Fall Time	t <sub>f</sub>		-	12	-	nS
Total Gate Charge	Q <sub>g</sub>	V <sub>DS</sub> =-20V, I <sub>D</sub> =-7A V <sub>GS</sub> =-10V	-	22.9	-	nC
Gate-Source Charge	Q <sub>gs</sub>		-	3.5	-	nC
Gate-Drain Charge	Q <sub>gd</sub>		-	5.3	-	nC
<b>Drain-Source Diode Characteristics</b>						
Diode Forward Voltage (Note 3)	V <sub>SD</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =-10A	-	-	-1.2	V

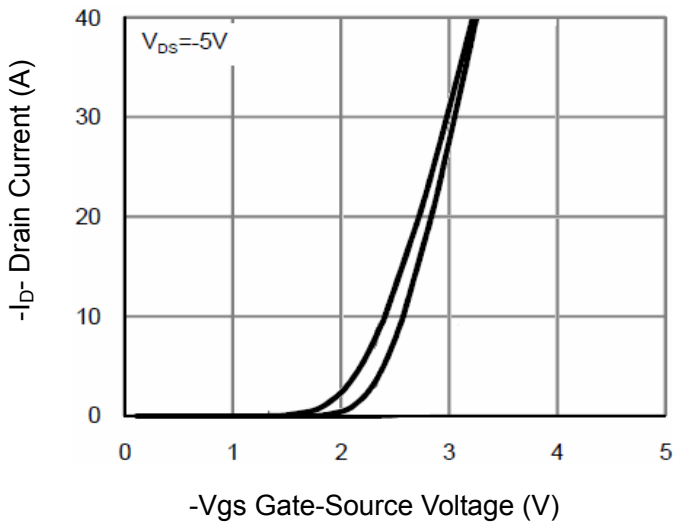
**P- Channel Typical Electrical and Thermal Characteristics (Curves)**



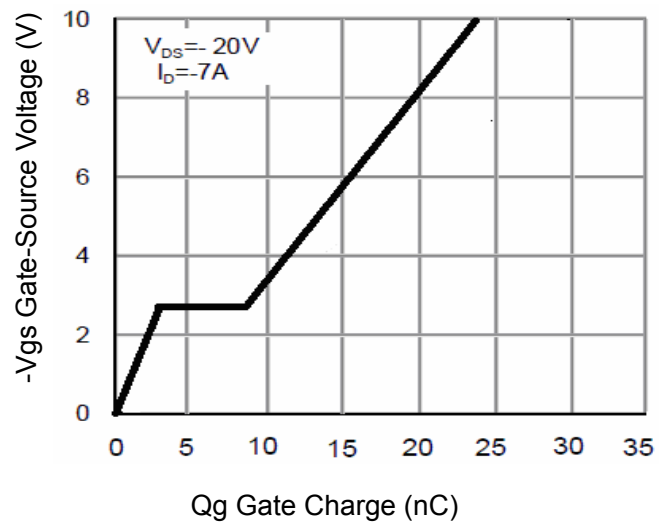
**Figure 1 Output Characteristics**



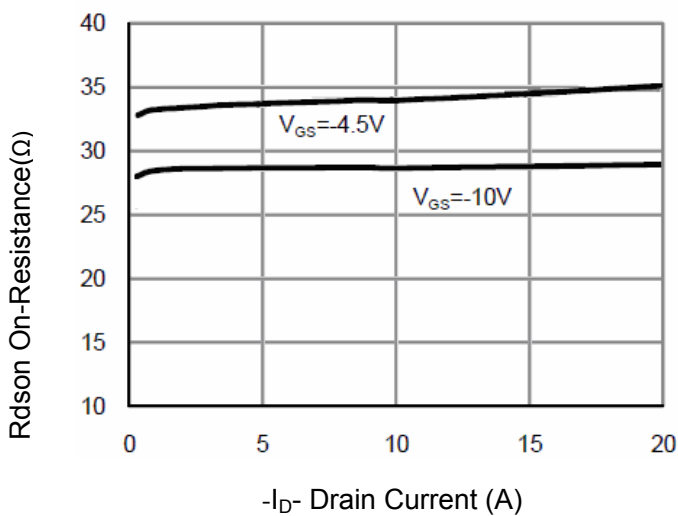
**Figure 4 Rdson-Junction Temperature**



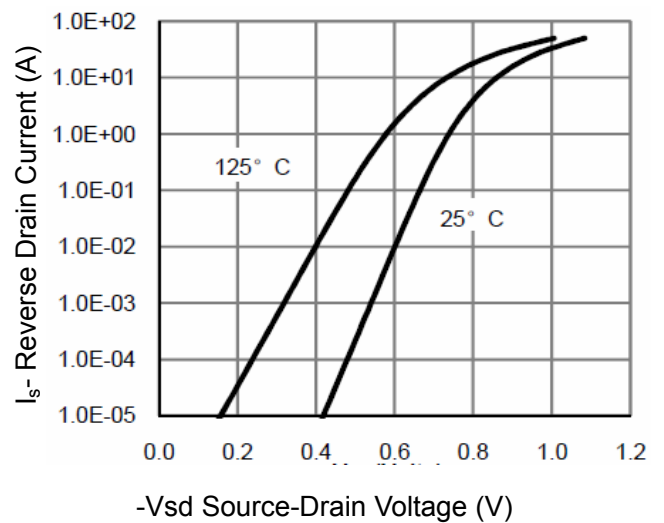
**Figure 2 Transfer Characteristics**



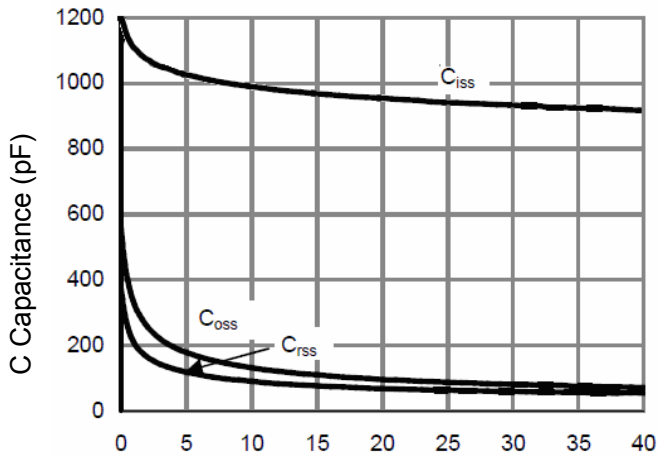
**Figure 5 Gate Charge**



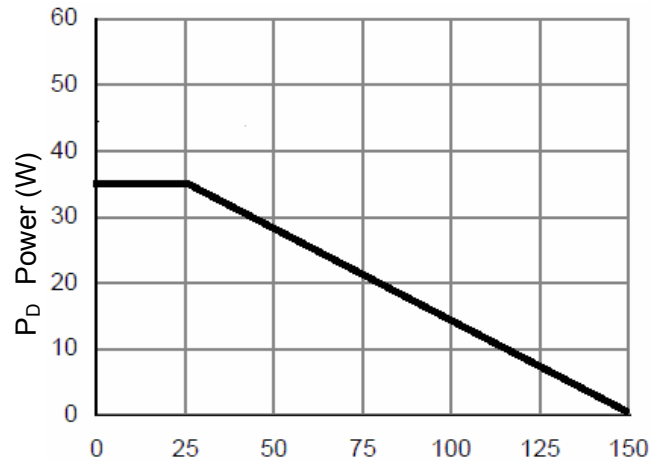
**Figure 3 Rdson- Drain Current**



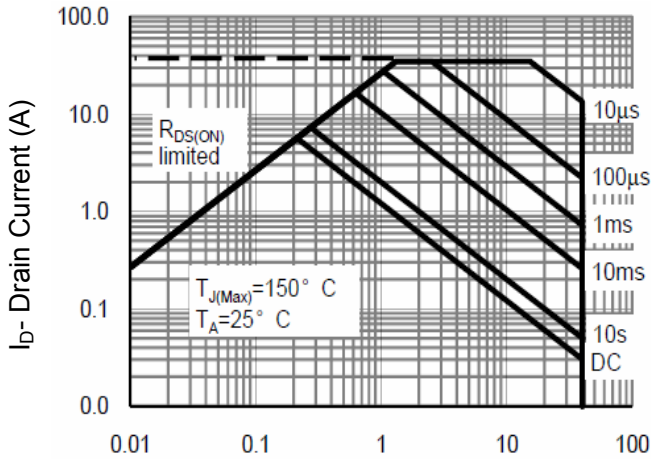
**Figure 6 Source- Drain Diode Forward**



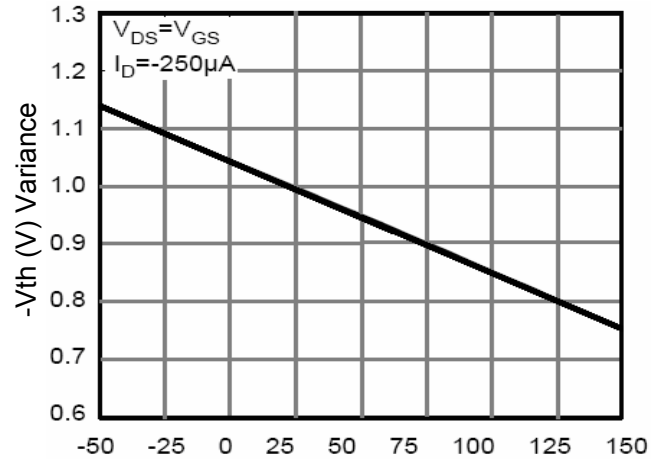
-Vds Drain-Source Voltage (V)  
**Figure 7 Capacitance vs Vds**



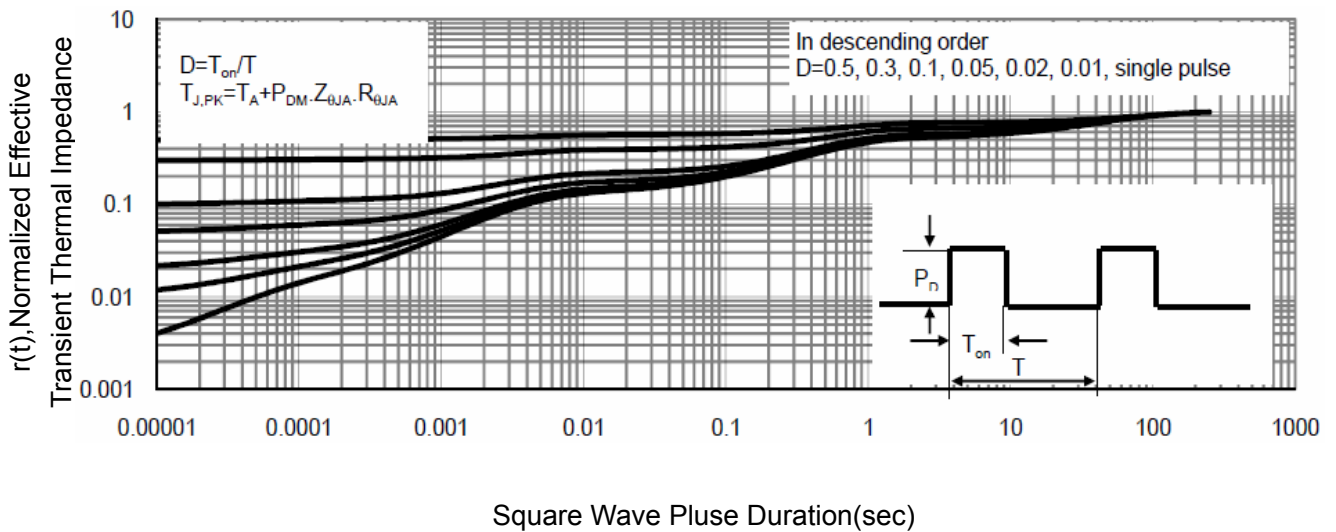
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 9 Power Dissipation**



-Vds Drain-Source Voltage (V)  
**Figure 8 Safe Operation Area**



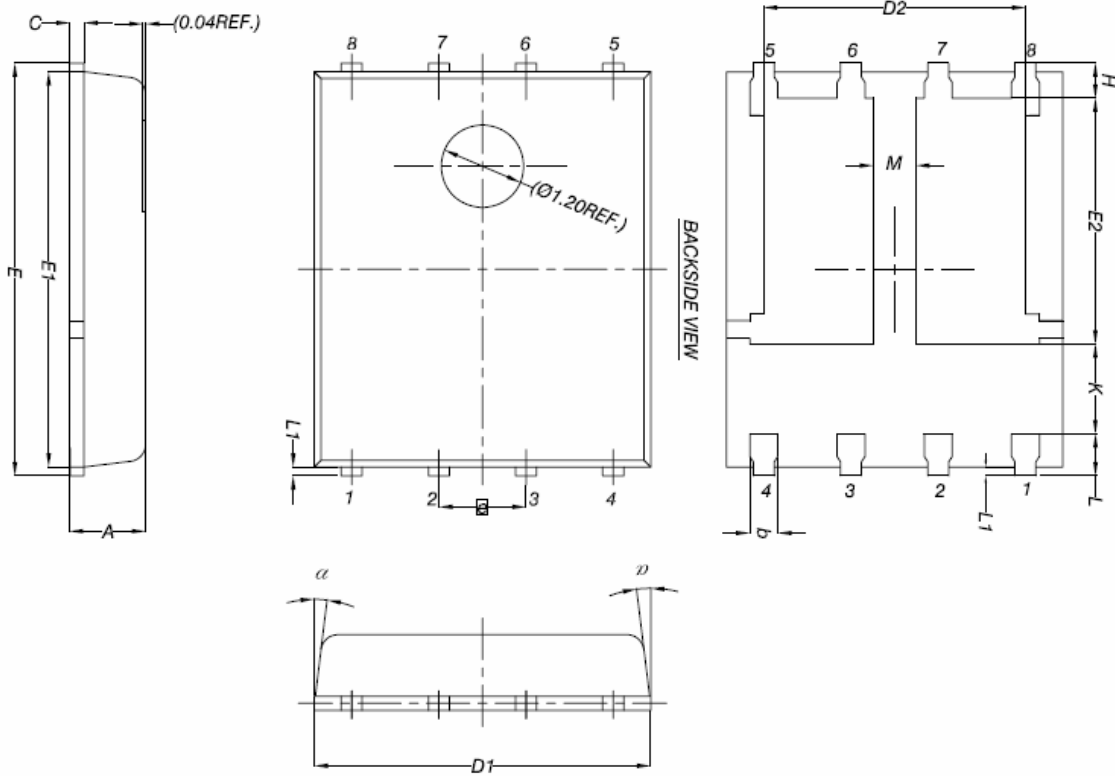
T<sub>J</sub>-Junction Temperature(°C)  
**Figure 10 V<sub>GS(th)</sub> vs Junction Temperature**



Square Wave Pulse Duration(sec)  
**Figure 11 Normalized Maximum Transient Thermal Impedance**



DFN5X6-8L Package Information



DIM.	MILLIMETERS		
	MIN.	NOM.	MAX.
A	0.90	1.00	1.10
b	0.33	0.41	0.51
C	0.20	0.25	0.30
D1	4.80	4.90	5.00
D2	3.61	3.81	3.96
E	5.90	6.00	6.10
E1	5.70	5.75	5.80
E2	3.38	3.58	3.78
e	1.27 BSC		
H	0.41	0.51	0.61
K	1.10	-	-
L	0.51	0.61	0.71
L1	0.06	0.13	0.20
M	0.50	-	-
α	0°	-	12°

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