


Features

- Provides ESD Protection per IEC 61000-4-Air ± 20kV, Contact ±20kV IEC6100-4-5(surge):3A (8/20us)
- Maximum working voltage Vrwm= 5V
- Ultra low capacitance =5.3pF (Typical)
- 1 Channel of ESD Protection
- Low Channel Input Capacitance
- **Totally Lead-Free & Fully RoHS Compliant**

Mechanical Characteristics

- Case: SLP0603 -2
- Case Material: Chip Scale Package
- Terminals: NiAu Bump. Solderable per MIL-STD-202, Method 208 
- Weight: 0.0002 grams (Approximate)

Applications

- Cellular Handsets
- Portable Electronics
- Computers and Peripheral

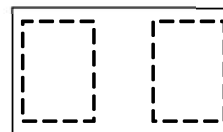
Ordering Information

Part Number	Qty per Reel	Reel Size
TPESD0551Y-CSP	15000	7"

Circuit Diagram



Pin Configuration



MARKING: A

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Absolute Maximum Rating

(Temp=25°C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit
Peak Pulse Power (tp=8/20μs waveform)	P _{PPP}	30	Watts
Peak Pulse Current(tp=8/20μs waveform)	I _{PP}	3	A
ESD Rating per IEC61000-4-2:	Contact	20	KV
	Air	20	
Lead Soldering Temperature	T _L	260 (10 sec.)	°C
Operating Temperature Range	T _J	-40 ~ 125	°C
Storage Temperature Range	T _{STG}	-60 ~ 150	°C

Electrical Characteristics

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Reverse Standoff Voltage	V _{RWM}	—	—	5	V	—
Channel Leakage Current (Note 6)	I _{RM}	—	1	100	nA	V _{RWM} = 5V
Snapback Voltage	V _{SNP}	5.3	—	—	V	—
Clamping Voltage, Positive Transients	V _{CL}	—	—	11.5	V	I _{PP} = 0.5A, t _P = 8/20μS
		—	—	12.8		I _{PP} = 1A, t _P = 8/20μS
Breakdown Voltage	V _{BR}	6	—	10	V	I _R = 1mA
Differential Resistance	R _{DYN}	—	2.0	—	Ω	TLP, 10A, t _P = 100ns
Channel Input Capacitance	C _{IN}	4	5.3	6	pF	V _R = 0V, f = 1MHz

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Typical characteristics

Fig1. 8/20μs Pulse Waveform

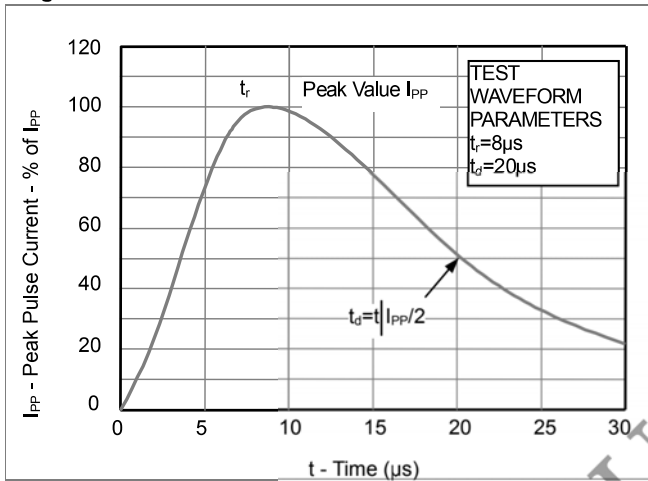


Fig2. ESD Pulse Waveform (according to IEC 61000-4-2)

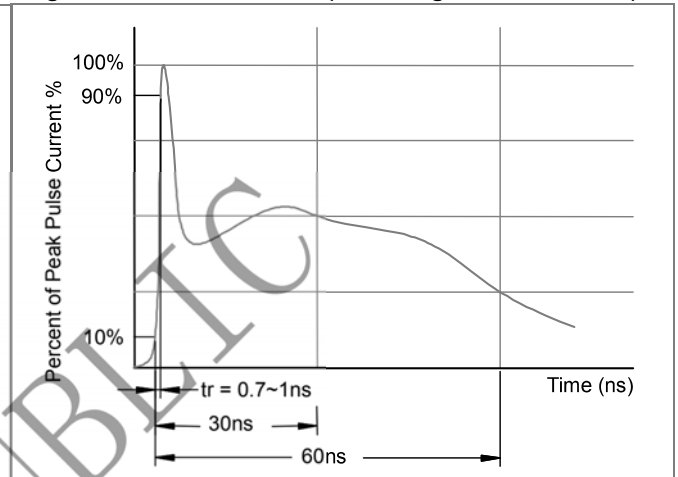
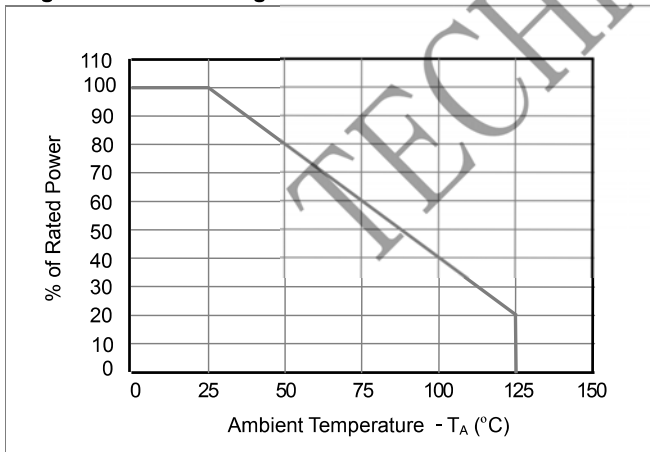
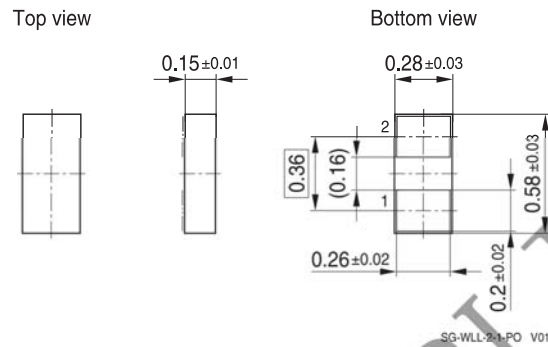


Fig3. Power Derating Curve



Outline Drawing -SLP0603-2



Land Pattern - SLP0603-2

