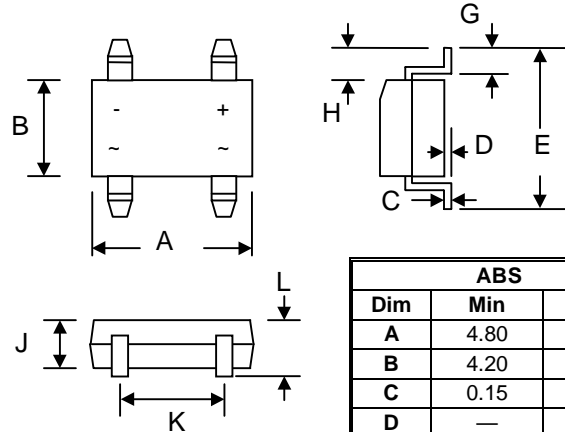


### 1.0A SURFACE MOUNT GLASS PASSIVATED BRIDGE RECTIFIER

#### Features

- Glass Passivated Die Construction
- Low Forward Voltage Drop
- High Current Capability
- High Surge Current Capability
- Designed for Surface Mount Application
- Plastic Material – UL Flammability 94V-O



ABS		
Dim	Min	Max
A	4.80	5.30
B	4.20	4.60
C	0.15	0.25
D	—	0.20
E	6.00	6.80
G	0.30	0.70
H	0.90	1.10
J	—	1.50
K	3.80	4.20
L	1.22	1.72
All Dimensions in mm		

#### Mechanical Data

- Case: SOPA-4, ABS, Molded Plastic
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: As Marked on Case
- Mounting Position: Any
- Marking: Type Number
- **Lead Free: For RoHS / Lead Free Version**

#### Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Single Phase, half wave, 60Hz, resistive or inductive load.  
For capacitive load, derate current by 20%.

Characteristic	Symbol	ABS05	ABS1	ABS2	ABS4	ABS6	ABS8	ABS10	Unit
Peak Repetitive Reverse Voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Working Peak Reverse Voltage	$V_{RWM}$								
DC Blocking Voltage	$V_R$								
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	140	280	420	560	700	V
Average Rectified Output Current (Note 1) @ $T_A = 30^\circ\text{C}$	$I_O$	1.0							A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	35							A
$I^2t$ Rating for Fusing ( $t < 8.3\text{ms}$ )	$I^2t$	5.0							$\text{A}^2\text{s}$
Forward Voltage per element @ $I_F = 1.0\text{A}$	$V_{FM}$	1.1							V
Peak Reverse Current @ $T_A = 25^\circ\text{C}$ At Rated DC Blocking Voltage @ $T_A = 125^\circ\text{C}$	$I_{RM}$	5.0 500							$\mu\text{A}$
Typical Junction Capacitance per leg (Note 3)	$C_j$	13							pF
Typical Thermal Resistance per leg (Note 1)	$R_{\theta JA}$ $R_{\theta JL}$	62.5 25							$^\circ\text{C/W}$
Operating and Storage Temperature Range	$T_j, T_{STG}$	-55 to +150							$^\circ\text{C}$

Note: 1. Mounted on aluminum substrate PC board with  $1.3\text{mm}^2$  solder pad.  
2. Measured at 1.0 MHz and applied reverse voltage of 4.0V D.C.

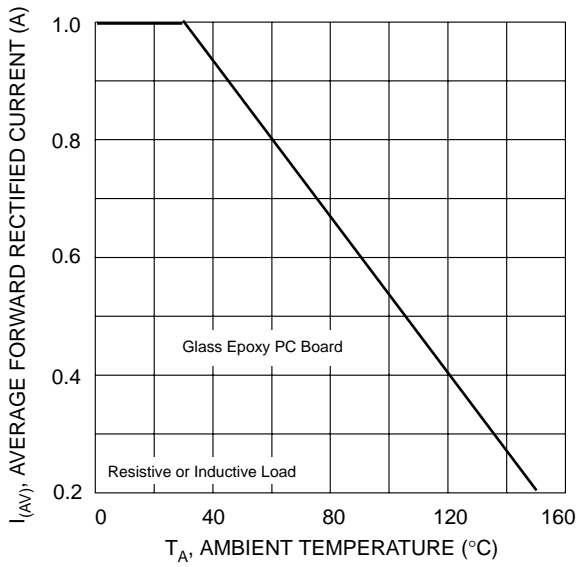


Fig. 1 Output Current Derating Curve

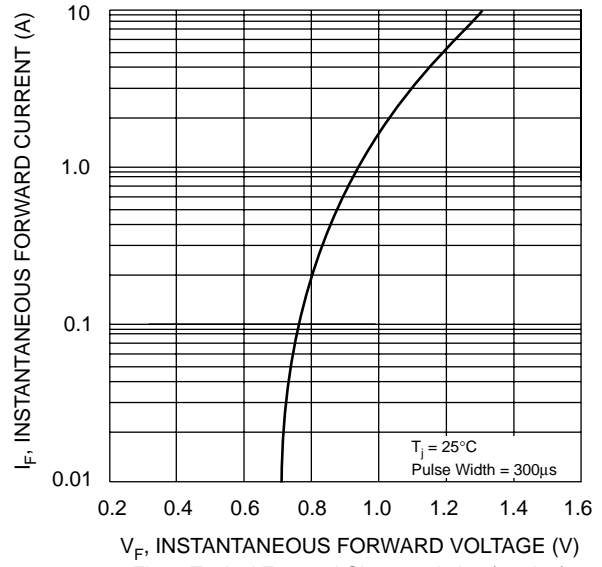


Fig. 2 Typical Forward Characteristics (per leg)

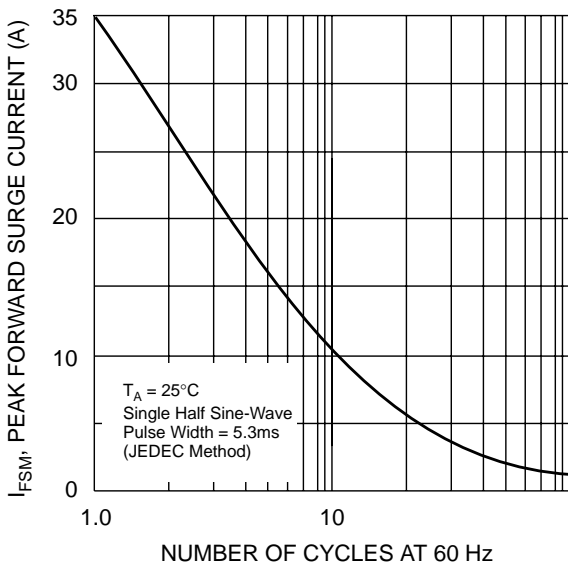


Fig. 3 Maximum Peak Forward Surge Current (per leg)

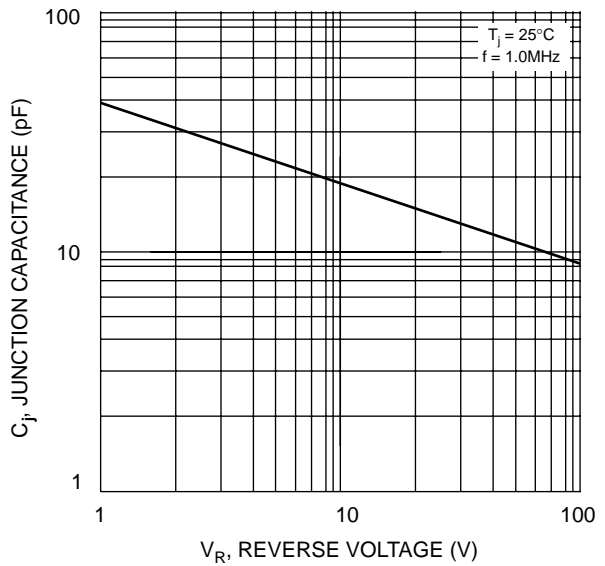


Fig. 4 Typical Junction Capacitance

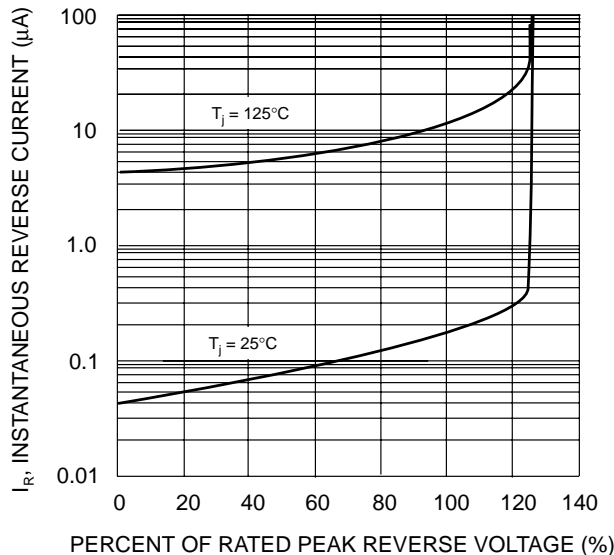


Fig. 5 Typical Reverse Characteristics (per element)