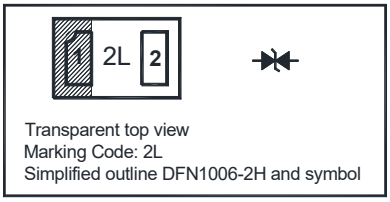
ESDBL2402HP

Transient Voltage Suppressor

for ESD Protection

PINNING

PIN	DESCRIPTION
1	Anode
2	Anode



Transparent top view
Marking Code: 2L
Simplified outline DFN1006-2H and symbol

Features

- Low Capacitance
- Low leakage current

Absolute Maximum Ratings (T_a = 25℃)

Parameter		Symbol	Value	Unit
Peak Pulse Power (tp = 8/20 µs)		P_{PK}	90	W
Peak Pulse Current (tp = 8/20 µs)		I_{PP}	2	A
IEC61000-4-2 (ESD)	Air Contact	V_{ESD}	± 20 ± 20	KV
Power Dissipation [1)]		P_D	250	mW
Thermal Resistance Junction to Ambient [1)]		$R_{\theta JA}$	500	℃/W
Operation and Storage Temperature Range		T_j , T_{stg}	- 65 to + 150	℃

[1)] Device mounted on FR-4 PCB pad layout (2oz copper).

Characteristics at T_a = 25℃

Parameter	Symbol	Min.	Max.	Unit
Reverse Stand-Off Voltage	V_{RWM}	-	24	V
Reverse Breakdown Voltage at I_R = 1 mA	$V_{(BR)R}$	26	32	V
Reverse Current at V_{RWM} = 24 V	I_R	-	0.1	µA
Clamping Voltage at I_{PP} = 1 A, tp = 8/20 µs at I_{PP} = 2 A, tp = 8/20 µs	V_C	- -	42 46	V
Junction Capacitance at V_R = 0 V, f = 1 MHz	C_j	-	17	pF

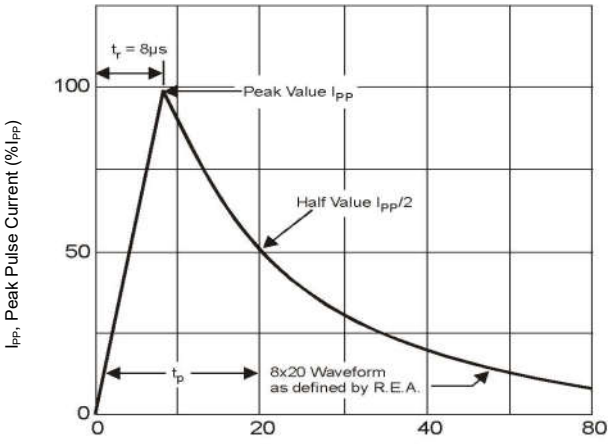


Figure 1. Pulse Waveform

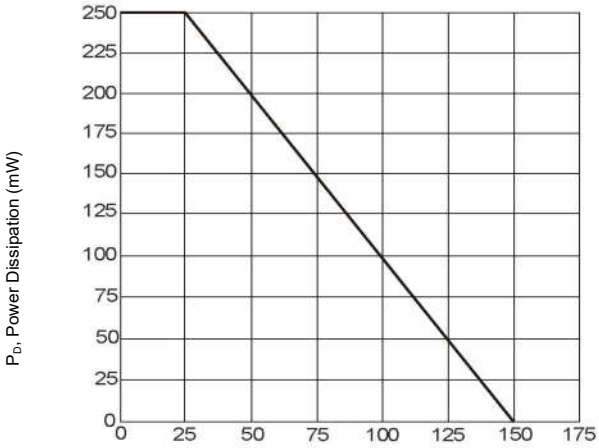


Figure 2. Power Derating Curve

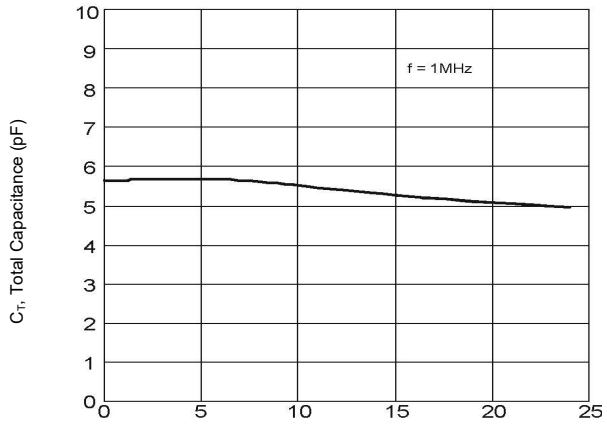


Figure 3. Typical Capacitance

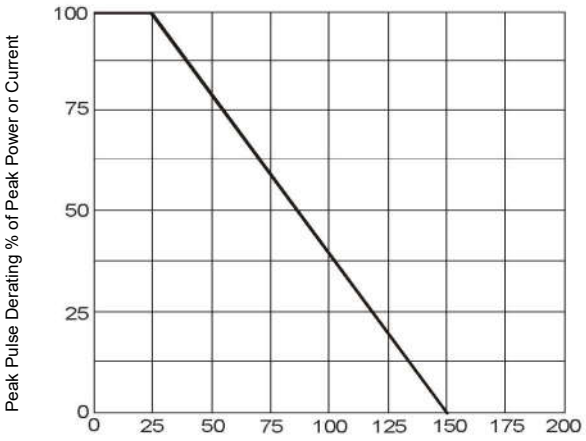


Figure 4. Pulse Derating Curve

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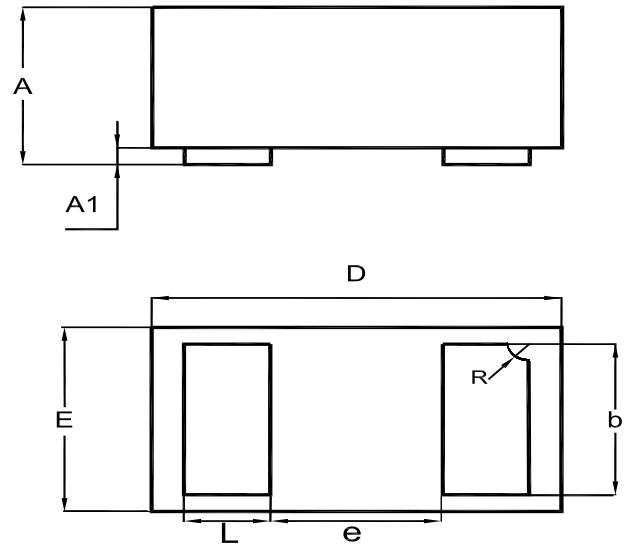
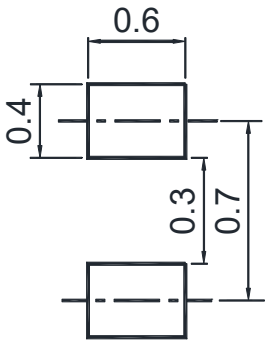
PACKAGE OUTLINE

Plastic surface mounted package **DFN1006-2H**



UNIT	A	A1	b	D	E	e	L	R
mm	0.51 0.46	0.05 0	0.55 0.45	1.05 0.95	0.65 0.55	0.4	0.3 0.2	0.15 0.05

Recommended Soldering Footprint



Packing information

Package	Tape Width (mm)	Pitch		Reel Size		Per Reel Packing Quantity
		mm	inch	mm	inch	
DFN1006-2H	8	4 ± 0.1	0.157 ± 0.004	178	7	5,000