



Is Now Part of



**ON Semiconductor®**

To learn more about ON Semiconductor, please visit our website at  
[www.onsemi.com](http://www.onsemi.com)

Please note: As part of the Fairchild Semiconductor integration, some of the Fairchild orderable part numbers will need to change in order to meet ON Semiconductor's system requirements. Since the ON Semiconductor product management systems do not have the ability to manage part nomenclature that utilizes an underscore (\_), the underscore (\_) in the Fairchild part numbers will be changed to a dash (-). This document may contain device numbers with an underscore (\_). Please check the ON Semiconductor website to verify the updated device numbers. The most current and up-to-date ordering information can be found at [www.onsemi.com](http://www.onsemi.com). Please email any questions regarding the system integration to [Fairchild\\_questions@onsemi.com](mailto:Fairchild_questions@onsemi.com).

ON Semiconductor and the ON Semiconductor logo are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.



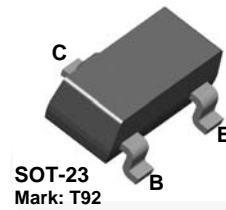
March 2014

# BSR18A

## PNP General-Purpose Amplifier

### Description

This device is designed as a general-purpose amplifier for switching applications at collector currents of 10  $\mu$ A to 100 mA. Sourced from process 66.



### Ordering Information

Part Number	Marking	Package	Packing Method
BSR18A	T92	SOT-23 3L	Tape and Reel

### Absolute Maximum Ratings<sup>(1),(2)</sup>

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only. Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Value	Unit
$V_{CEO}$	Collector-Emitter Voltage	-40	V
$V_{CBO}$	Collector-Base Voltage	-40	V
$V_{EBO}$	Emitter-Base Voltage	-5	V
$I_C$	Collector Current - Continuous	-200	mA
$T_J, T_{STG}$	Junction and Storage Temperature Range	-55 to +150	$^\circ\text{C}$

#### Notes:

1. These ratings are based on a maximum junction temperature of 150 $^\circ\text{C}$ .
2. These are steady-state limits. Fairchild Semiconductor should be consulted on applications involving pulsed or low-duty-cycle operations.

### Thermal Characteristics<sup>(3)</sup>

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Max.	Unit
$P_D$	Total Device Dissipation	350	mW
	Derate Above $T_A = 25^\circ\text{C}$	2.8	mW/ $^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	357	$^\circ\text{C}/\text{W}$

**Note:**

3. Device mounted on FR-4 PCB 40 mm X 40 mm X 1.5 mm.

### Electrical Characteristics

Values are at  $T_A = 25^\circ\text{C}$  unless otherwise noted.

Symbol	Parameter	Conditions	Min.	Max.	Unit
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	$I_C = -10 \mu\text{A}, I_B = 0$	-40		V
$V_{(BR)CBO}$	Collector-Base Breakdown Voltage	$I_C = -1.0 \text{ mA}, I_E = 0$	-40		V
$V_{(BR)EBO}$	Emitter-Base Breakdown Voltage	$I_E = -10 \mu\text{A}, I_C = 0$	-5.0		V
$I_{CBO}$	Collector Cut-Off Current	$V_{CB} = -30 \text{ V}, I_E = 0$		-50	nA
$I_{EBO}$	Emitter Cut-Off Current	$V_{EB} = -3.0 \text{ V}, I_C = 0$		-50	nA
$h_{FE}$	DC Current Gain <sup>(4)</sup>	$I_C = -0.1 \text{ mA}, V_{CE} = -1.0 \text{ V}$	60		
		$I_C = -1.0 \text{ mA}, V_{CE} = -1.0 \text{ V}$	80		
		$I_C = -10 \text{ mA}, V_{CE} = -1.0 \text{ V}$	100	300	
		$I_C = -50 \text{ mA}, V_{CE} = -1.0 \text{ V}$	60		
		$I_C = -100 \text{ mA}, V_{CE} = -1.0 \text{ V}$	30		
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage <sup>(4)</sup>	$I_C = -10 \text{ mA}, I_B = -1.0 \text{ mA}$		-0.25	V
		$I_C = -50 \text{ mA}, I_B = -5.0 \text{ mA}$		-0.40	
$V_{BE(sat)}$	Base-Emitter Saturation Voltage <sup>(4)</sup>	$I_C = -10 \text{ mA}, I_B = -1.0 \text{ mA}$	-0.65	-0.85	V
		$I_C = -50 \text{ mA}, I_B = -5.0 \text{ mA}$		-0.95	
$f_T$	Transition Frequency	$I_C = -10 \text{ mA}, V_{CE} = -20 \text{ V}, f = 100 \text{ MHz}$	250		MHz
$C_{cb}$	Collector-Base Capacitance	$V_{CB} = -5.0 \text{ V}, I_E = 0, f = 100 \text{ kHz}$		4.5	pF
$C_{eb}$	Emitter-Base Capacitance	$V_{EB} = -0.5 \text{ V}, I_C = 0, f = 100 \text{ kHz}$		10	pF
$h_{ie}$	Input Impedance	$V_{CE} = -10 \text{ V}, I_C = -1.0 \text{ mA}, f = 1.0 \text{ kHz}$	2	12	k $\Omega$
$h_{ie}$	Small-Signal Current Gain	$V_{CE} = -10 \text{ V}, I_C = -1.0 \text{ mA}, f = 1.0 \text{ kHz}$	100	400	
$h_{ie}$	Output Admittance	$V_{CE} = -10 \text{ V}, I_C = -1.0 \text{ mA}, f = 1.0 \text{ kHz}$	3	60	$\mu\text{S}$
$t_d$	Delay Time	$I_C = -10 \text{ mA}, I_{B1} = -1.0 \text{ mA}, V_{EB} = -0.5 \text{ V}$		35	ns
$t_r$	Rise Time			35	ns
$t_s$	Storage Time	$I_C = -10 \text{ mA}, I_{Bon} = I_{Boff} = -1.0 \text{ mA}$		275	ns
$t_f$	Fall Time			75	ns

**Note:**

4. Pulse test: pulse width 300  $\mu\text{s}$ , duty cycle 0.01%.

## Typical Performance Characteristics

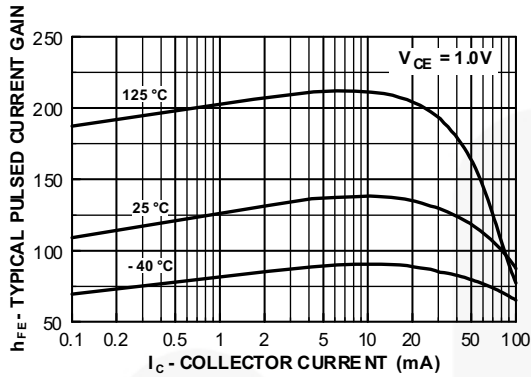


Figure 1. Typical Pulsed Current Gain vs. Collector Current

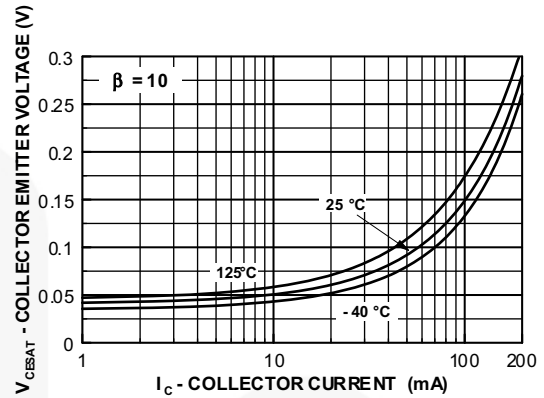


Figure 2. Collector-Emitter Saturation Voltage vs. Collector Current

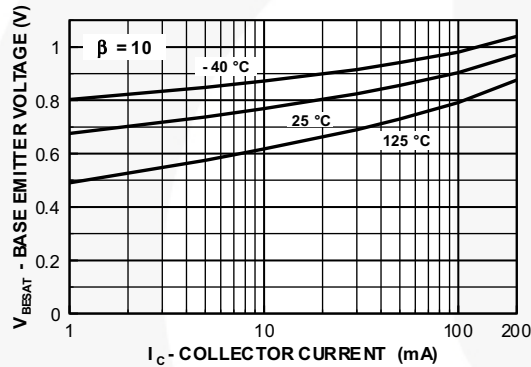


Figure 3. Base-Emitter Saturation Voltage vs. Collector Current

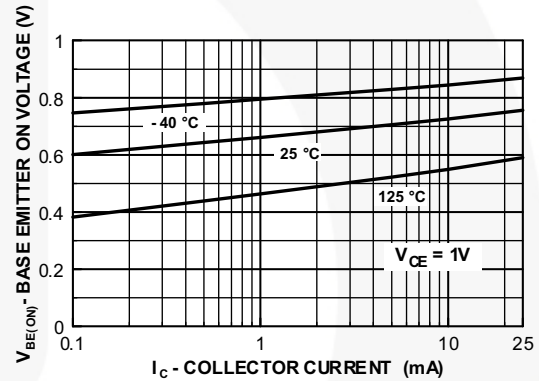


Figure 4. Base-Emitter On Voltage vs. Collector Current

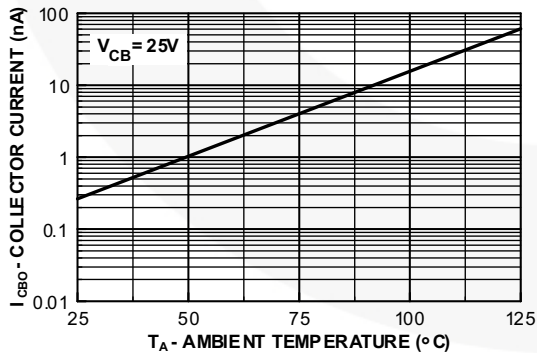


Figure 5. Collector Cut-Off Current vs. Ambient Temperature

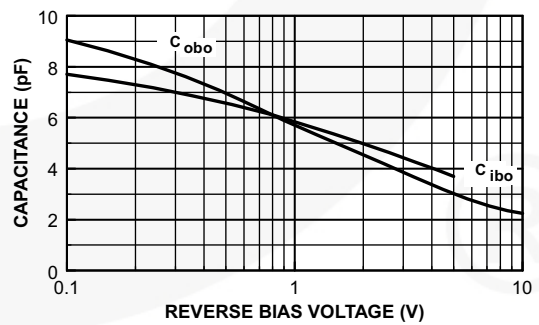
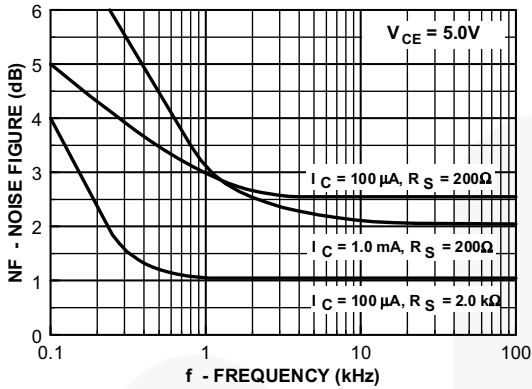
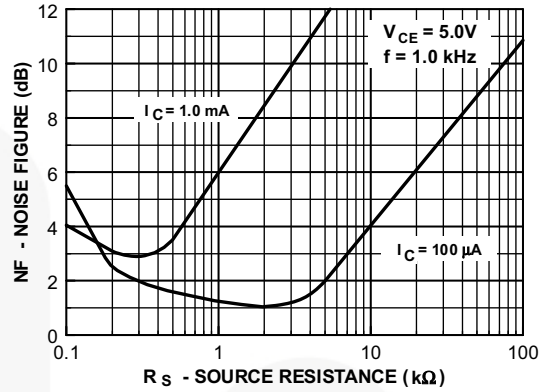


Figure 6. Common-Base Open Circuit Input and Output Capacitance vs. Reverse Bias Voltage

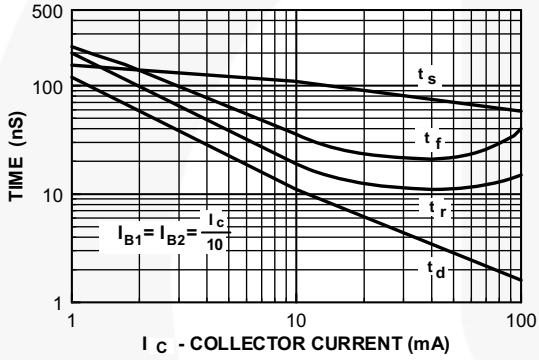
**Typical Performance Characteristics** (Continued)



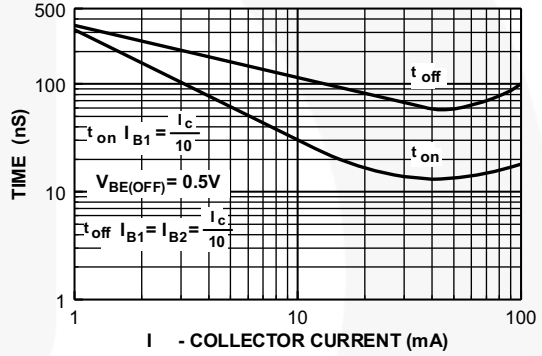
**Figure 7. Noise Figure vs. Frequency**



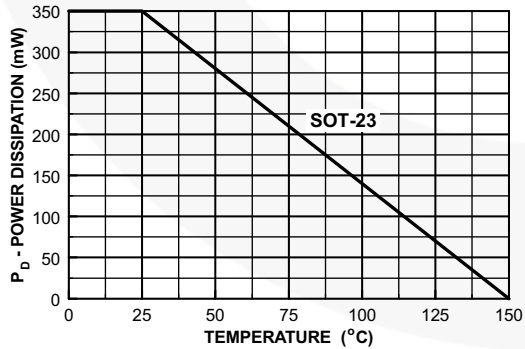
**Figure 8. Noise Figure vs. Source Resistance**



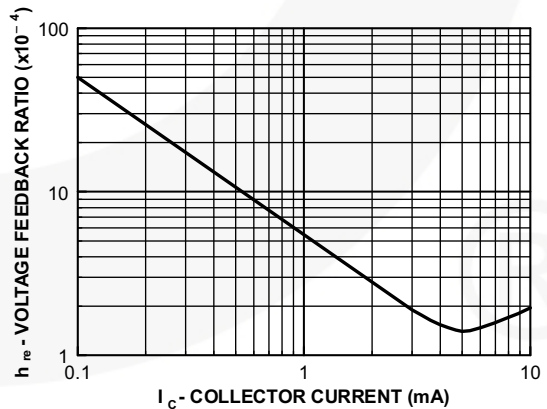
**Figure 9. Switching Times vs. Collector Current**



**Figure 10. Turn-On and Turn-Off Times vs. Collector Current**



**Figure 10. Power Dissipation vs. Ambient Temperature**



**Figure 11. Voltage Feedback Ratio**

Typical Performance Characteristics (Continued)

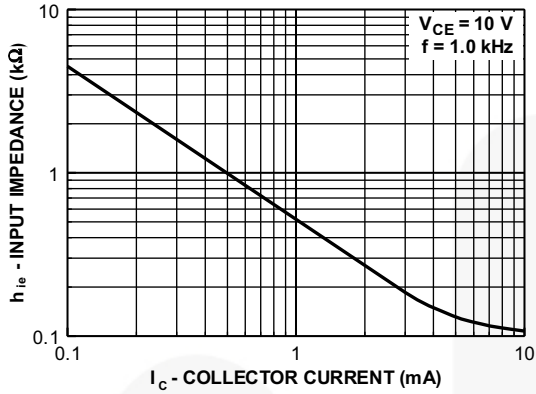


Figure 13. Input Impedance

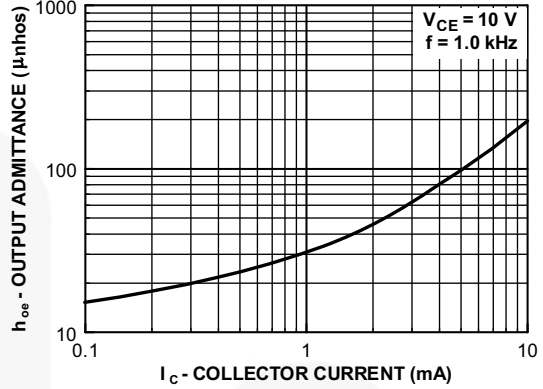


Figure 14. Output Admittance

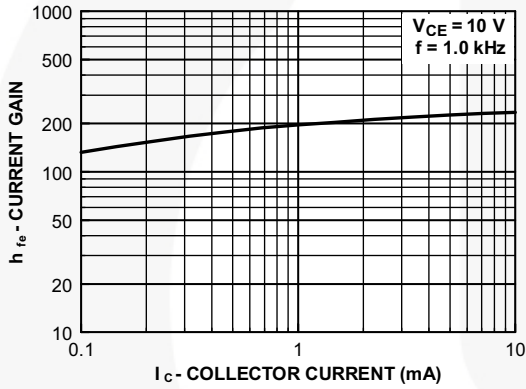


Figure 15. Current Gain

Physical Dimensions

SOT-23

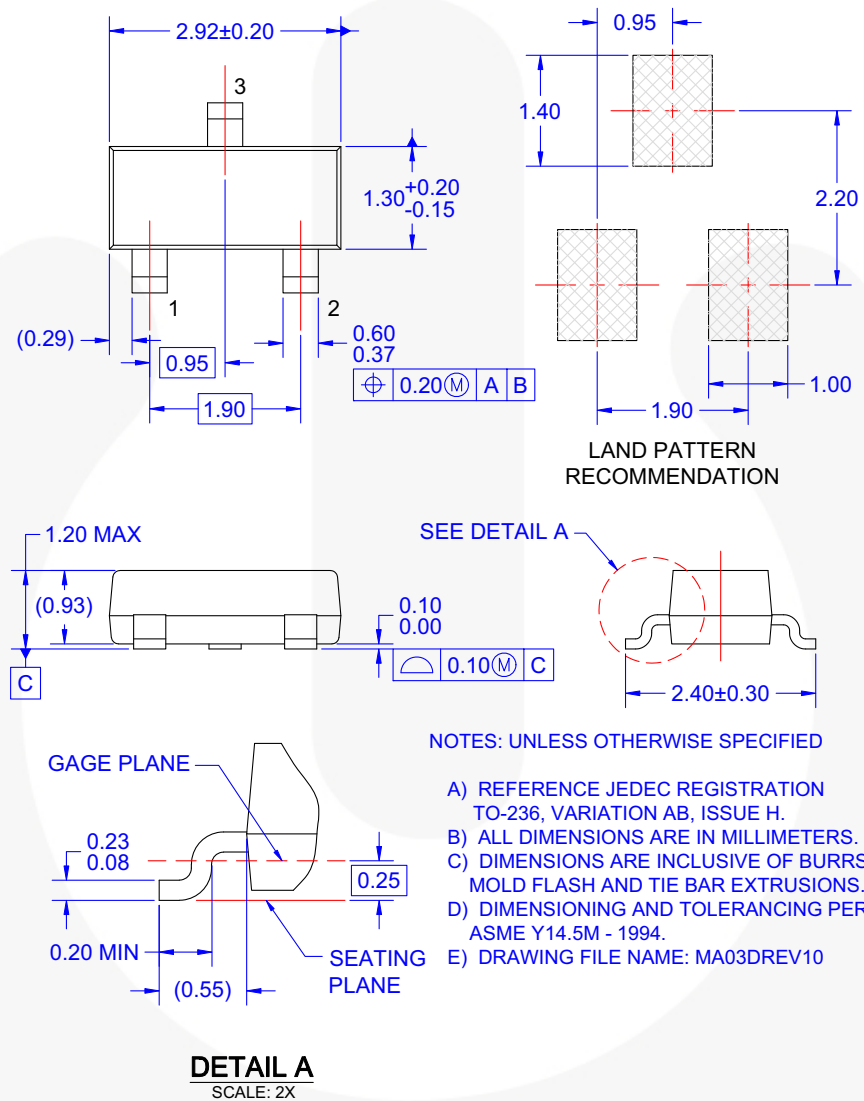


Figure 16. 3-LEAD, SOT23, JEDEC TO-236, LOW PROFILE (ACTIVE)

Package drawings are provided as a service to customers considering Fairchild components. Drawings may change in any manner without notice. Please note the revision and/or date on the drawing and contact a Fairchild Semiconductor representative to verify or obtain the most recent revision. Package specifications do not expand the terms of Fairchild's worldwide terms and conditions, specifically the warranty therein, which covers Fairchild products.






Always visit Fairchild Semiconductor's online packaging area for the most recent package drawings:  
<http://www.fairchildsemi.com/dwg/MA/MA03D.pdf>

For current tape and reel specifications, visit Fairchild Semiconductor's online packaging area:  
[http://www.fairchildsemi.com/packaging\\_dwg/PKG-MA03D.pdf](http://www.fairchildsemi.com/packaging_dwg/PKG-MA03D.pdf)



## TRADEMARKS

The following includes registered and unregistered trademarks and service marks, owned by Fairchild Semiconductor and/or its global subsidiaries, and is not intended to be an exhaustive list of all such trademarks.

AccuPower™	F-PFS™		
AX-CAP®*	FRFET®	PowerTrench®	TinyBoost®
BitSiC™	Global Power Resource <sup>SM</sup>	PowerXS™	TinyBuck®
Build it Now™	GreenBridge™	Programmable Active Droop™	TinyCalc™
CorePLUS™	Green FPS™	QFET®	TinyLogic®
CorePOWER™	Green FPS™ e-Series™	QS™	TINYOPTO™
CROSSVOL™	Gmax™	Quiet Series™	TinyPower™
CTL™	GTO™	RapidConfigure™	TinyPWM™
Current Transfer Logic™	IntelliMAX™		TinyWire™
DEUXPEED®	ISOPLANAR™	Saving our world, 1mW/W/kW at a time™	TranSiC™
Dual Cool™	Making Small Speakers Sound Louder and Better™	SignalWise™	TriFault Detect™
EcoSPARK®	MegaBuck™	SmartMax™	TRUECURRENT®*
EfficientMax™	MICROCOUPLER™	SMART START™	μSerDes™
ESBC™	MicroFET™	Solutions for Your Success™	
	MicroPak™	SPM®	UHC®
Fairchild®	MicroPak2™	STEALTH™	Ultra FRFET™
Fairchild Semiconductor®	MillerDrive™	SuperFET®	UniFET™
FACT Quiet Series™	MotionMax™	SuperSOT™-3	VXC™
FACT®	mWSaver®	SuperSOT™-6	VisualMax™
FAST®	OptoHiT™	SuperSOT™-8	VoltagePlus™
FastvCore™	OPTOLOGIC®	SupreMOS®	XS™
FETBench™	OPTOPLANAR®	SyncFET™	仙童™
FPS™		Sync-Lock™	

\* Trademarks of System General Corporation, used under license by Fairchild Semiconductor.

## DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION, OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS. THESE SPECIFICATIONS DO NOT EXPAND THE TERMS OF FAIRCHILD'S WORLDWIDE TERMS AND CONDITIONS, SPECIFICALLY THE WARRANTY THEREIN, WHICH COVERS THESE PRODUCTS.

## LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

## ANTI-COUNTERFEITING POLICY

Fairchild Semiconductor Corporation's Anti-Counterfeiting Policy. Fairchild's Anti-Counterfeiting Policy is also stated on our external website, [www.fairchildsemi.com](http://www.fairchildsemi.com), under Sales Support.

Counterfeiting of semiconductor parts is a growing problem in the industry. All manufacturers of semiconductor products are experiencing counterfeiting of their parts. Customers who inadvertently purchase counterfeit parts experience many problems such as loss of brand reputation, substandard performance, failed applications, and increased cost of production and manufacturing delays. Fairchild is taking strong measures to protect ourselves and our customers from the proliferation of counterfeit parts. Fairchild strongly encourages customers to purchase Fairchild parts either directly from Fairchild or from Authorized Fairchild Distributors who are listed by country on our web page cited above. Products customers buy either from Fairchild directly or from Authorized Fairchild Distributors are genuine parts, have full traceability, meet Fairchild's quality standards for handling and storage and provide access to Fairchild's full range of up-to-date technical and product information. Fairchild and our Authorized Distributors will stand behind all warranties and will appropriately address any warranty issues that may arise. Fairchild will not provide any warranty coverage or other assistance for parts bought from Unauthorized Sources. Fairchild is committed to combat this global problem and encourage our customers to do their part in stopping this practice by buying direct or from authorized distributors.

## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
No Identification Needed	Full Production	Datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve the design.
Obsolete	Not In Production	Datasheet contains specifications on a product that is discontinued by Fairchild Semiconductor. The datasheet is for reference information only.

Rev. I68



ON Semiconductor and  are trademarks of Semiconductor Components Industries, LLC dba ON Semiconductor or its subsidiaries in the United States and/or other countries. ON Semiconductor owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of ON Semiconductor's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). ON Semiconductor reserves the right to make changes without further notice to any products herein. ON Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does ON Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using ON Semiconductor products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by ON Semiconductor. "Typical" parameters which may be provided in ON Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. ON Semiconductor does not convey any license under its patent rights nor the rights of others. ON Semiconductor products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use ON Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold ON Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that ON Semiconductor was negligent regarding the design or manufacture of the part. ON Semiconductor is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

## PUBLICATION ORDERING INFORMATION

### LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor  
19521 E. 32nd Pkwy, Aurora, Colorado 80011 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910  
**Japan Customer Focus Center**  
Phone: 81-3-5817-1050

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)  
**Order Literature:** <http://www.onsemi.com/orderlit>  
For additional information, please contact your local  
Sales Representative