

PNP SILICON EPITAXIAL TRANSISTOR **2SA1836**

PNP SILICON EPITAXIAL TRANSISTOR

DESCRIPTION

The 2SA1836 is PNP silicon epitaxial transistor.

FEATURES

- High DC current gain: hFE2 = 200 TYP.
- High voltage: VCEO = -50 V
- Can be automatically mounted

★ ORDERING INFORMATION

PART NUMBER	PACKAGE
2SA1836	SC-75 (USM)

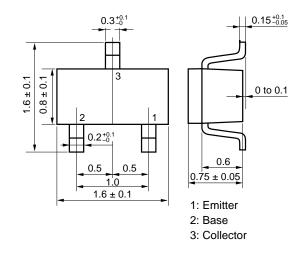
ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Collector to Base Voltage	Vсво	-60	V
Collector to Emitter Voltage	VCEO	-50	V
Emitter to Base Voltage	Vево	-5.0	V
Collector Current (DC)	IC(DC)	-100	mA
Collector Current (pulse) Note1	C(pulse)	-200	mA
Total Power Dissipation Note2	Pτ	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	Tstg	–55 to + 150	°C

Notes 1. $PW \le 10 \text{ ms}$, Duty Cycle $\le 50\%$

2. When mounted on ceramic substrate of 3.0 $\mbox{cm}^2 \mbox{ x } 0.64 \mbox{ mm}$





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ELECTRICAL CHARACTERISTICS (T_A = 25°C)

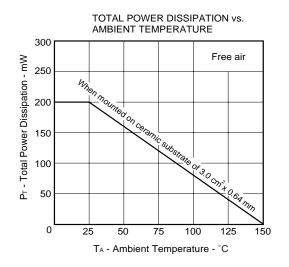
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	Ісво	$V_{CB} = -60 \text{ V}, I_E = 0$			-100	nA
Emitter Cut-off Current	Іево	V _{EB} = -5.0 V, I _C = 0			-100	nA
DC Current Gain ^{Note}	hfe1	Vce = -6.0 V, Ic = -0.1 mA	50			_
	hfe2	V _{CE} = -6.0 V, I _C = -1.0 mA	90	200	600	-
Base to Emitter Voltage ^{Note}	VBE	V _{CE} = -6.0 V, I _C = -1.0 mA		-0.62		V
Collector Saturation Voltage	VCE(sat)	Ic = −100 mA, I _B = −10 mA		-0.18	-0.30	V
Base Saturation Voltage Note	V _{BE(sat)}	Ic = -100 mA, I _B = -10 mA		-0.86	-1.00	V
Gain Bandwidth Product	f⊤	V _{CE} = -6.0 V, I _E = 10 mA	50	180		MHz
Output Capacitance	Cob	V _{CE} = -6.0 V, I _E = 0 mA, f = 1.0 MHz		4.5	6.0	pF

Note Pulsed: PW $\leq 350~\mu s,$ Duty Cycle $\leq 2\%$

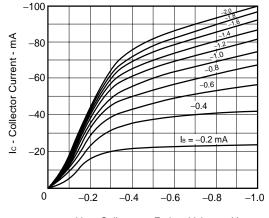
hfe CLASSFICATION

Marking	M4	M5	M6	M7
hfe2	90 to 180	135 to 270	200 to 400	300 to 600

TYPICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

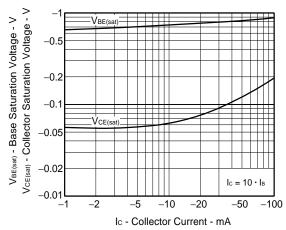


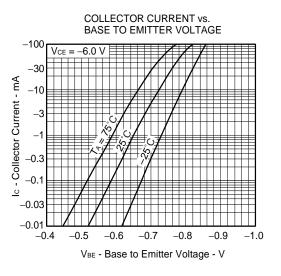




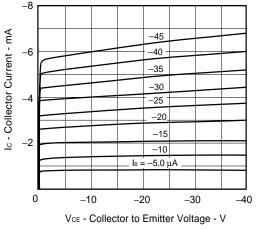
VCE - Collector to Emitter Voltage - V

COLLECTOR AND BASE SATURATION VOLTAGE vs. COLLECTOR CURRENT

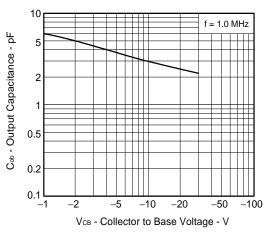




COLLECTOR CURRENT vs. COLLECTOR TO EMITTER VOLTAGE

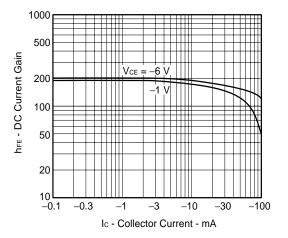


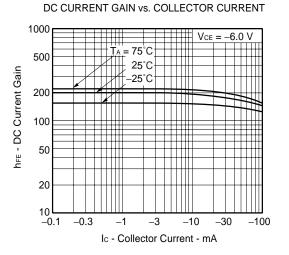
OUTPUT CAPACITANCE vs. REVERSE VOLTAGE



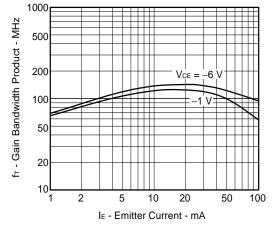


DC CURRENT GAIN vs. COLLECTOR CURRENT









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