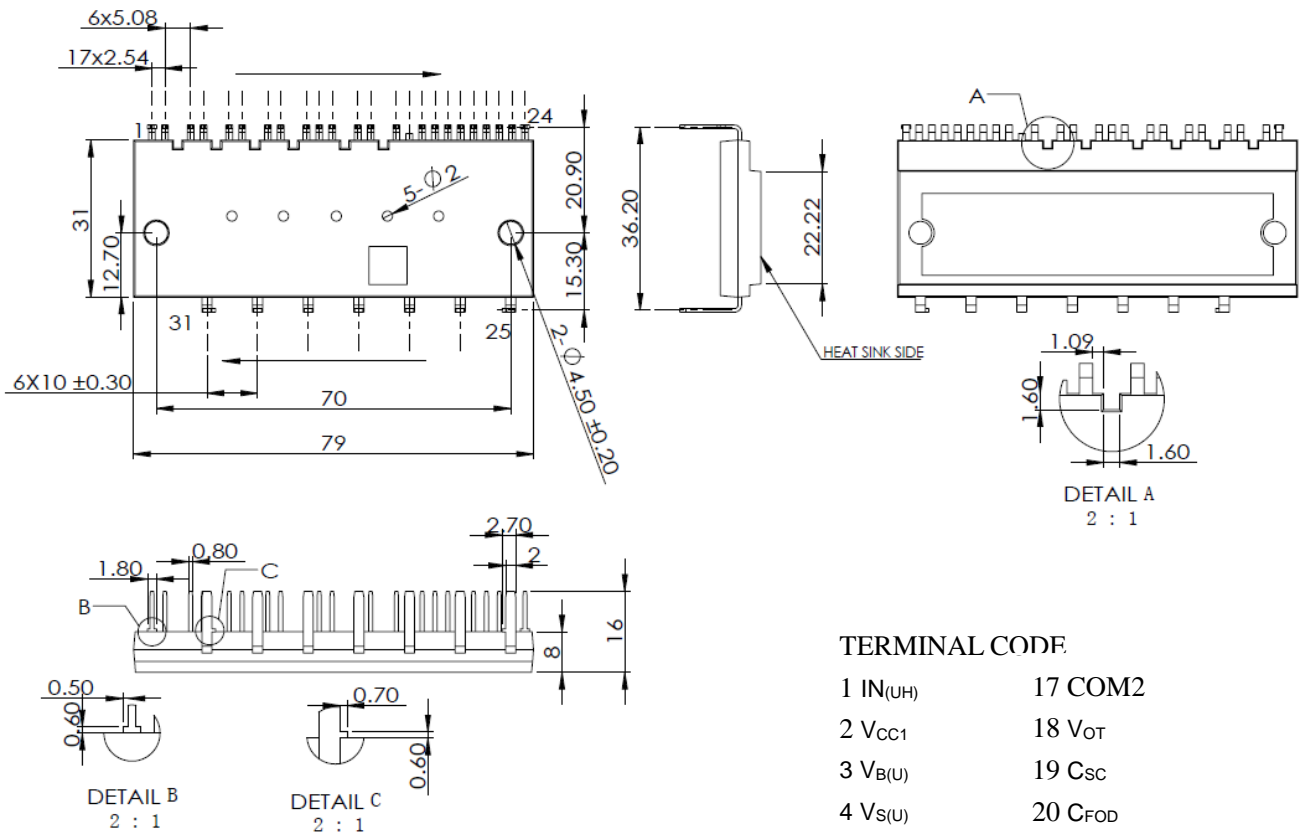


Compact - IPM ID50FFX60U2S

Features

- Adopt the latest trench IGBT technology to get a good overall loss trade-off.
- Open Emitter on N terminal for low cost current sensing application.
- Matched propagation delay and arm shooting through prevention.
- Built-in bootstrap diodes with current limiting resistor.
- Provided a fault signal (FO pin) and shut-off internal IGBT when suffer S.C. and under-voltage faulty event.
- RoHS compatible.
- UL 1557 Compliance.



TERMINAL CODE

1 IN(UH)	17 COM2
2 V _{CC1}	18 V _{OT}
3 V _{B(U)}	19 C _{SC}
4 V _{S(U)}	20 C _{FO} D
5 IN(VH)	21 V _{FO}
6 V _{CC1}	22 IN(U _L)
7 V _{B(V)}	23 IN(V _L)
8 V _{S(V)}	24 IN(W _L)
9 IN(WH)	25 P
10 V _{CC1}	26 U
11 COM1	27 V
12 V _{B(W)}	28 W
13 V _{S(W)}	29 N _U
14 V _{SC}	30 N _V
15 NC	31 N _W
16 V _{CC2}	

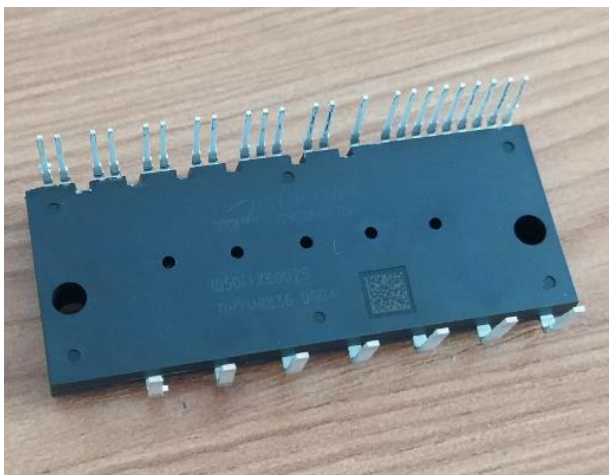


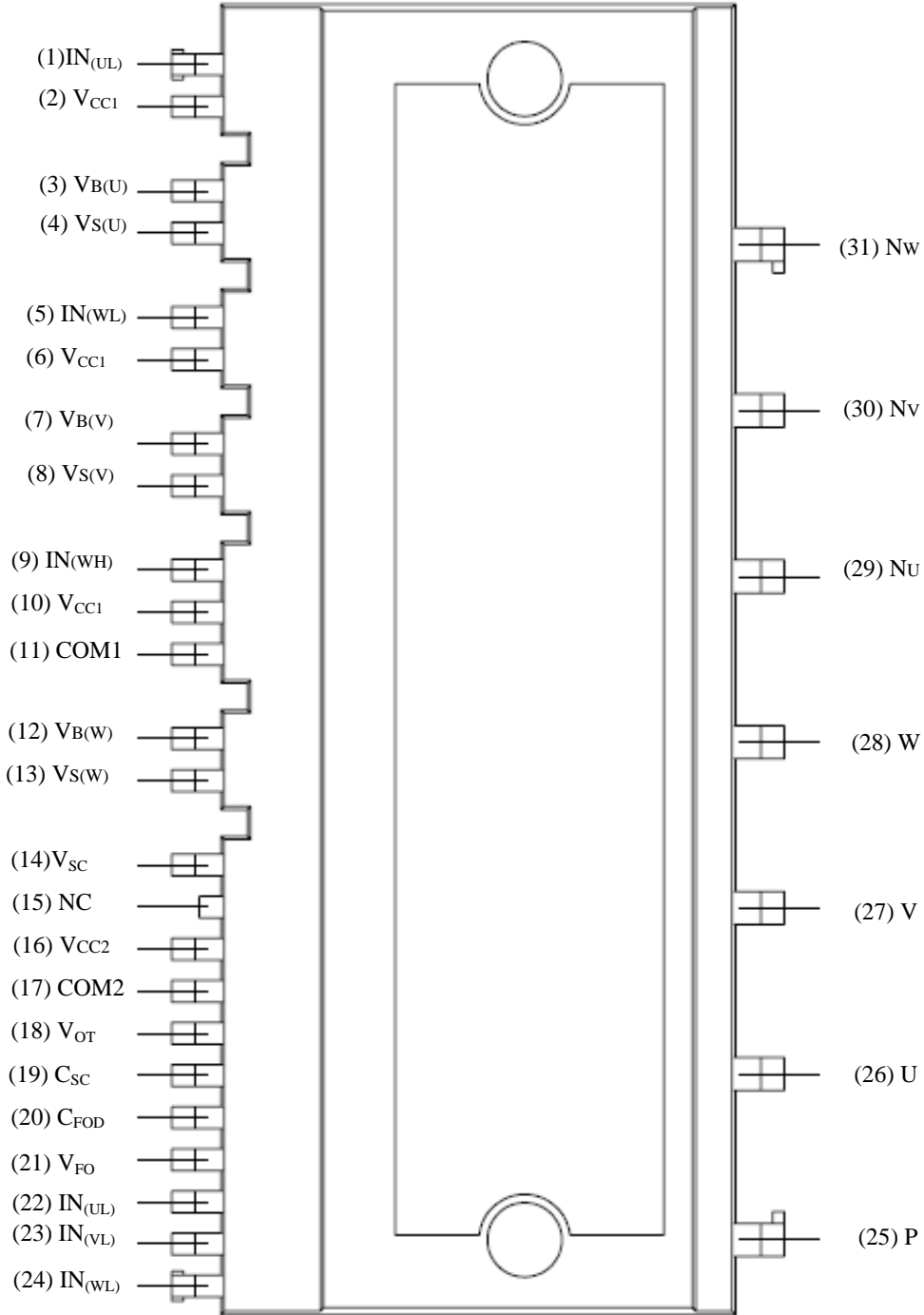
Table1: Pin Descriptions

No.	Symbol	Pin Description
1	IN _(UH)	Signal Input Terminal for High-side U Phase
2	V _{CC1}	Supply Voltage Terminal for Driver IC
3	V _{B(U)}	High - side Bias Voltage for U Phase IGBT Driving
4	V _{S(U)}	High - side Bias Voltage Ground for U Phase IGBT Driving
5	IN _(VH)	Signal Input Terminal for High-side V Phase
6	V _{CC1}	Supply Voltage Terminal for Driver IC
7	V _{B(V)}	High - side Bias Voltage for V Phase IGBT Driving
8	V _{S(V)}	High - side Bias Voltage Ground for V Phase IGBT Driving
9	IN _(WH)	Signal Input Terminal for High-side W Phase
10	V _{CC1}	Supply Voltage Terminal for Driver IC
11	COM1	Reference Voltage Terminal for Driver IC
12	V _{B(W)}	High - side Bias Voltage for W Phase IGBT Driving
13	V _{S(W)}	High - side Bias Voltage Ground for W Phase IGBT Driving
14	V _{SC}	Current sensing input voltage
15	NC	No connection
16	V _{CC2}	Supply Voltage Terminal for Driver IC
17	COM2	Reference Voltage Terminal for Driver IC
18	V _{OT}	Temperature Output Terminal
19	C _{SC}	Capacitor (Low-pass Filter) for Short-Current Detection Input
20	C _{FOD}	Capacitor for Fault Output Duration Time Selection
21	V _{FO}	Fault Output Terminal
22	IN _(UL)	Signal Input Terminal for Low-side U Phase
23	IN _(VL)	Signal Input Terminal for Low-side V Phase
24	IN _(WL)	Signal Input Terminal for Low-side W Phase
25	P	Positive DC – Link Input
26	U	Output Terminal for U Phase
27	V	Output Terminal for V Phase
28	W	Output Terminal for W Phase
29	N _U	Negative DC-Link Input Terminal for U Phase
30	N _V	Negative DC-Link Input Terminal for V Phase
31	N _W	Negative DC-Link Input Terminal for W Phase

(see figure 2, next page)

Pin Configuration

Bottom View



The IPM Internal Block Diagram

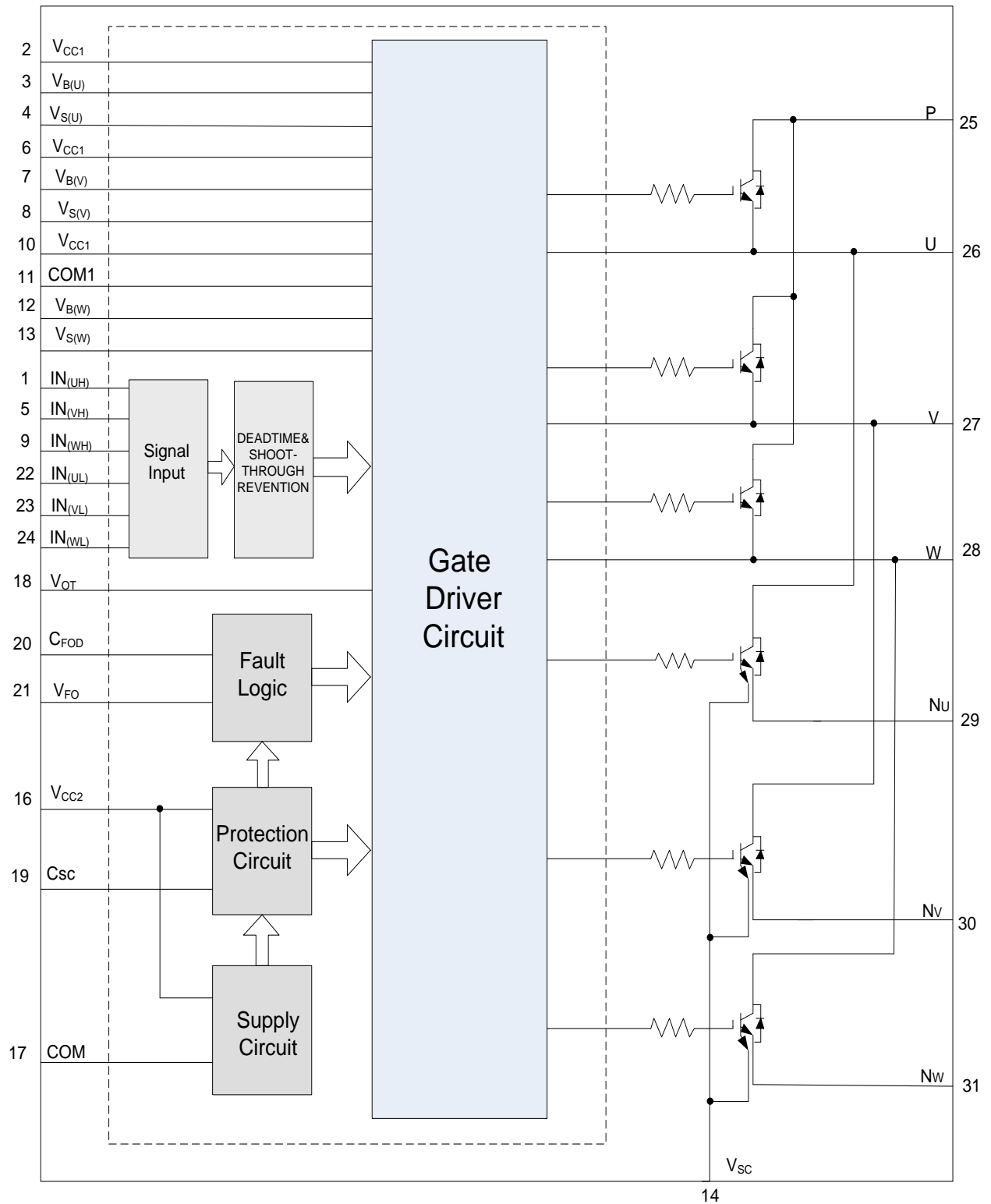


Figure 3. IPM Internal Block Diagram

Application:

- Short-circuit current protection AC 100~240Vrms class 3 phase output for low power motor control.

MAXIMUM RATINGS ($T_j = 25^\circ\text{C}$)

INVERTER PART

Item	Symbol	Min.	Max.	Unit
Between collector to emitter voltage	V_{CES} (IGBT)	-	600	V
Supply voltage P-N	V_{PN}	-	450	V
Supply voltage (surge) P-N	V_{PN} (surge)	-	500	V
Each IGBT collector current	$\pm I_C$ ($T_C = 25^\circ\text{C}$)	-	20	A
Each IGBT collector current (peak)	$\pm I_{CP}$ ($T_C = 25^\circ\text{C}$, pulse)	-	60	A
Collector dissipation	P_C ($T_C = 25^\circ\text{C}$, per one chip)	-	70	W
Junction temperature	T_j (Note 1)	-40	+150	$^\circ\text{C}$

Note 1: Power chip in IPM is qualified for 150°C operation. But overall junction temperature should be limited by $T_j \leq 125^\circ\text{C}$ ($@ T_c \leq 100^\circ\text{C}$) to fit long term reliability requirement.

CONTROL PART

Item	Symbol	Min.	Max.	Unit
Driver IC supply voltage	V_{CC}	-0.3	20	V
P - side floating supply voltage	$V_{B(u)S(u), B(V)S(V), B(W)S(W)}$	-0.3	20	V
Current sensing input voltage	V_{SC}	-0.3	$V_{CC}+0.3$	V
Logic input voltage	$IN_{(UH)}, IN_{(VH)}, IN_{(WH)},$ $IN_{(UL)}, IN_{(VL)}, IN_{(WL)}$	-0.3	$V_{CC}+0.3$	V
Fault output voltage	V_{FO}	-0.3	$V_{CC}+0.3$	V
Fault output current	I_{FO}	-	10	mA

TOTAL SYSTEM

Item	Symbol	Min.	Max.	Unit
Module case operating temperature	T_C (Note 2)	-20	+100	$^\circ\text{C}$
Storage temperature	T_{stg}	-40	+125	$^\circ\text{C}$
Isolation voltage (60Hz Sinusoidal, AC 1 minute, pins to heat-sink plate)	V_{iso}	-	2500	Vrms

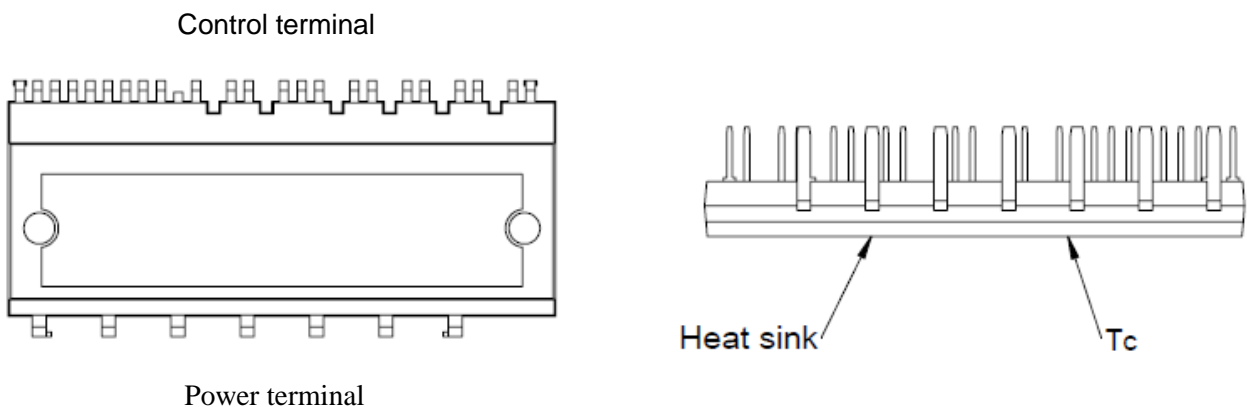


Figure 4. T_c Measurement Point

THERMAL RESISTANCE

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Junction to case thermal resistance	$R_{th(j-c)Q}$	IGBT part (1/6)	-	0.78		°C/W
	$R_{th(j-c)F}$	FWD part (1/6)	-	1.4		

ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$)

INVERTER PART

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Collector-emitter saturation voltage	$V_{CE(sat)}$	$V_{CC} = V_{B(U)S(U), B(V)S(V), B(W)S(W)} = 15\text{V}$, $I_C = 15$, $V_{SC} = 0\text{V}$		1.5		V
FWD forward voltage drop	V_F	$T_j = 25^\circ\text{C}$, $-I_C = 20\text{A}$		1.5		V
Switching times (Fig. 5)	T_{on}	$V_D = 300\text{V}$, $V_{CC} = V_{B(U)S(U), B(V)S(V), B(W)S(W)} = 15\text{V}$, $I_C = 20\text{A}$, $T_j = 25^\circ\text{C}$, $V_{IN} = 5\text{V} \leftrightarrow 0\text{V}$, $V_{SC} = 0\text{V}$, Inductive Load		1.5		μS
	T_r			0.3		
	$T_{c(on)}$			0.4		
	T_{off}			3		
	T_f			0.15		
	$T_{c(off)}$			0.4		
	T_{rr}			0.25		
Collector-emitter cut-off current	I_{CES}	$V_{CE} = V_{CES}$			500	μA

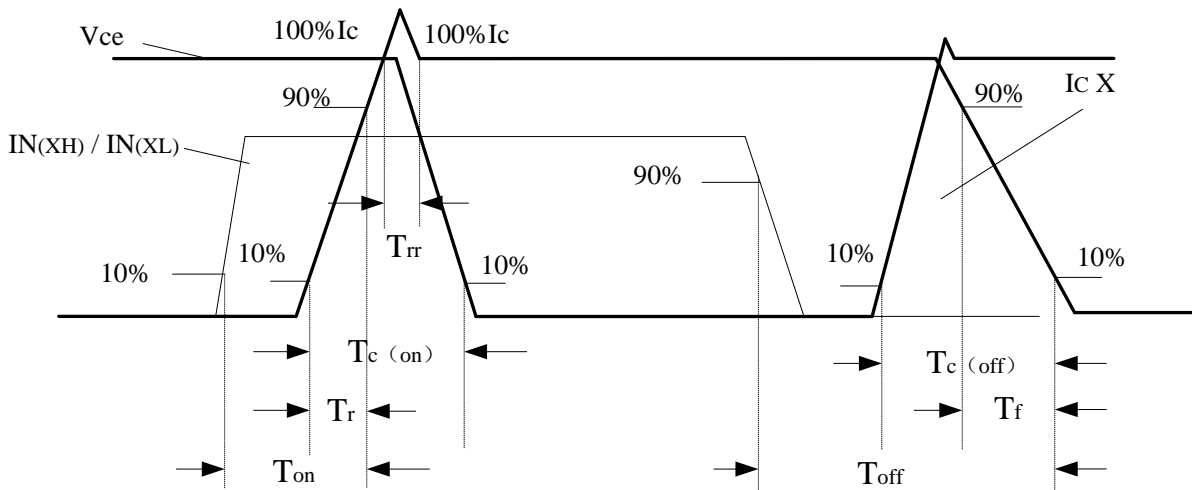


Figure 5. Switching Time Definition

CONTROL PART ($T_j = 25^\circ\text{C}$)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
$\text{IN}_{(\text{UH}, \text{VH}, \text{WH})}, \text{IN}_{(\text{UL}, \text{VL}, \text{WL})}$ ON threshold voltage	$V_{\text{th}(\text{on})}$		1.4	1.7	2.0	V
$\text{IN}_{(\text{UH}, \text{VH}, \text{WH})}, \text{IN}_{(\text{UL}, \text{VL}, \text{WL})}$ OFF threshold voltage	$V_{\text{th}(\text{off})}$		2.2	2.5	2.8	V
$\text{IN}_{(\text{UH}, \text{VH}, \text{WH})}$ input bias current	$I_{\text{IN}(\text{UH}, \text{VH}, \text{WH})(\text{HI})}$	$V_{\text{IN}(\text{UH}, \text{VH}, \text{WH})} = 5\text{V}$	-	-	220	μA
	$I_{\text{IN}(\text{UH}, \text{VH}, \text{WH})(\text{LO})}$	$V_{\text{IN}(\text{UH}, \text{VH}, \text{WH})} = 0\text{V}$	-	-	300	
$\text{IN}_{(\text{UL}, \text{VL}, \text{WL})}$ input bias current	$I_{\text{IN}(\text{UL}, \text{VL}, \text{WL})(\text{HI})}$	$V_{\text{IN}(\text{UL}, \text{VL}, \text{WL})} = 5\text{V}$	-	-	220	μA
	$I_{\text{IN}(\text{UL}, \text{VL}, \text{WL})(\text{LO})}$	$V_{\text{IN}(\text{UL}, \text{VL}, \text{WL})} = 0\text{V}$	-	-	300	
Short circuit trip level	Isc	$R_s=40.2$, not connecting external shut resistor		90		A
Driver IC supply voltage	V_{CC}		13.5	15.0	16.5	V
P - side floating supply voltage	$V_{\text{B}(\text{U})\text{S}(\text{U})}, \text{B}(\text{V})\text{S}(\text{V}), \text{B}(\text{W})\text{S}(\text{W})$		13.5	15.0	16.5	V
V_{CC} terminal input current	I_{c}		-	-	2.3	mA
Fault output voltage	V_{FOH}	$V_{\text{sc}}=0\text{V}$ (Note 2)	4.9	-	-	V
	V_{FOL}	$V_{\text{sc}}=1\text{V}$	-	-	200	mV
Short circuit trip level	$V_{\text{SC}(\text{ref})}$	$V_{\text{CC}}=15\text{V}, T_j=25^\circ\text{C}$	0.41	0.46	0.51	V
Fault output pulse width	t_{FOD}	(Note 3)	100		-	μs
Supply circuit under voltage protection	UVT_{VCC}	Trip level	8.6	9.4	10.2	V
	UVR_{VCC}	Reset level	9.6	10.4	11.2	V
	UVH	Hysteresis	-	1.0	-	V
$\text{IN}_{(\text{UL}, \text{VL}, \text{WL})}$ Input filter time	$t_{\text{IN}, \text{FIL}}$	$V_{\text{IN}} = 0 \ \& \ 5\text{V}$ (Note 4)	100	200	-	ns

Note 2: V_{FO} output is open collector type, so this signal line should be pulled up to the +5V power supply with approximately 4.7K Ω

Note 3: Fault output pulse width is filter capacitor of S.C. depended.

Note 4: For high side PWM, $\text{IN}_{(\text{UH}, \text{VH}, \text{WH})}$ pulse width must be $\geq 1 \ \mu\text{s}$.

Input Filter Function

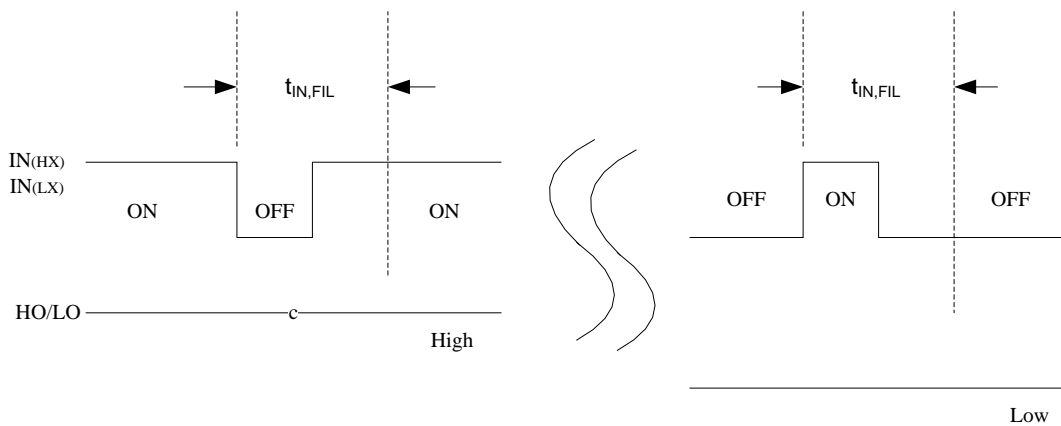


Figure 6. Input Filter Function

RECOMMENDED OPERATION CONDITIONS

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
DC – Link Supply voltage	V_D	Applied between P-N	0	400	450	V
Driver IC supply voltage	V_{CC}	Applied between V_{CC} - COM	13.5	15.0	16.5	V
P - side floating supply voltage	V_{BS}	Applied between $V_{B(u. v. w)} - V_{S(u. v. w)}$	13.5	15.0	16.5	V
Input ON threshold voltage	$V_{sc(ON)}$	Applied between $IN_{(UH. VH. WH)} - COM$ and $IN_{(UL. VL. WL)} - COM$	0 ~ 0.65			
Input OFF threshold voltage	$V_{sc(OFF)}$		4.0 ~ 5.5			
Supply voltage ripple	$\Delta V_D, \Delta V_{DB}$		-1	-	1	V/ μ s
Arm shoot-through blocking time	t_{dead}		2	-	-	μ s
PWM input frequency	f_{PWM}	$T_C \leq 100^\circ C, T_J \leq 125^\circ C$	-	15	-	kHz

MECHANICAL CHARACTERISTICS AND RATINGS

Item	Condition		Min.	Typ.	Max.	Unit
Mounting torque	Mounting screw: M3	Recommended 0.65N•m	0.60	0.65	0.70	N•m
Weight		--	-	69	-	g
Heat-sink flatness		--	-50	-	100	μ m

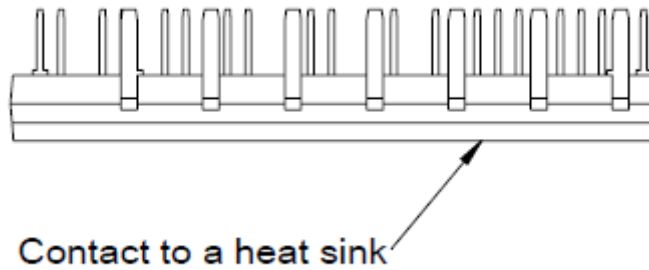


Figure 7. Measurement Location of Heat-sink Flatness

Input/Output Timing Diagram

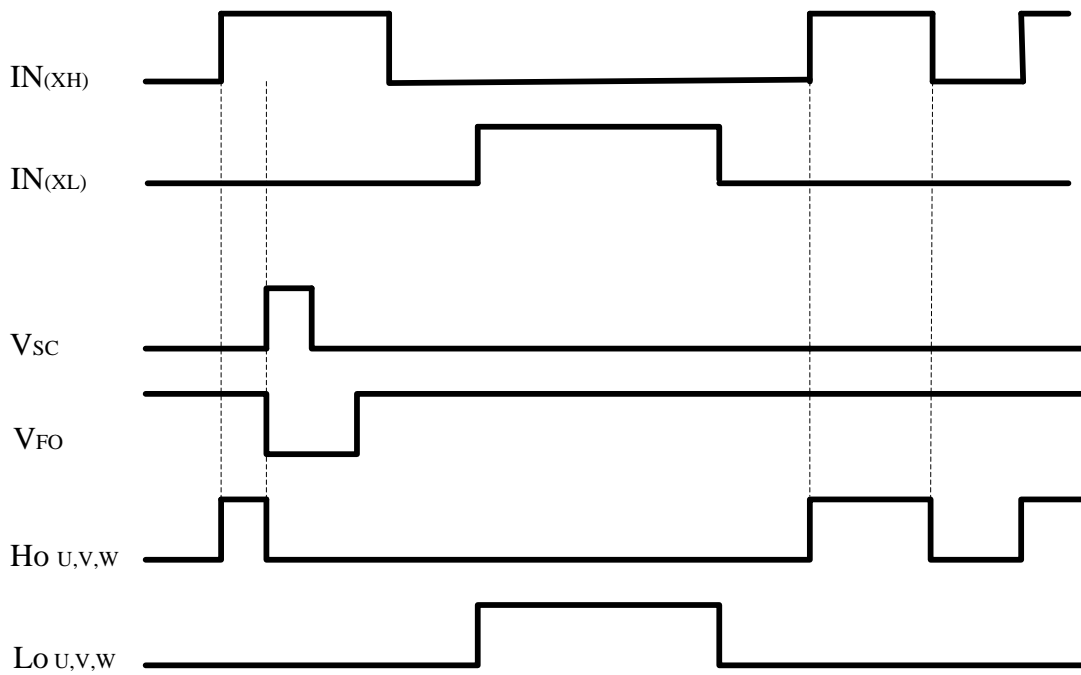


Figure 8. Input/Output Timing Diagram

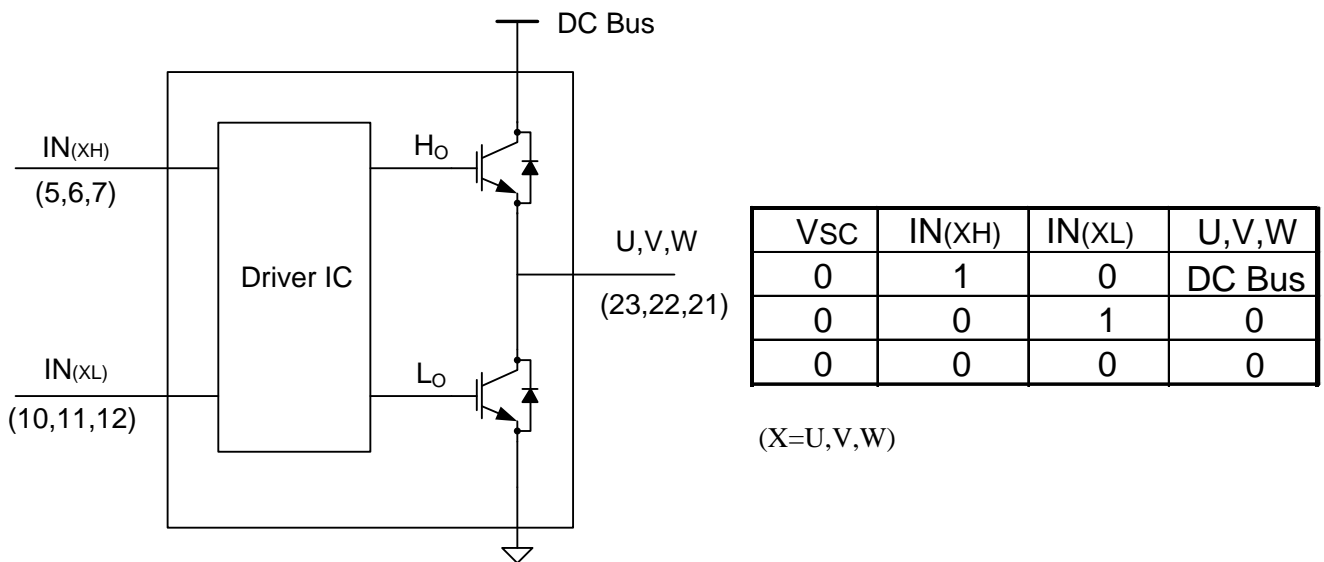


Figure 9. Input/Output Signal Circuit

IPM Short-Circuit Protection Function

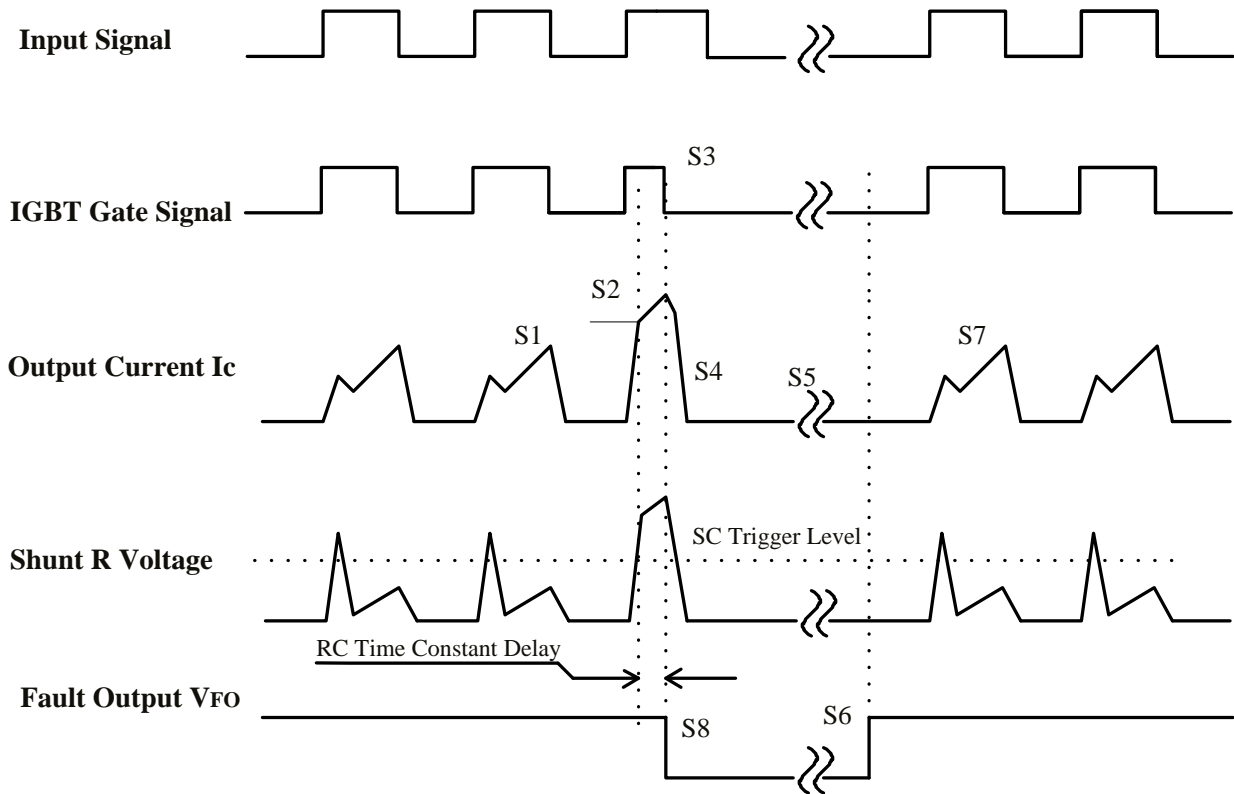


Figure 10. Timing Chart of SC Operation

- S1. The IGBT's are controlled by input PWM signal.(Normal operation)
- S2. Short circuit event occur and reach the limited level. (SC protection is trigger).
- S3. IGBT gate driving signal is disabled.
- S4. Current is cut off caused by IGBT turns OFF.
- S5. Disabled state
- S6. Fault level is kept on low level during the fault output activation
- S7. IGBT returns for normal operation when fault state is reset
- S8. Fault output starting once SC protection is trigger.

IPM Under-Voltage Protection Function

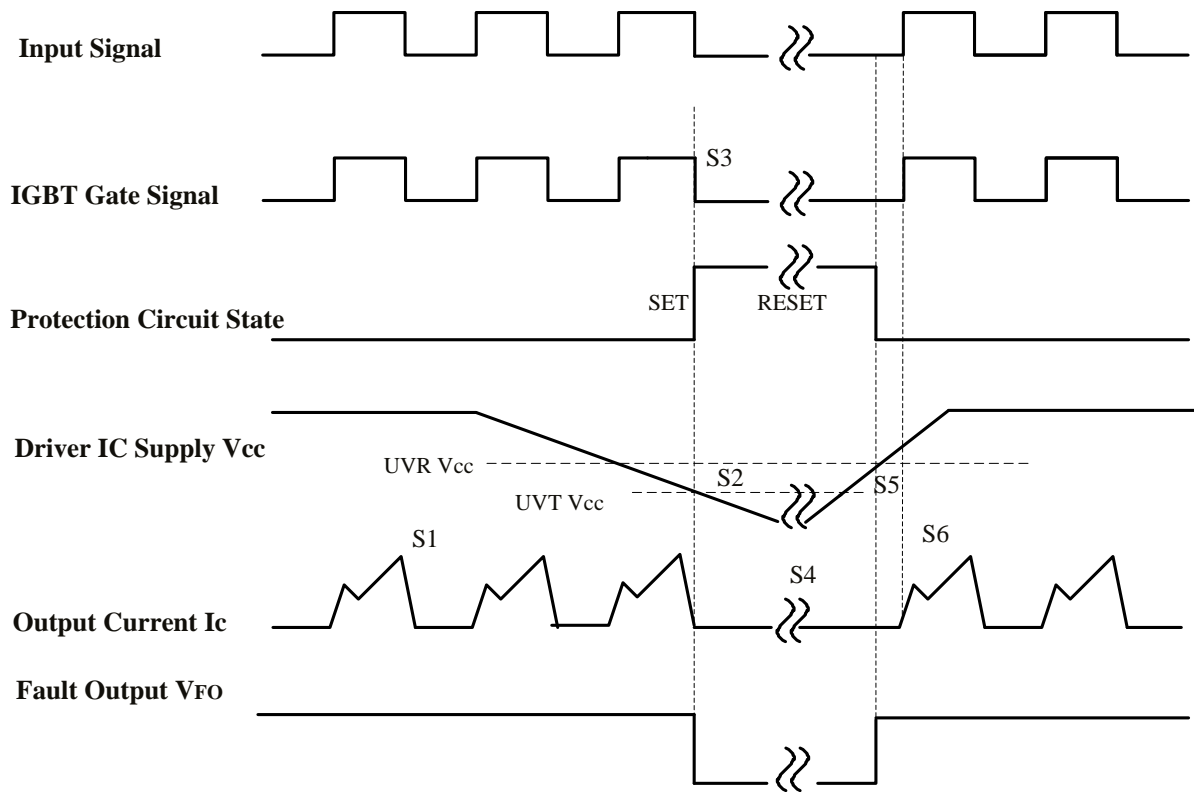


Figure 11. Timing Chart of Under-Voltage Operation

- S1. The IGBT's are controlled by input PWM signal.(Normal operation)
- S2. Under-voltage protection is trigger
- S3. IGBT driving signals are disabled when fault condition occur
- S4. Fault state and the period will be able to control by external capacitor value.
- S5. Under-voltage event is recovered
- S6. IGBT returns for normal operation when fault state is reset

Direct Input (without Photo Coupler) Interface Example

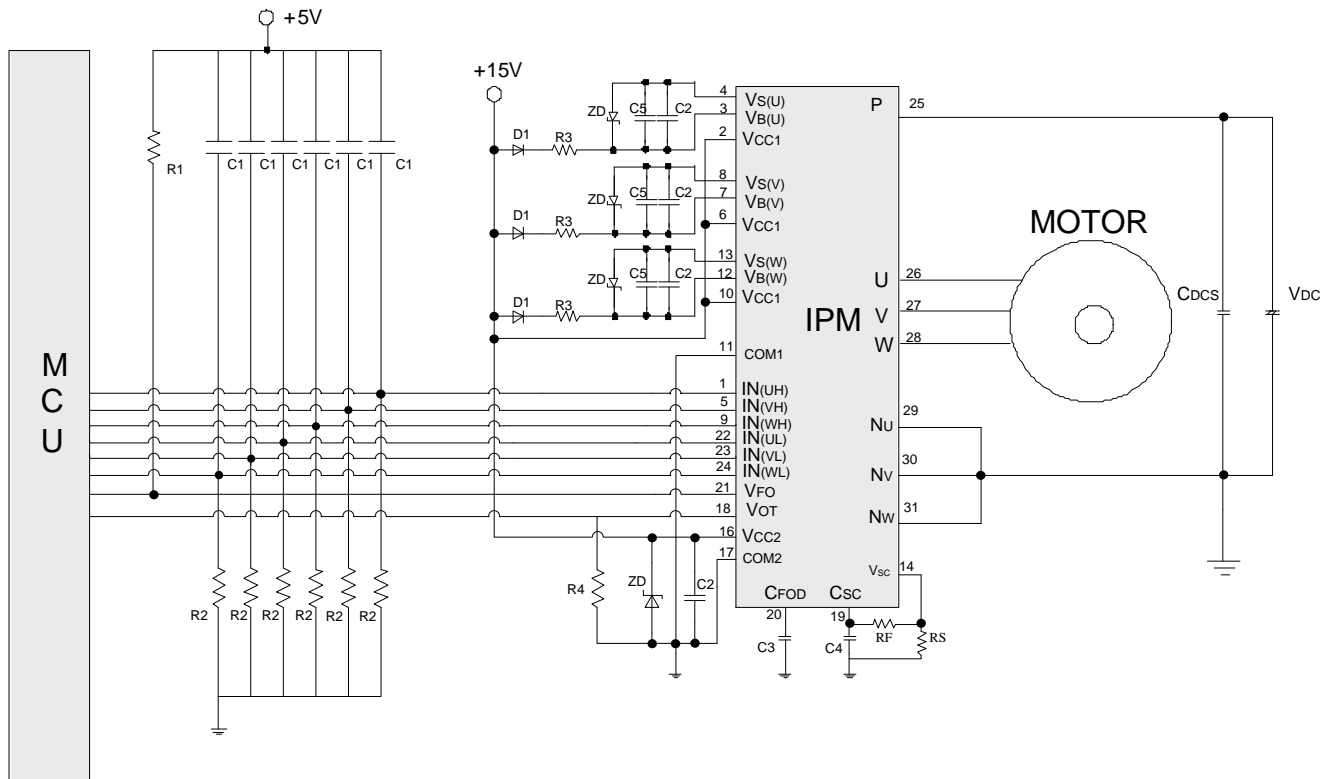


Figure 12. Typical Application Circuit Interface Example without Photo-Coupler

Design reference:

1. R1 : 100Ω
2. R2 : 4.7~10KΩ (VFO output is open collector so to have a pull high resistor is needed.)
3. C1 : 10 ~ 100μF (Electrolytic, low impedance)
4. C2 : 100 ~ 1000pF (Ceramic) (The capacitor could filter the noise, but should be careful to the dead time)
5. C4 : 22nF ~ 44nF (Ceramic)
6. C8 : 220μF (Electrolytic, low impedance)
7. D1 : Zener diode (It is recommended to insert a Zener diode to prevent gate lifting and surge destruction)
8. Only connect either pin 9 or pin 16 to ground, do not connect both together to form the ground loop internally.

Current Sense Scheme

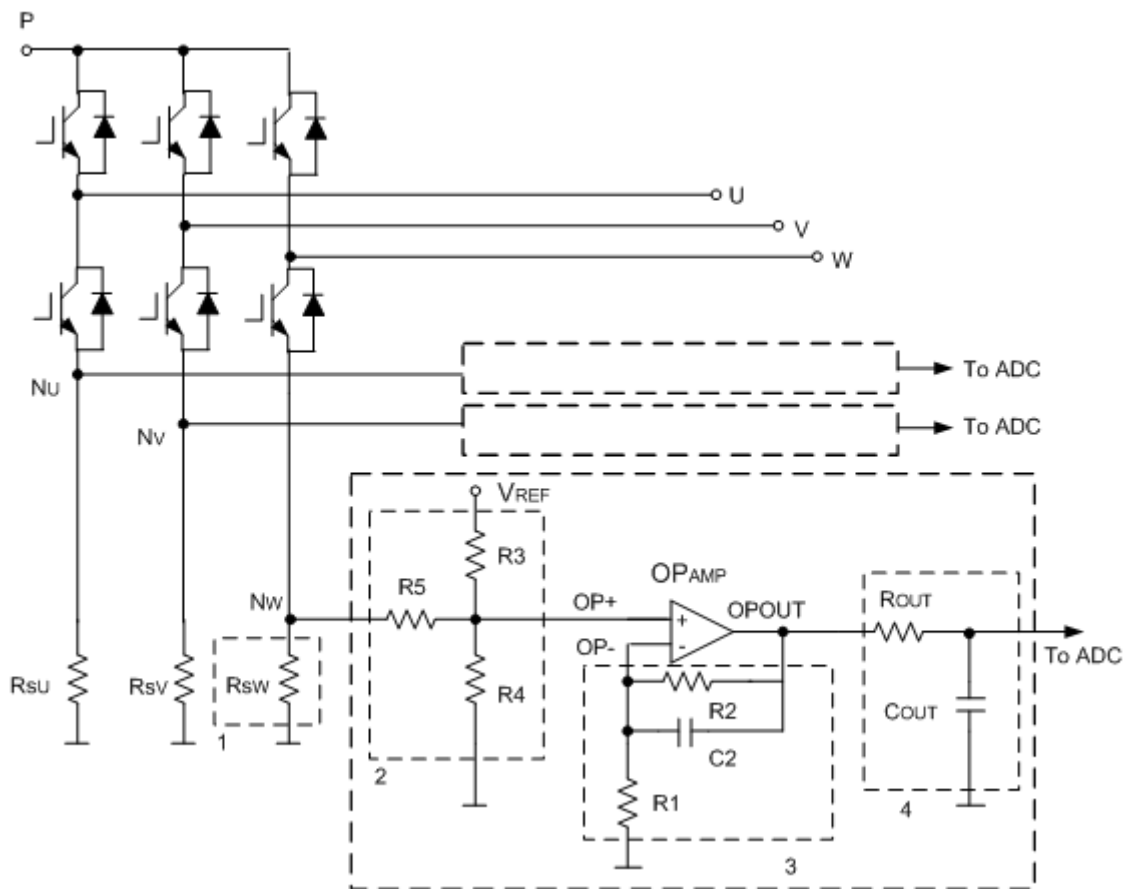


Figure 13. Current Sense Scheme

Description :

- 1、 Half-bridge current sensing
- 2、 Voltage shifting of the V_{sense}
- 3、 Voltage gain and filtering
- 4、 Capacitor required by the ADC for sampling purpose

R_{OUT} resistor is usually required in order to make the OP_{AMP} stable when the C_{OUT} capacitance increases

Design Reference :

1. R_1 : 1.0 K Ω
2. R_2 : 5.6 K Ω
3. R_3 : 4.7 K Ω
4. R_4 : 910 Ω
5. R_5 : 910 Ω
6. R_{OUT} : 1.0 K Ω
7. C_2 : 10pF (Ceramic)

Precautions on Electrostatic Electricity

- (1) Operators must wear anti-static clothing and conductive shoes (or a leg or heel strap).
- (2) Operators must wear a wrist strap grounded to earth via a resistor of about 1 M Ω .
- (3) Soldering irons must be grounded from iron tip to earth, and must be used only at low voltages.
- (4) If the tweezers you use are likely to touch the device terminals, use anti-static tweezers and in particular avoid metallic tweezers. If a charged device touches a low-resistance tool, rapid discharge can occur. When using vacuum tweezers, attach a conductive chucking pat to the tip, and connect it to a dedicated ground used especially for anti-static purposes (suggested resistance value: 10^4 to $10^8\Omega$).
- (5) Do not place devices or their containers near sources of strong electrical fields (such as above a CRT).
- (6) When storing printed circuit boards which have devices mounted on them, use a board container or bag that's protected against static charge. To avoid the occurrence of static charge or discharge due to friction, keep the boards separate from one other and do not stack them directly on top of one another.
- (7) Ensure, if possible, that any articles (such as clipboards) which are brought to any location where the level of static electricity must be closely controlled are constructed of anti-static materials.
- (8) In cases where the human body comes into direct contact with a device, be sure to wear anti-static finger covers or gloves (suggested resistance value: $10^8\Omega$ or less).
- (9) Equipment safety covers installed near devices should have resistance ratings of $10^9\Omega$ or less.
- (10) If a wrist strap cannot be used for some reason, and there is a possibility of imparting friction to devices, use an ionizer.

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