



**Features**

- Ideal for printed circuit boards
- Applicable for automotive insertion
- High surge current capability
- Solder Dip 260 , 40seconds

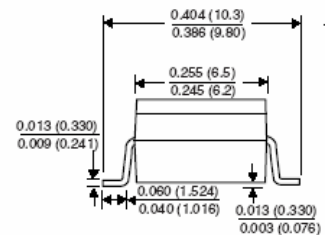
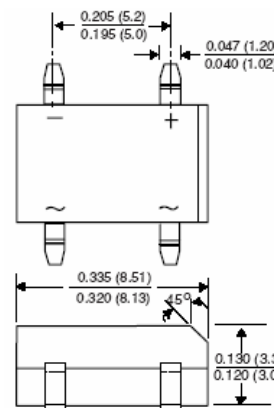
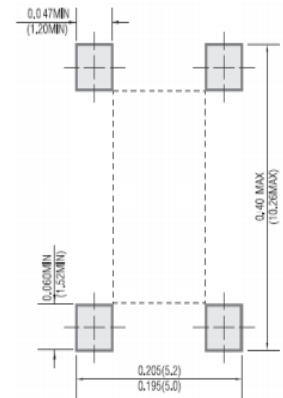
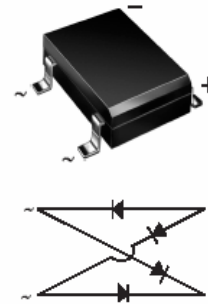
**Mechanical Data**

- Case:DFS
- Epoxy meets UL-94V-0 Flammability rating
- Terminals:Matte tin plated (E3 Suffix) leads, solderable per J-STD-002B and JESD22-B102D
- Polarity:As marked on body

**Typical Applications**

- General purpose use in ac-to-dc bridge full wave rectification for SMPS, Lighting Ballaster, Adapter, Battery Charger, Home Appliances, Office Equipment, and Telecommunication applications.

Case Style DFS



**Maximum Ratings & Electrical Characteristics** Ratings at 25

ambient temperature unless otherwise specified.

Parameter	Symbol	DB101S	DB102S	DB103S	DB104S	DB105S	DB106S	DB107S	Unit
Maximum repetitive peak reverse voltage	$V_{RRM}$	50	100	200	400	600	800	1000	V
Maximum RMS voltage	$V_{RMS}$	35	70	140	280	420	560	700	V
Maximum DC blocking voltage	$V_{DC}$	50	100	200	400	600	800	1000	V
Maximum Average forward output rectified current at TA=40	$I_{F(AV)}$	1.0							A
Peak forward surge current single sine-wave superimposed on rated load (JEDEC Method)	$I_{FSM}$	30							A
Rating for fusig (t<8.3ms)	$I^2t$	3.7							A <sup>2</sup> sec
Maximum instantaneous forward voltage drop per leg at 1.0A	VF	1.10							V
Maximum DC reverse current at TA=25 rated DC blocking voltage per leg TA=125	IR	5							μA
Typical junction capacitance per eiement at 4.0V, 1MHZ	Cj	25							pF
Typical thermal resistance per leg (Note 1)	$R_{\theta JA}$ $R_{\theta JL}$	40 15							/W
Operating junction temperature range	TJ	-55 to +150							
Storage temperature range	TSTG	-55 to +150							

**Notes:** 1. Device mounted P.C.B with 0.47x0.47"(12mmx12mm) Copper Pads.  
2. JEDEC registered values

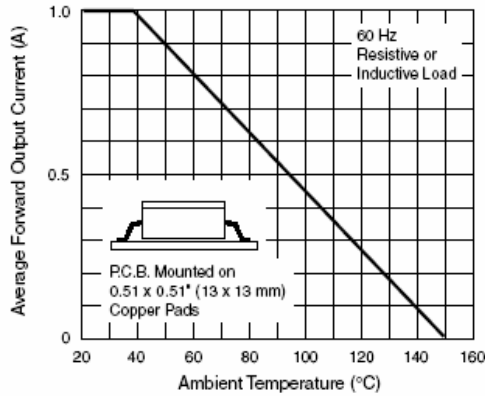
**Ratings and Characteristics Curves** ( $T_A = 25$  unless otherwise noted)


Figure 1. Derating Curve Output Rectified Current

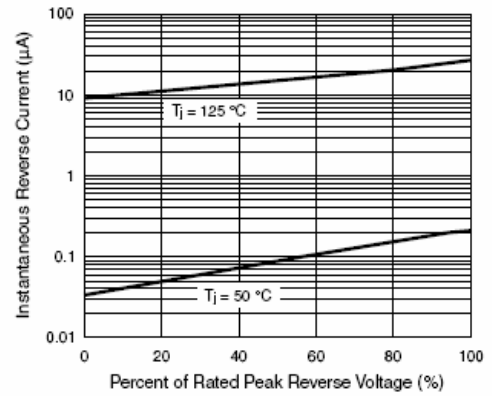


Figure 4. Typical Reverse Leakage Characteristics Per Diode

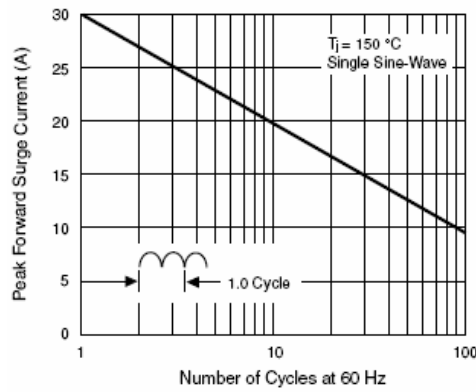


Figure 2. Maximum Non-Repetitive Peak Forward Surge Current Per Diode

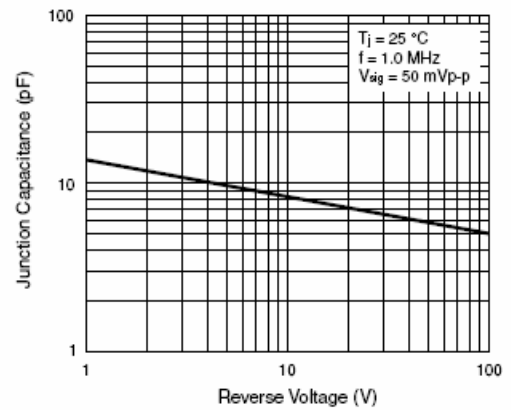


Figure 5. Typical Junction Capacitance Per Diode

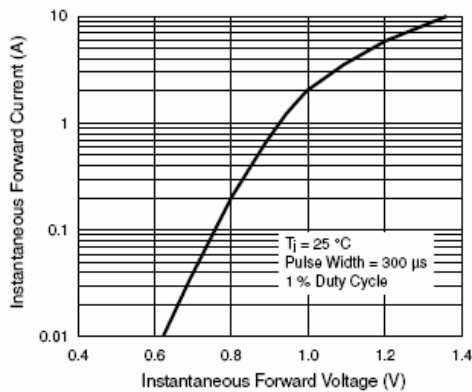


Figure 3. Typical Forward Characteristics Per Diode

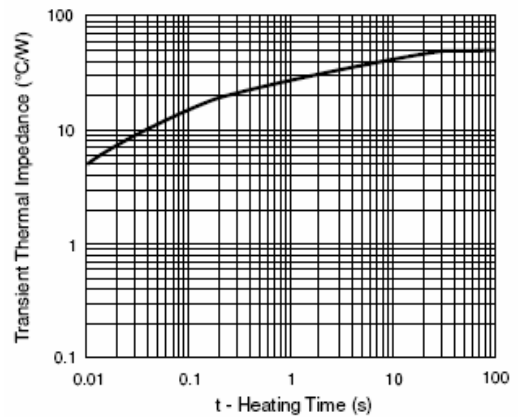
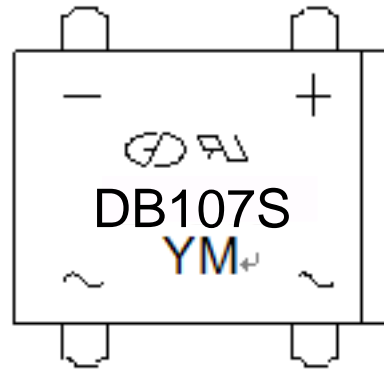


Figure 6. Typical Transient Thermal Impedance

Marking



DATE CODE

Year	2009	2010	2011	2012	2013	2014	2015	2016	2017	2018	2019	2020
Code	9	A	B	C	D	E	F	G	H	J	K	0
Month	1	2	3	4	5	6	7	8	9	10	11	12
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