

# C-MOS QUAD SPST ANALOG SWITCH

#### ■ GENERAL DESCRIPTION

The NJU7301 is a quad break-before-make SPST analog switch protected up to 44V operating voltage.

Each switch is controlled by TTL or C-MOS compatible input.

#### ■ PACKAGE OUTLINE



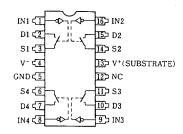
NJU7301D

NJU7301M

#### **■ FEATURES**

- High Break Down Voltage -- 44V
- Package Outline
   DIP/DMP 16
- C-MOS Technology

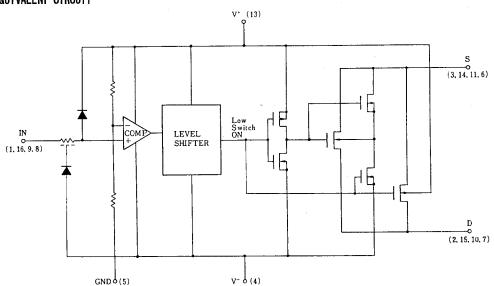
#### ■ PIN CONFIGURATION



#### TRUTH TABLE

Logic (In)	Switch
0	ON
1	OFF

#### EQUIVALENT CIRCUIT



\* Logic input threshold voltage  $V_{\rm TH}$  is about  $V^+$  x 0.128(V). When the designing, enough margin is required.



# **■ TERMINAL DESCRIPTION**

No.	SYMBOL	FUNCTION	No.	SYMBOL	FUNCTION
1	IN1	Control Signal Input	9	1 N3	Control Signal Input
2	D1	I	10	D3	Innut (Outnut 2
3	<b>S</b> 1	Input/Output 1	11	<b>S</b> 3	Input/Output 3
4	٧-	Negative (V <sup>-</sup> ) Power Supply	12	NC	Non Connection
5	GND	Ground	13	V <sup>+</sup>	Positive (V <sup>+</sup> ) Power Supply
6	S4	1 10 1 1	14	S2	1
7	D4	Input/Output 4	15	D2	Input/Output 2
8	I N4	Control Signal Input	16	1 N2	Control Signal Input

# **MAXIMUM RATINGS**

( Ta=25℃ )

PARAMETER	SYMBOL	RATINGS	UNIT	
	V+ - V-	44		
Supply Voltage	V+ - GND	19	٧	
•	GND - V-	25		
Input Voltage	V <sub>I</sub> ,V <sub>S</sub> ,V <sub>D</sub>	V <sup>-</sup> -0.5 ~ V <sup>+</sup> +0.5 *	٧	
Input Current	l <sub>I</sub>	30	mA	
	Is,I⊃ Continuous	20		
	Peak Value (PW=1ms,Duty0.1)	Value 70 1ms, Duty0.1)		
Power Dissipation	PD	500 (DIP) 200 (DMP)	mW	
Operating Temperature Range	Topr	0 ~+ 70	င	
Storage Temperature Range	Tstg	- 65 <b>~</b> + 125	ပ္	

<sup>\*</sup>  $V^++0.5V$  must be 44V or less.



# ■ ELECTRICAL CHARACTERISTICS (DC CHARACTERISTICS)

(  $V^{+}=15V$  ,  $V^{-}=-15V$  , GND=0V )

	CVMDO	CONDITIONS		TYP		MAX		UNIT	
PARAMETER	SYMBOL			25℃	0℃	25℃	70℃	וואטן	
Analog Signal Range	Vanalog			±15		±15	±15	٧	
	-	V <sub>1 №</sub> =0.8V	V <sub>D</sub> =10V	105	200	200	250	Ω	
On-state Resistance	Ron	ls=-1mA	V <sub>D</sub> =-10V	115	200	200	250		
Source-off	1 ( 55)	V <sub>1</sub> =2.4V	Vs=14V,VD=-14V	0.01		5	100		
Leakage Current	ls(off)	V:=2.4V	Vs=-14V,VD=14V	-0.02		- 5	-100	nA	
Drain-off	1 ((1)		V <sub>D</sub> =14V,V <sub>S</sub> =-14V	0.01		5	100		
Leakage Current	l <sub>D</sub> (off)	(TTO) a i	V 1-2.4V	V <sub>D</sub> =-14V, V <sub>S</sub> =14V	-0.02		- 5	-100	nA
Drain-on	1 ()		V <sub>D</sub> =V <sub>S</sub> =14V	0.1		5	200		
Leakage Current	l <sub>⊅</sub> (on)	V1=0.8V	V <sub>D</sub> =V <sub>S</sub> =-14V	-0.15		- 5	-200	nA	
Input Current		V1=2.4V		-0.0004		- 1	- 10		
	IH	V 1=15V	ı=15V			1	10	μA	
	l <sub>IL</sub>	V <sub>1</sub> =0V		-0.0004		- 1	- 10		
0	1+	V:=0 or 2.4V		0.9		2		mA	
Quiescent Current	1-			-0.3		-1			

#### ■ SWITCHING CHARACTERISTICS

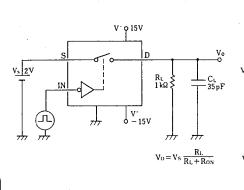
(  $V^{+}=15V$  ,  $V^{-}=-15V$  , GND=0V )

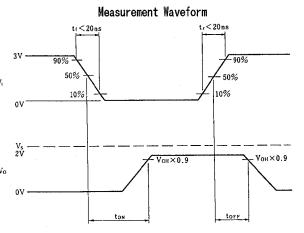
	OVERDOL	CONDITIONS		TYP	MAX			HMIT	
PARAMETER	SYMBOL			25℃	0℃	25℃	70℃	UNIT	
Turn-on Time	ton	$R_L=1k\Omega$ , $C_L=35pF$		480		600			
Turn-off Time	toff	n∟-IK32,	01-93bL	370		450		ns	
Charge Injection	Q	$C_L$ =1000pF, $V_{GEN}$ =0V, $R_{GEN}$ =0 $\Omega$		20				рC	
Source-Off Capacit.	Cs(off)	f=100kHz	Vs=0V, V1=5V	5					
Drain-Off Capacit.	C <sub>D</sub> (off)		f=100kHz	V <sub>D</sub> =0V, V <sub>I</sub> =5V	5				рF
Channel-On Capacitance	C <sub>D</sub> (on) +C <sub>s</sub> (on)			V <sub>D</sub> =V <sub>S</sub> =0V, V <sub>1</sub> =0V	16				ÞΓ
Off Isolation	OIRR		V -2V	70				dB	
Channel-to-channel Crosstalk	CCRR		$V_s=2V_{P_{R_L}}$ , $R_L=75\Omega$	90				ub	



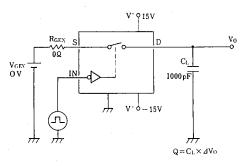
### **MEASUREMENT CIRCUITS**

#### (1) Turn-on/Turn-off Time

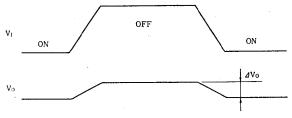




#### (2) Charge Injection

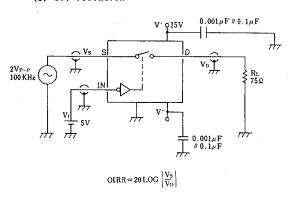




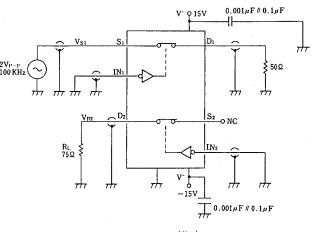


Measurement Waveform

#### (3) Off Isolation



#### (4) Channel-To-Channel Crosstalk



# **NJU7301**

# **MEMO**

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