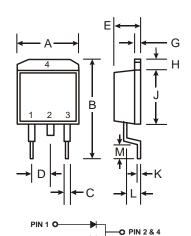
16A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

Features

- Guard Ring Die Construction for Transient Protection
- Low Power Loss, High Efficiency
- **High Surge Capability**
- High Current Capability and Low Forward Voltage Drop
- Surge Overload Rating to 225A Peak
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications
- Lead Free Finish/RoHS Compliant (Note 3)

Mechanical Data

- Case: D²PAK
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Tin. Solderable per MIL-STD-202, Method 208 (e3)
- Ordering Information, Page 2
- Polarity: See Diagram
- Marking: Type Number
- Weight: 1.7 grams (approximate)



D ² PAK					
Dim	Min	Max			
Α	9.65	10.69			
В	14.60	15.88			
С	0.51	1.14			
D	2.29	2.79			
Е	4.37	4.83			
G	1.14	1.40			
Н	1.14	1.40			
J	8.25	9.25			
K	0.30	0.64			
L	2.03	2.92			
М	2.29	2.79			
All Dimensions in mm					

Maximum Ratings and Electrical Characteristics @T_A = 25°C unless otherwise specified

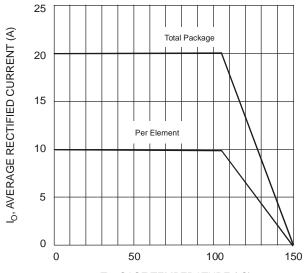
Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic		Symbol	SBG 2030CT	SBG 2035CT	SBG 2040CT	SBG 2045CT	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage (Note 4)		V _{RRM} V _{RWM} V _R	30	35	40	45	V
RMS Reverse Voltage		V _{R(RMS)}	21	25	28	32	V
Average Rectified Output Current	@ T _C = 105°C	Ιο	20		Α		
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	225				Α
Forward Voltage, per Element	@ I _F = 10A	V_{FM}	0.55		V		
Peak Reverse Current at Rated DC Blocking Voltage (Note 4)	@ T _j = 25°C @ T _j = 100°C	I _{RM}	1.0 50		mA		
Typical Total Capacitance (Note 2)		C _T	650				pF
Typical Thermal Resistance Junction to Case (Note 1)		$R_{\theta JC}$	2.0				°C/W
Operating and Storage Temperature Range		T _j , T _{STG}	-65 to +150				°C

Notes:

- Thermal resistance junction to case mounted on heatsink.
- Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.
- RoHS revision 13.2.2003. Glass and high temperature solder exemptions applied, see EU Directive Annex Note 7.
- Short duration pulse test used to minimize self-heating effect.





T_C, CASE TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve

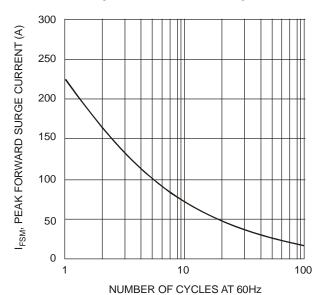


Fig. 3 Max Non-Repetitive Surge Current

100 T_j = 25°C T

 $V_{\rm F}$, INSTANTANEOUS FORWARD VOLTAGE (V) Fig. 2 Typical Forward Characteristics, Per Element

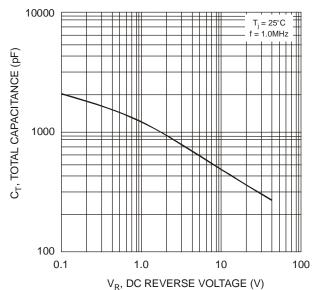


Fig. 4 Typical Total Capacitance, Per Element

Ordering Information (Note 5)

Device	Packaging	Shipping
SBG2030CT-T-F	D ² PAK	800/Tape & Reel, 13-inch
SBG2035CT-T-F	D ² PAK	800/Tape & Reel, 13-inch
SBG2040CT-T-F	D ² PAK	800/Tape & Reel, 13-inch
SBG2045CT-T-F	D ² PAK	800/Tape & Reel, 13-inch

Notes: 5. For packaging details, visit our website at http://www.diodes.com/datasheets/ap02007.pdf.



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