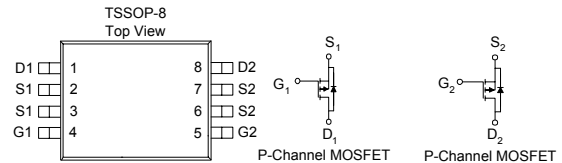


Dual P-Channel 12-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY		
V_{DS} (V)	$r_{DS(on)}$ (OHM)	I_D (A)
-12	0.022 @ $V_{GS} = -4.5V$	-5.7
	0.027 @ $V_{GS} = -2.5V$	-4.9
	0.032 @ $V_{GS} = -1.8V$	-4.0

- Low $r_{DS(on)}$ provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe TSSOP-8 saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ UNLESS OTHERWISE NOTED)			
Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	-12	V
Gate-Source Voltage	V_{GS}	± 8	
Continuous Drain Current ^a	I_D	$T_A = 25^\circ C$	-5.7
		$T_A = 70^\circ C$	-4.7
Pulsed Drain Current ^b	I_{DM}	-10	A
Continuous Source Current (Diode Conduction) ^a	I_S	± 1.6	A
Power Dissipation ^a	P_D	$T_A = 25^\circ C$	1.15
		$T_A = 70^\circ C$	0.7
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 150	$^\circ C$

THERMAL RESISTANCE RATINGS					
Parameter	Symbol	Typ	Max		
Maximum Junction-to-Ambient ^a	R_{thJA}	t \leq 10 sec	93	110	$^\circ C/W$
		Steady State	130	150	

Notes

- Surface Mounted on 1" x 1" FR4 Board.
- Pulse width limited by maximum junction temperature

SPECIFICATIONS (T _A = 25°C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Conditions	Limits			Unit
			Min	Typ	Max	
Static						
Gate-Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-0.4			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = +/-8 V			±100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -9.6 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -9.6 V, V _{GS} = 0 V, T _J = 55°C			-10	
On-State Drain Current ^A	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -10 V	-3			A
Drain-Source On-Resistance ^A	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -4.0 A			0.022	Ω
		V _{GS} = -2.5 V, I _D = -3.6 A			0.027	
		V _{GS} = -1.8 V, I _D = -3.2 A			0.032	
Forward Transconductance ^A	g _{fs}	V _{DS} = -5 V, I _D = -4.0 A		3		S
Diode Forward Voltage	V _{SD}	I _S = -1.6 A, V _{GS} = 0 V		-0.7		V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -5 V, V _{GS} = -4.5 V, I _D = -4.0 A		19		nC
Gate-Source Charge	Q _{gs}			4.5		
Gate-Drain Charge	Q _{gd}			5.3		
Input Capacitance	C _{iss}	P-Channel V _{DS} = -6V, V _{GS} = 0V, f = 1MHz		1800		pF
Output Capacitance	C _{oss}			400		
Reverse Transfer Capacitance	C _{rss}			300		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -5 V, R _L = 5 OHM, V _{GEN} = -4.5 V, R _G = 6 OHM		240		ns
Rise Time	t _r			580		
Turn-Off Delay Time	t _{d(off)}			7		
Fall-Time	t _f			4.2		

Notes

- Pulse test: PW ≤ 300us duty cycle ≤ 2%.
- Guaranteed by design, not subject to production testing.

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Typical Electrical Characteristics

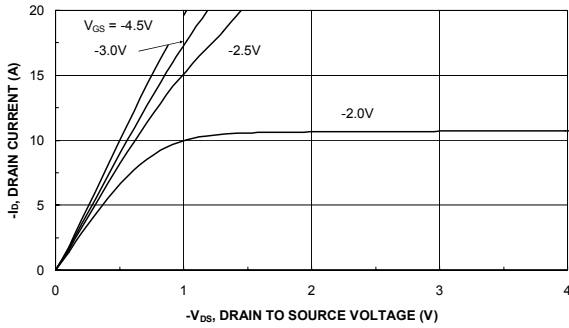


Figure 1. Output Characteristics

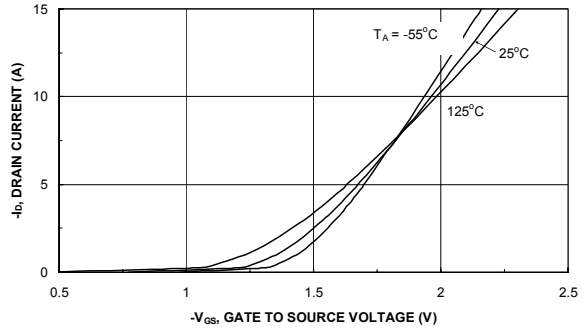


Figure 2. Transfer Characteristics

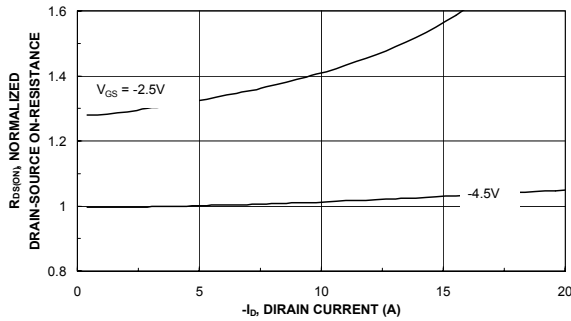


Figure 3. On-Resistance vs. Drain Current

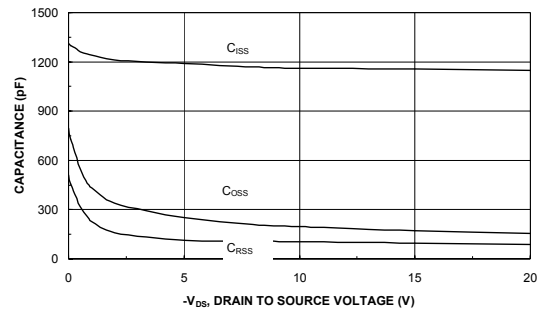


Figure 4. Capacitance

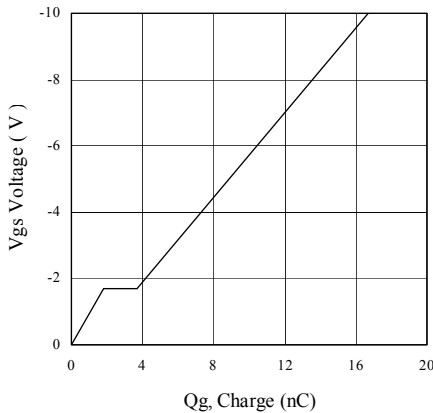


Figure 5. Gate Charge

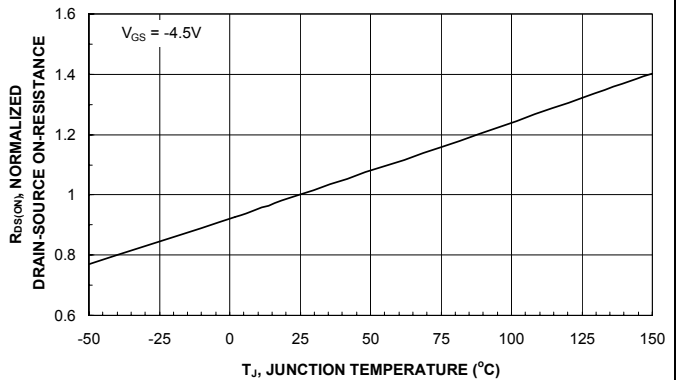


Figure 6. On-Resistance vs. Junction Temperature

Typical Electrical Characteristics

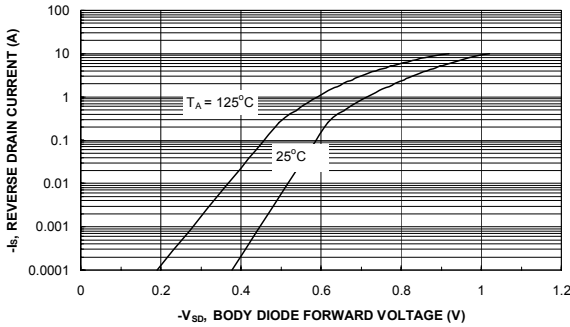


Figure 7. Source-Drain Diode Forward Voltage

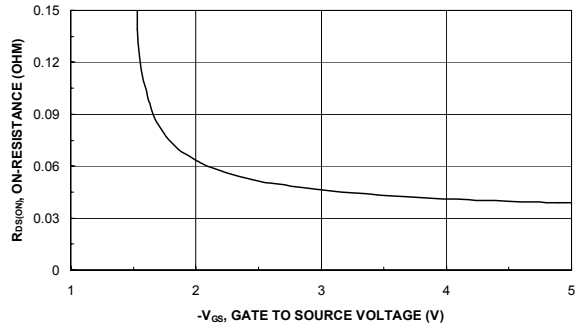


Figure 8. On-Resistance with Gate to Source Voltage

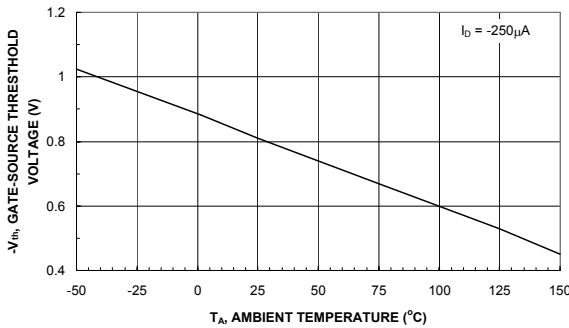


Figure 9. Vth Gate to Source Voltage Vs Temperature

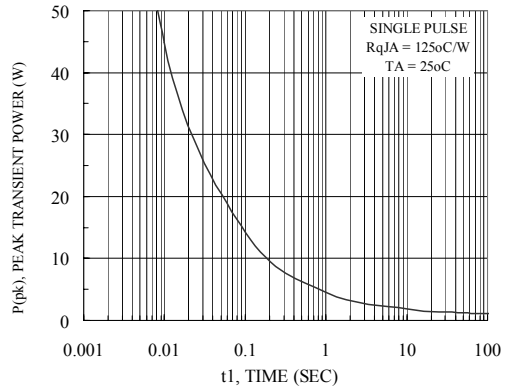


Figure 10. Single Pulse Maximum Power Dissipation

Normalized Thermal Transient Junction to Ambient

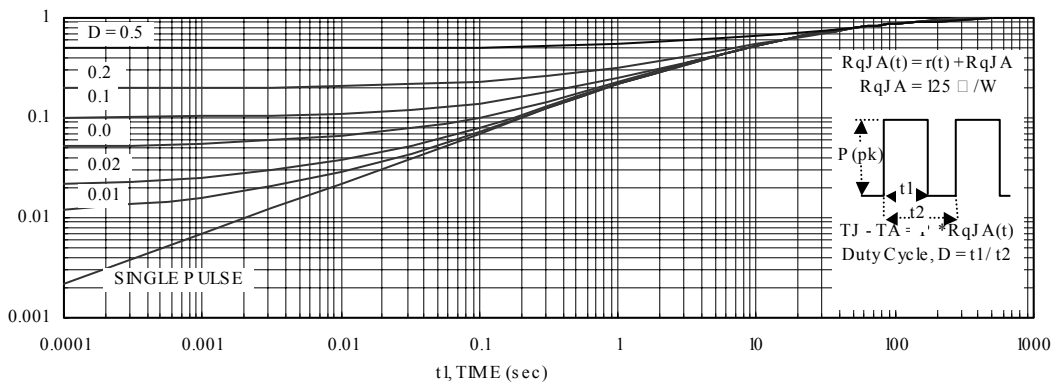
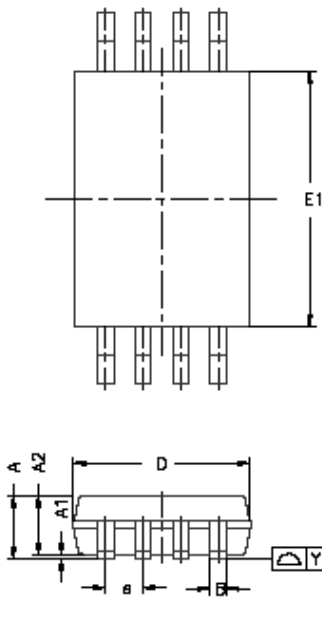


Figure 11. Transient Thermal Response Curve

Package Information

TSSOP-8: 8LEAD



DIM.	MILLIMETERS		
	MIN.	NDM.	MAX.
A	1.05	1.10	1.20
A(1)	0.05	0.10	0.15
A(2)	0.99	1.02	1.05
B	0.19	0.25	0.30
C	---	0.127	---
D	2.90	3.00	3.10
E	6.20	6.40	6.60
E1	4.30	4.40	4.50
b	0.6595C		
L	0.45	0.60	0.75
L1	0.90	1.00	1.10
Y	---	---	0.10
Ø1	Ø	Ø	Ø
R	0.09	---	---
S	0.20	---	---