

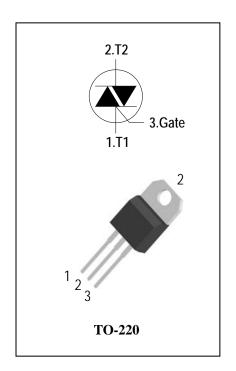
3 Quadrants Triacs

General Description

High current density due to mesa technology . the T8XX triac series is suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, High power motor controls e.g. washing machines and vacuum cleaners,Rectifier-fed DC inductive loads e.g.DC motors and solenoids , motor speed controllers.

Features

- ◆ Repetitive Peak Off-State Voltage: 600V/800V
- ◆ R.M.S On-State Current (I_{T(RMS)}=8A)
- ◆ High Commutation dv/dt
- ◆ These Devices are Pb-Free and are RoHS Compliant



Absolute Maximum Ratings

Symbol	Items	Cond	Ratings	Unit	
V _{DRM}	Denetitive Deals Off Ctate Veltage	T: - 25°C	T8XXC-6B	600	V
V _{RRM}	Repetitive Peak Off-State Voltage	Tj = 25°C	T8XXC-8B	800	
I _{T(RMS)}	R.M.S On-State Current	T _C = 100°C	8	Α	
I _{TSM}	Surge On-State Current	tp=20ms(50Hz)/tp=16.7	80/84	А	
l²t	I ² t for fusing	tp=10ms	36	A ² s	
41/44	Critical rate of rise of on-state F = 120 Hz Tj = 125°C			50	A/µs
dl/dt	current I _G = 2 x I _{GT} , tr ≤ 100 ns				
Ідм	Peak Gate Current	tp = 20 μs Tj = 125°C	4	Α	
P _{G(AV)}	Average Gate Power Dissipation(Tj=125°C)			1	W
P _{GM}	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			5	W
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T _{STG}	Storage Temperature			- 40 ~ 150	°C



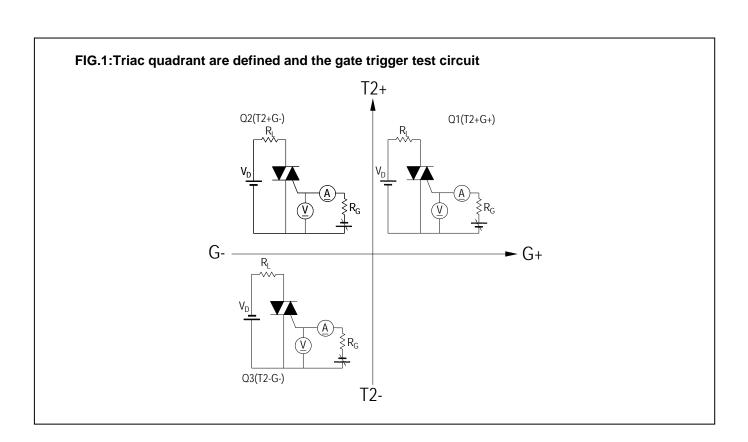


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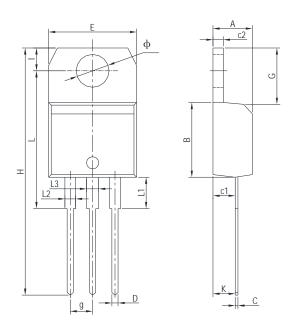
Electrical Characteristics (Tj = 25°C unless otherwise specified)

Symbol	Items		Conditions		T8XXC-6/8B			Unit	
					T805	T810	T835	T850	
I _{DRM}	Peak Forward Reverse Blocking		V _{DRM} = V _{RRM} , Tj = 25°C	N4	5			uA	
I _{RRM}	Current		V _{DRM} = V _{RRM} , Tj = 125°C	Max.	1			mA	
V _{TM}	Peak On-State Voltage		I _{TM} = 11A, t _p = 380 μs	Max.	1.55			V	
$V_{\sf GD}$	Q1-Q2-Q3	Non – Trigger Gate Voltage	$V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$ $Tj = 125^{\circ}\text{C}$	Min.	0.2		V		
V _{GT}	Q1-Q2-Q3	GateTrigger Voltage	Max.		1.3			V	
l _{GT}	Q1-Q2-Q3	GateTrigger Current	$V_D = 12V$, $R_L = 33\Omega$	Max.	5	10	35	50	mA
lΗ	Q1-Q2-Q3	Holding Current	I _T = 0.1A	Max.	10	15	40	60	mA
	Q1-Q3	Latabina Current	1 - 4 2 1	Max.	10	25	50	70	mA
IL.	Q2	Latching Current	I _G = 1.2 I _{GT}		15	30	70	80	
dV/dt	Critical Rate of Rise of Off-State Voltage		$V_D = 2/3V_{DRM}$ gate open $Tj = 125^{\circ}C$	Min.	20	40	400	1000	V/µs
(dV/dt)c	Rate of Change of Commutating Current,		(dl/dt)c=-3.5A/ms Tj = 125°C	Min.	0.5	1	10	25	V/µs
R _{th(j-c)}	Junction to case (AC)		Max.	1.6			°C/W		
R _{th(j-a)}	Junction to ambient		Max.	60			°C/W		



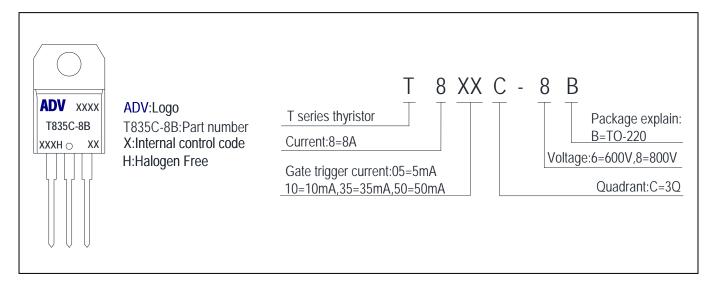


PACKAGE MECHANICAL DATA TO-220 Package Dimension



	Dimer	nsions	Dimensions			
Symbol	In Milli	meters	In Inches			
	Min	Max	Min	Max		
Α	4.40	4.60	0.173	0.181		
В	9.00	9.30	0.354	0.366		
С	0.40	0.60	0.015	0.023		
c1	2.00	2.60	0.078	0.102		
c2	1.23	1.32	0.048	0.051		
D	0.70	1.00	0.027	0.039		
E	10.00	10.40	0.393	0.409		
g	2.40	2.70	0.094	0.106		
G	6.20	6.80	0.244	0.267		
Н	28.00	29.85	1.102	1.175		
I	2.65	2.95	0.104	0.116		
L	15.80	16.80	0.622	0.661		
L1	3.	3.75 0.147		47		
L2	1.14	1.70	0.044	0.066		
L3	1.14	1.70	0.044	0.066		
Ф	3.60	3.90	0.141	0.153		
К	2.60TYP		0.102TYP			

Making Diagram



Ordering information

Part number Package		Marking	Packing	Quantity			
T835C-8B	TO-220	T835C-8B	T835C-8B Tube				
Note: Gate Trigger Current Sensitivity and type05=5mA,10=10mA,35=35mA,50=50mA							



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