SS32T THRU SS325T

3.0AMP SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

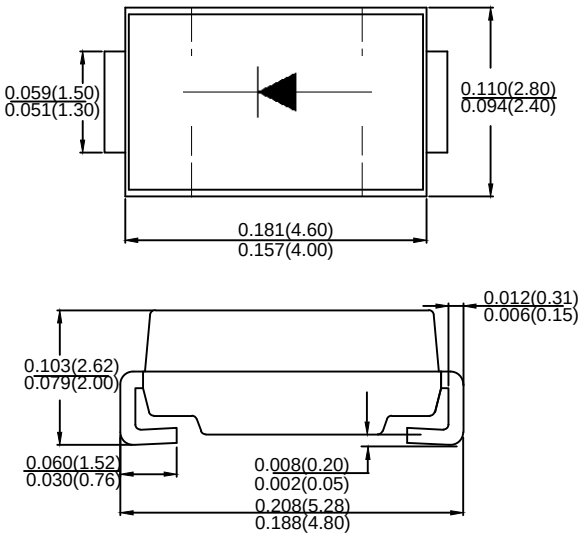
Features

- Schottky Brrier Chip
- Low Power Loss,High Efficiency
- Ideally Suited for Automatic Assembly
- Surge Overload Rating to 100APeak
- Plastic Case Material has UL Flammability Classification Rating 94V-0

Mechanical Data

- Case: Molded plastic SMA
- Terminals: Plated leads solderable per MIL-STD-750,Method 2026 guaranteed
- Polarity: Color band denotes cathode end
- Mounting Position: Any
- Making: Type Number

SMA/DO-214AC



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25℃ ambient temperature unless otherwise specified

Single phase,half wave,60Hz,resistive or inductive load

For capacitive load derate current by 20%

Type Number	SYMBOL	SS 32T	SS 33T	SS 34T	SS 345T	SS 35T	SS 36T	SS 38T	SS 310T	SS 315T	SS 320T	SS 325T	Unit
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	20	30	40	45	50	60	80	100	150	200	250	V
Maximum RMS Voltage	V_{RMS}	14	21	28	31	35	42	56	70	105	140	175	V
Maximum DC Blocking Voltage	V_{DC}	20	30	40	45	50	60	80	100	150	200	250	V
Average Rectified Output Current @T_L =100℃	$IF_{(AV)}$	3.0											A
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed On Rated Load (JEDEC Method) @$T_{j=25}$ ℃ @$T_{j=125}$ ℃	I_{FSM}	100 80											A
Non-Repetitive Peak Forward Surge Current 1.0ms Single half sine-wave Superimposed On Rated Load (JEDEC Method) @$T_{j=25}$ ℃ @$T_{j=125}$℃	I_{FSM}	200 160											A
10000 times of the wave surge current (time width1ms, time interval 3s)	I_{FSM}	75											A
Rating for fusing (t<8.3ms)	I^2t	41.50											A^2s
Forward Voltage @IF=3.0A	V_{FM}	0.48				0.6		0.8		0.86		0.9	V
Peak Reverse Current @T_A =25℃	I_R	0.1						0.05					mA
At Rated DC Blocking Voltage @T_A =100℃		10						5					
Typical Junction Capacitance (Note 1)	C_J	170						100					pF
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	110											℃/W
Operating and Storage Temperature Range	T_J,T_{STG}	-55 to+150											℃

Note:

1. Measured at 1.0 MHz and Applied reverse Voltage of 4.0V D.C
2. Device mounted on FR-4 substrate, 1"*1", 2oz, single-sided, PC boards with 0.1"*0.15" copper pad.

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Fig. 1 Forward Current Derating Curve

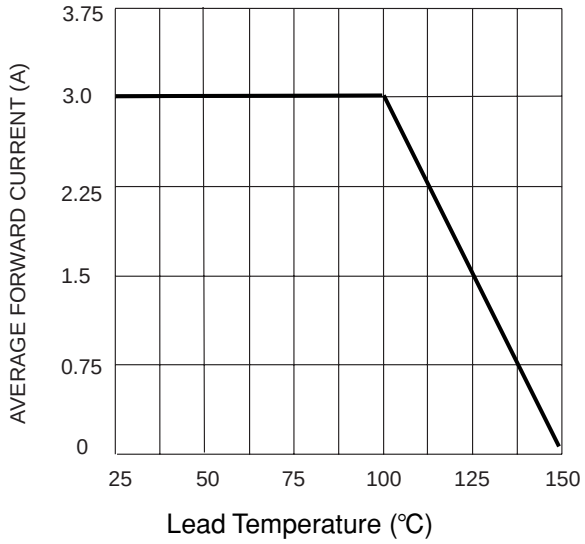


Fig. 2 Typ. Forward Characteristics

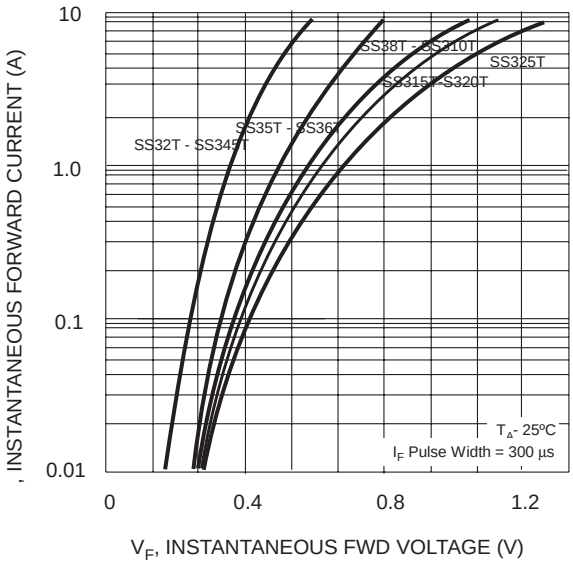


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

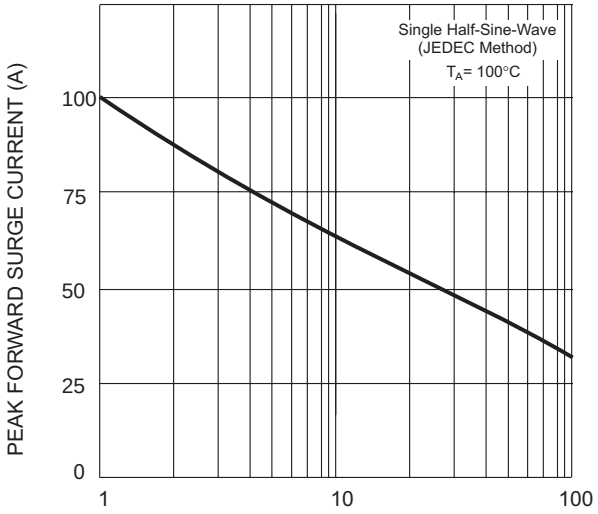
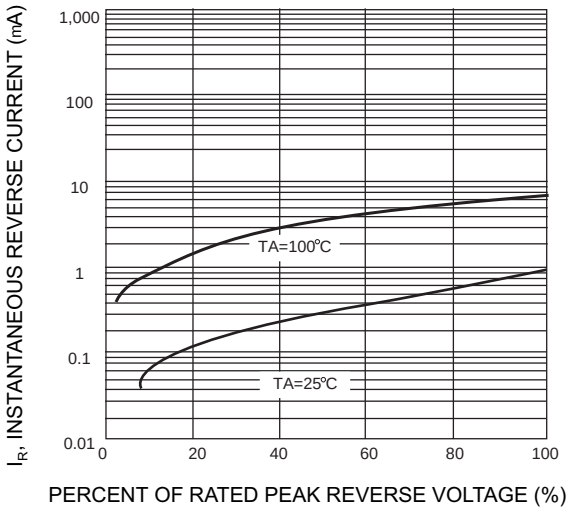
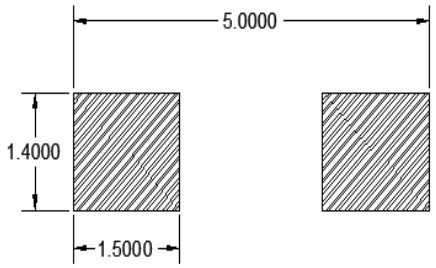


Fig. 4 T ypical Reverse Characteristics (per element)



SMA PAD LAYOUT



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