

MBRD320/S - MBRD3100/S

3.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

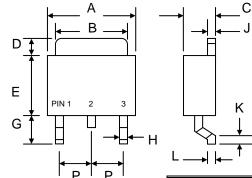


Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- High Surge Current Capability
- Low Power Loss, High Efficiency
- Ideally Suited for Automatic Assembly
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity **Protection Applications**

Mechanical Data

- Case: DPAK/TO-252, Molded Plastic •
- Terminals: Plated Leads Solderable per MIL-STD-202, Method 208
- Polarity: See Diagram
- Weight: 0.3 grams (approx.)
- Mounting Position: Any
- Marking: Device Code, See Page 3
- Lead Free: For RoHS / Lead Free Version, Add "-LF" Suffix to Part Number, See Page 4





PIN 3 O O Case, PIN 2 (MBRD320S Series)

DPAK/TO-252						
Dim	Min	n Max				
Α	6.05	6.65				
В	5.05	5.55				
С	2.25	2.40				
D	1.05	1.25				
E	5.48	6.08				
G	2.55	3.00				
н	0.55	0.90				
J	0.49	0.55				
к	0.95	1.25				
L	0.49	0.55				
Р	2.30 Typical					
All Dimensions in mm						

Maximum Ratings and Electrical Characteristics @TA=25°C unless otherwise specified

Characteristic	Symbol	MBRD 320/S	MBRD 330/S	MBRD 340/S	MBRD 350/S	MBRD 360/S	MBRD 380/S	MBRD 3100/S	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	Vrrm Vrwm Vr	20	30	40	50	60	80	100	V
RMS Reverse Voltage	VR(RMS)	14	21	28	35	42	56	70	V
Average Rectified Output Current @T _c = 125°C	lo	3.0						А	
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load (JEDEC Method)	IFSM	75					A		
Forward Voltage @I _F = 3.0A	Vгм	0.50 0.75 0.85				85	V		
Peak Reverse Current $@T_J = 25^{\circ}C$ At Rated DC Blocking Voltage $@T_J = 100^{\circ}C$	Irm	0.2 20						mA	
Typical Junction Capacitance (Note 1)	CJ	250 180 140			40	pF			
Thermal Resistance, Junction to Ambient (Note 2) Thermal Resistance, Junction to Case (Note 2)	R JA R JC	80 6.0					°C/W		
Operating and Storage Temperature Range	TJ, TSTG	-55 to +150					°C		

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V DC.

2. Mounted on FR-4 PC board with minimum recommended pad layout.

MBRD320/S - MBRD3100/S

(AV), AVERAGE FORWARD RECTIFIED CURRENT (A)

3.0

2.4

1.8

1.2

0.6

0

75

0 15

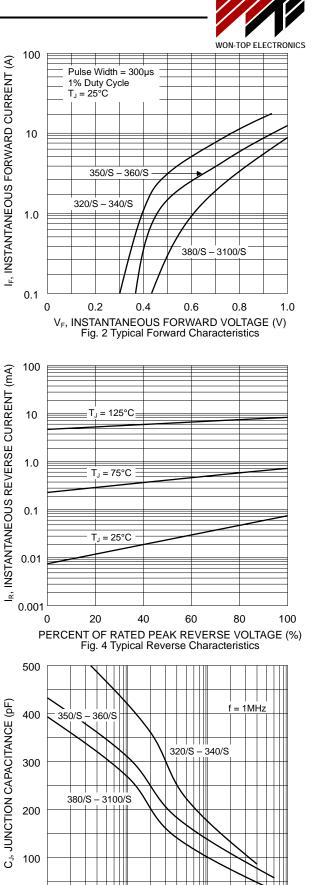
Resistive or Inductive Load

30 45 60 75

T_c, CASE TEMPERATURE (°C) Fig. 1 Forward Current Derating Curve

90 105 120 135 150

Single Half-Sine-Wave JEDEC Method



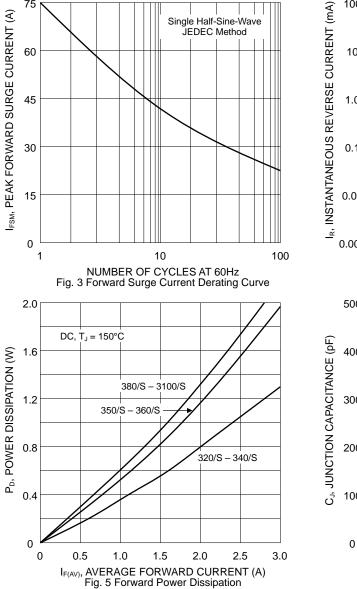


Fig. 6 Typical Junction Capacitance © Won-Top Electronics Co., Ltd.

10

V_R, DC REVERSE VOLTAGE (V)

0.1

1

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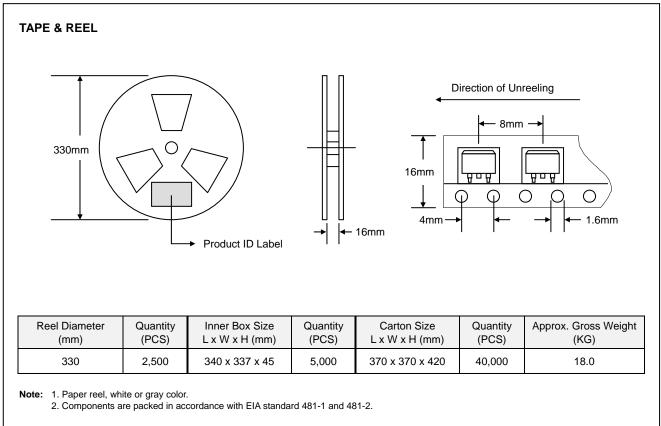
Revision: September, 2012

100



MARKING INFORMATION RECOMMENDED FOOTPRINT 0.298 0.109 (2.77) (7.57) B3xxS -1-0.091 0.24 (2.30) (6.10) 0.056 (1.42) B3xx = Device Number = 20, 30, 40, 50, 60, 80 or 100 хх s = Suffix for MBRD320S Series (remove 0.261 (6.63) when ordering MBRD320 series) inches(mm)

PACKAGING INFORMATION





Product No.	Package Type	Shipping Quantity				
MBRD320/S-T3	DPAK	2500/Tape & Reel				
MBRD330/S-T3	DPAK	2500/Tape & Reel				
MBRD340/S-T3	DPAK	2500/Tape & Reel				
MBRD350/S-T3	DPAK	2500/Tape & Reel				
MBRD360/S-T3	DPAK	2500/Tape & Reel				
MBRD380/S-T3	DPAK	2500/Tape & Reel				
MBRD3100/S-T3	DPAK	2500/Tape & Reel				

ORDERING INFORMATION

1. Shipping quantity given is for minimum packing quantity only. For minimum order quantity, please consult the Sales Department.

 To order RoHS / Lead Free version (with Lead Free finish), add "-LF" suffix to part number above. For example, MBRD320-T3-LF.

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