

4V Drive Pch MOSFET

RP1H065SP

● Structure

Silicon P-channel MOSFET

● Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (MPT6).

● Application

Switching

● Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	1000
RP1H065SP		○

● Absolute maximum ratings (T_a = 25 °C)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V _{DSS}	-45	V
Gate-source voltage	V _{GSS}	±20	V
Drain current	Continuous	I _D	±6.5 A
	Pulsed	I _{DP} *1	±26 A
Source current (Body Diode)	Continuous	I _S	-1.6 A
	Pulsed	I _{SP} *1	-26 A
Power dissipation	P _D *2	2.0	W
Channel temperature	T _{ch}	150	°C
Range of storage temperature	T _{stg}	-55 to +150	°C

*1 Pw≤10μs, Duty cycle≤1%

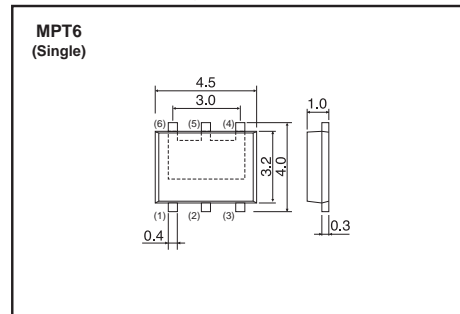
*2 Mounted on a ceramic board.

● Thermal resistance

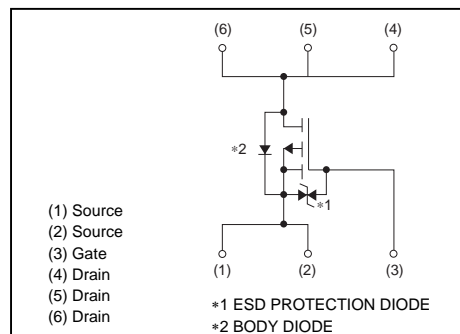
Parameter	Symbol	Limits	Unit
Channel to Ambient	R _{th(ch-a)} *	62.5	°C / W

*Mounted on a ceramic board.

● Dimensions (Unit : mm)



● Inner circuit



●Electrical characteristics (T_a = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±10	μA	V _{GS} =±20V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	-45	-	-	V	I _D =-1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	-1	μA	V _{DS} =-45V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	-1.0	-	-3.0	V	V _{DS} =-10V, I _D =-1mA
Static drain-source on-state resistance	R _{DS(on)} *	-	22	31	mΩ	I _D =-6.5A, V _{GS} =-10V
		-	30	42		I _D =-6.5A, V _{GS} =-4.5V
		-	33	46		I _D =-6.5A, V _{GS} =-4.0V
Forward transfer admittance	Y _{fs} *	9	-	-	S	I _D =-6.5A, V _{DS} =-10V
Input capacitance	C _{iss}	-	3200	-	pF	V _{DS} =-10V
Output capacitance	C _{oss}	-	400	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	240	-	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	-	19	-	ns	I _D =-3.25A, V _{DD} =-25V
Rise time	t _r *	-	27	-	ns	V _{GS} =-10V
Turn-off delay time	t _{d(off)} *	-	120	-	ns	R _L =7.7Ω
Fall time	t _f *	-	58	-	ns	R _G =10Ω
Total gate charge	Q _g *	-	28	-	nC	I _D =-6.5A, V _{DD} =-25V
Gate-source charge	Q _{gs} *	-	7	-	nC	V _{GS} =-5V
Gate-drain charge	Q _{gd} *	-	9	-	nC	

*Pulsed

●Body diode characteristics (Source-Drain) (T_a = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	-1.2	V	I _S =-6.5A, V _{GS} =0V

*Pulsed

●Electrical characteristic curves (Ta=25°C)

Fig.1 Typical Output Characteristics (I)

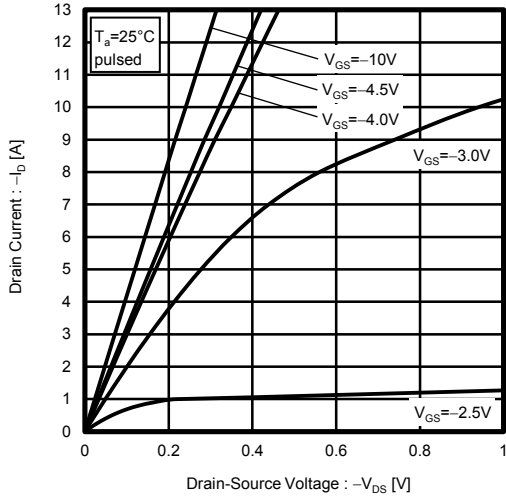


Fig.3 Static Drain-Source On-State Resistance vs. Drain Current

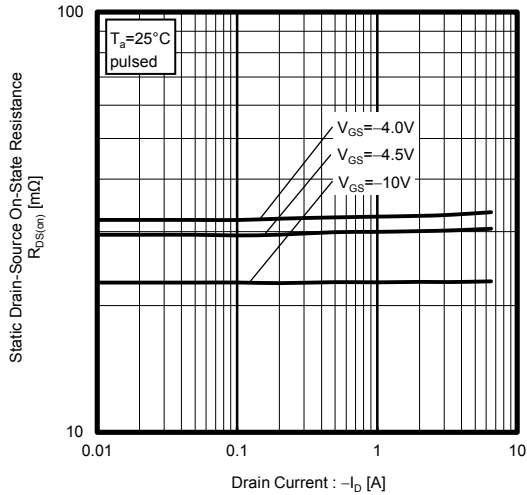


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

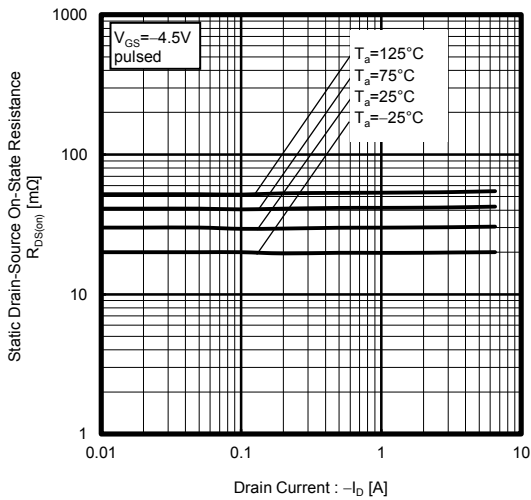


Fig.2 Typical Output Characteristics (II)

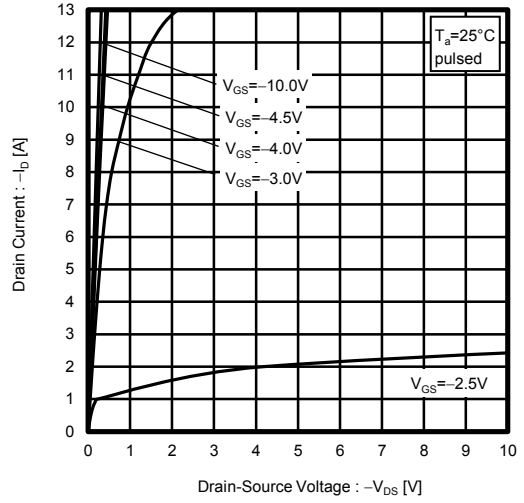


Fig.4 Static Drain-Source On-State Resistance vs. Drain Current

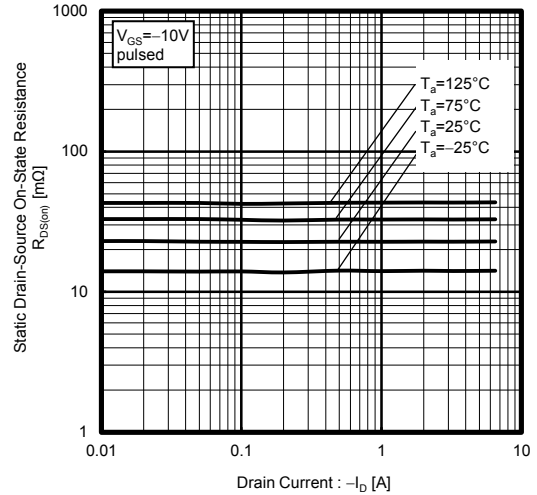


Fig.6 Static Drain-Source On-State Resistance vs. Drain Current

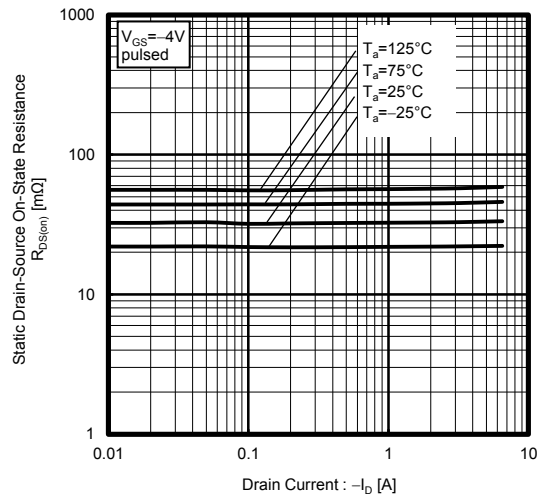


Fig.7 Forward Transfer Admittance vs. Drain Current

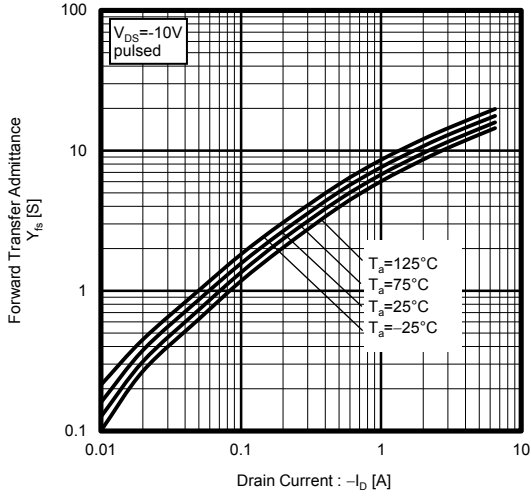


Fig.8 Typical Transfer Characteristics

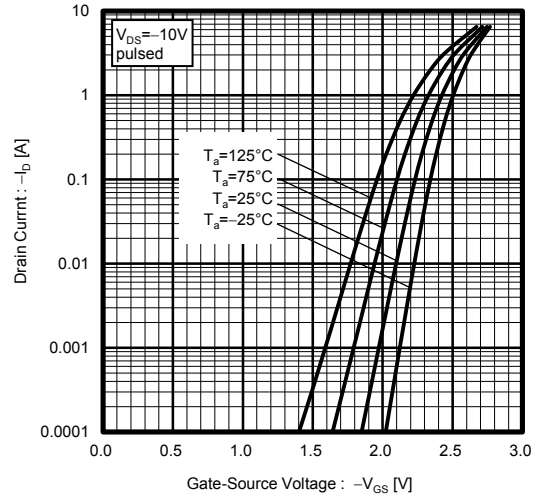


Fig.9 Source Current vs. Source-Drain Voltage

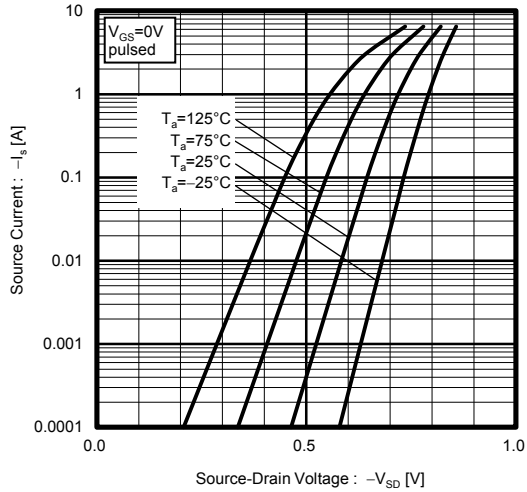


Fig.10 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

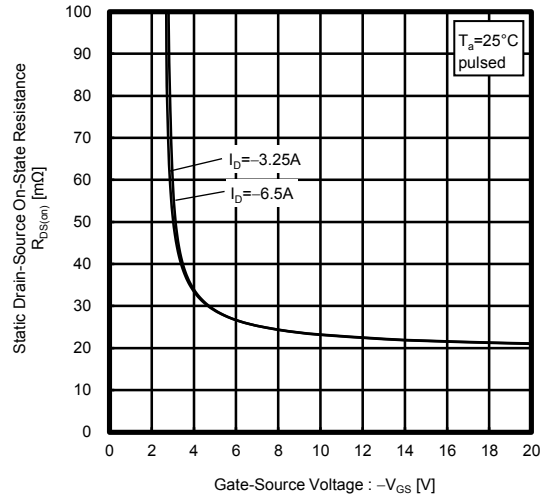


Fig.11 Switching Characteristics

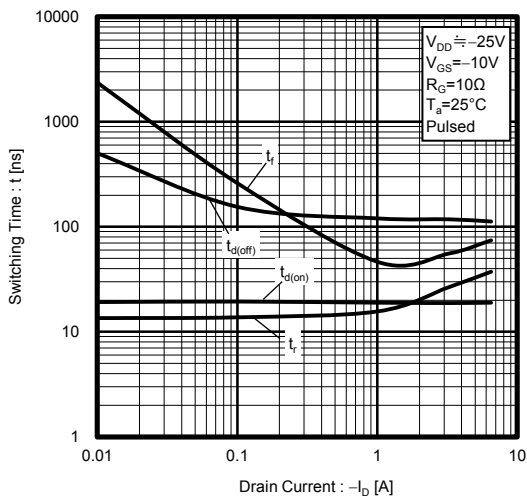


Fig.12 Dynamic Input Characteristics

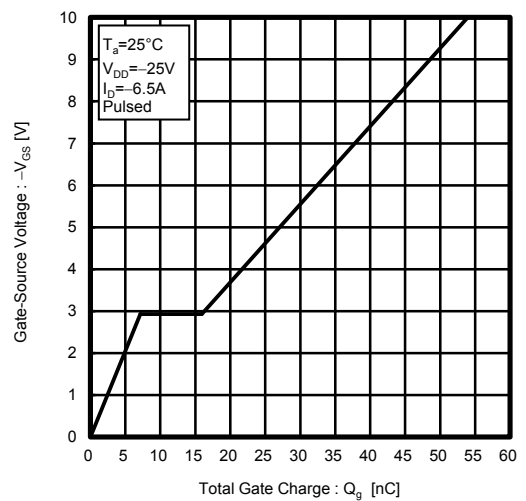


Fig.13 Typical Capacitance vs. Drain-Source Voltage

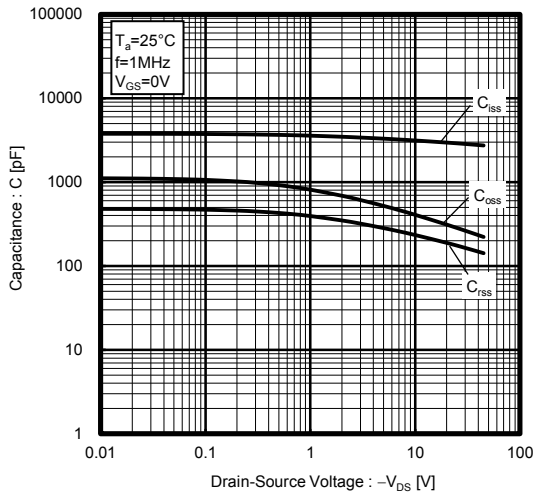


Fig.14 Maximum Safe Operating Area

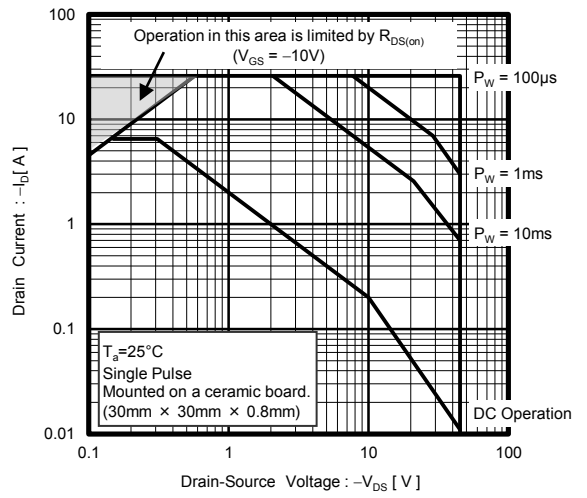
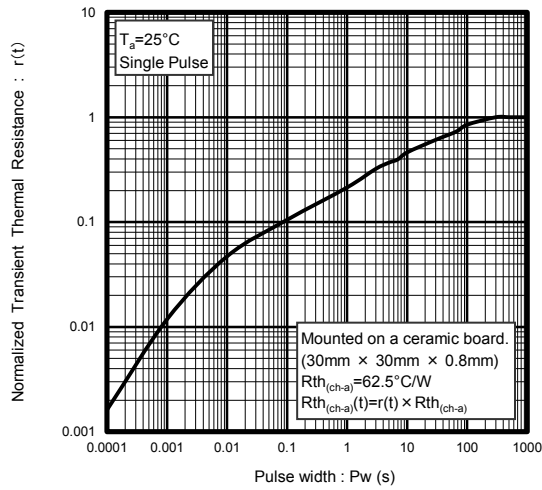


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width



● Measurement circuits

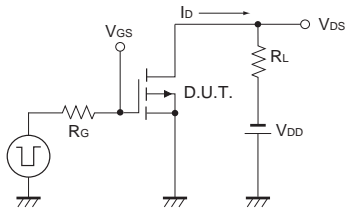


Fig.1-1 Switching Time Measurement Circuit

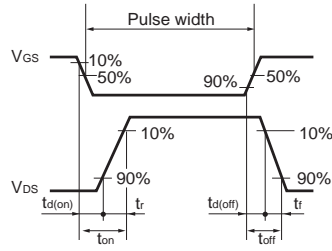


Fig.1-2 Switching Waveforms

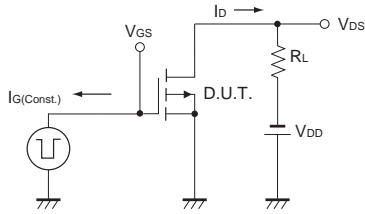


Fig.2-1 Gate Charge Measurement Circuit

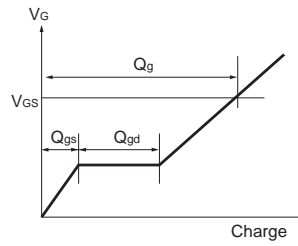


Fig.2-2 Gate Charge Waveform

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