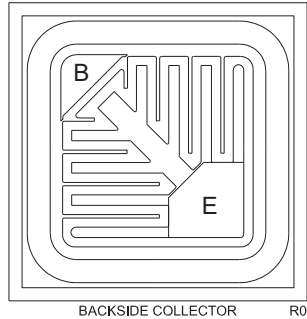


# CP595-2N5680

## PNP - High Current Transistor Die

### 1.0 Amp, 120 Volt

The CP595-2N5680 is a silicon PNP transistor designed for and high current applications.



#### MECHANICAL SPECIFICATIONS:

Die Size	30 x 30 MILS
Die Thickness	7.0 MILS
Base Bonding Pad Size	6.0 x 6.0 MILS
Emitter Bonding Pad Size	7.5 x 7.5 MILS
Top Side Metalization	Al – 30,000Å
Back Side Metalization	Au – 10,000Å
Scribe Alley Width	2.4 MILS
Wafer Diameter	4 INCHES
Gross Die Per Wafer	12,500

#### MAXIMUM RATINGS: ( $T_A=25^\circ\text{C}$ unless otherwise noted)

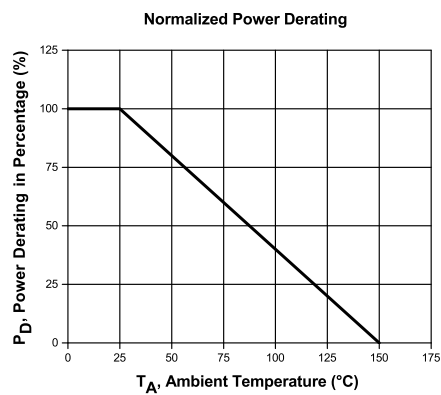
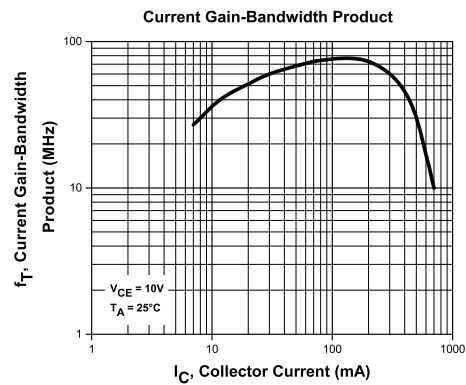
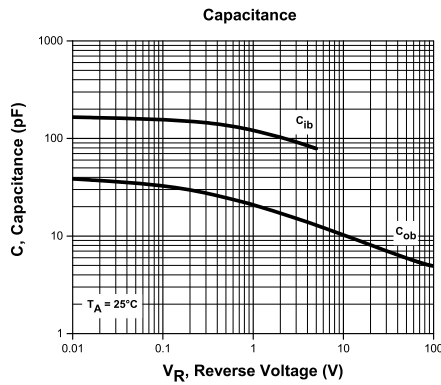
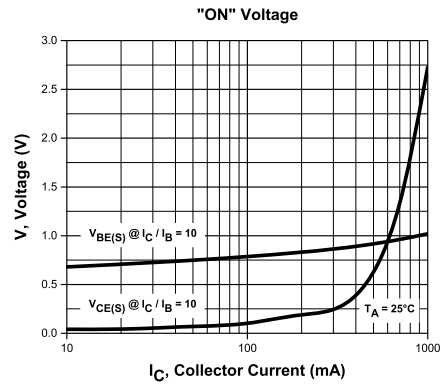
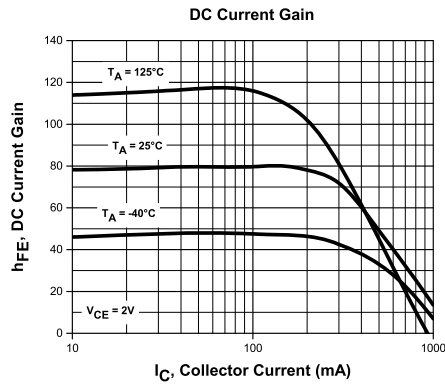
	SYMBOL		UNITS
Collector-Base Voltage	$V_{CBO}$	120	V
Collector-Emitter Voltage	$V_{CEO}$	120	V
Emitter-Base Voltage	$V_{EBO}$	4.0	V
Continuous Collector Current	$I_C$	1.0	A
Continuous Base Current	$I_B$	0.5	A
Operating and Storage Junction Temperature	$T_J, T_{stg}$	-65 to +150	$^\circ\text{C}$

#### ELECTRICAL CHARACTERISTICS: ( $T_A=25^\circ\text{C}$ unless otherwise noted)

SYMBOL	TEST CONDITIONS	MIN	MAX	UNITS
$I_{CBO}$	$V_{CB}=120\text{V}$		1.0	$\mu\text{A}$
$I_{CEV}$	$V_{CE}=120\text{V}, V_{EB}=1.5\text{V}$		1.0	$\mu\text{A}$
$I_{CEO}$	$V_{CE}=80\text{V}$		10	$\mu\text{A}$
$I_{EBO}$	$V_{EB}=4.0\text{V}$		1.0	$\mu\text{A}$
$BV_{CEO}$	$I_C=10\text{mA}$	120		V
$V_{CE(SAT)}$	$I_C=250\text{mA}, I_B=25\text{mA}$		0.6	V
$V_{CE(SAT)}$	$I_C=500\text{mA}, I_B=50\text{mA}$		1.0	V
$V_{CE(SAT)}$	$I_C=1.0\text{A}, I_B=200\text{mA}$		2.0	V
$V_{BE(ON)}$	$V_{CE}=2.0\text{V}, I_C=250\text{mA}$		1.0	V
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=250\text{mA}$	40	150	
$h_{FE}$	$V_{CE}=2.0\text{V}, I_C=1.0\text{A}$	5.0		
$h_{fe}$	$V_{CE}=1.5\text{V}, I_C=0.2\text{A}, f=1.0\text{kHz}$	40		
$f_T$	$V_{CE}=10\text{V}, I_C=100\text{mA}, f=10\text{MHz}$	30		MHz
$C_{ob}$	$V_{CB}=20\text{V}, I_E=0, f=1.0\text{MHz}$		50	pF

# CP595-2N5680

## Typical Electrical Characteristics



## BARE DIE PACKING OPTIONS

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### BARE DIE IN TRAY (WAFFLE) PACK

**CT:** Singulated die in tray (waffle) pack.  
(example: CP211-PART NUMBER-CT)

**CM:** Singulated die in tray (waffle) pack 100% visually inspected as per MIL-STD-750, (method 2072 transistors, method 2073 diodes).  
(example: CP211-PART NUMBER-CM)

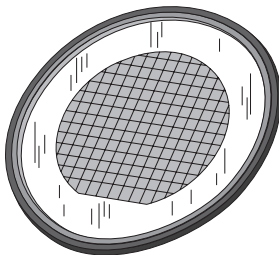
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### UNSAWN WAFER

**WN:** Full wafer, unsawn, 100% tested with reject die inked.  
(example: CP211-PART NUMBER-WN)

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### SAWN WAFER ON PLASTIC RING

**WR:** Full wafer, sawn and mounted on plastic ring,  
100% tested with reject die inked.  
(example: CP211-PART NUMBER-WR)

Please note: Sawn Wafer on Metal Frame (WS) is possible as a special order. Please contact your Central Sales Representative at 631-435-1110.



Visit the Central website for a complete listing of specifications:  
[www.centrasemi.com/bdspecs](http://www.centrasemi.com/bdspecs)

## OUTSTANDING SUPPORT AND SUPERIOR SERVICES



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### PRODUCT SUPPORT

Central's operations team provides the highest level of support to insure product is delivered on-time.

- Supply management (Customer portals)
- Inventory bonding
- Consolidated shipping options
- Custom bar coding for shipments
- Custom product packing

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### DESIGNER SUPPORT/SERVICES

Central's applications engineering team is ready to discuss your design challenges. Just ask.

- Free quick ship samples (2<sup>nd</sup> day air)
- Online technical data and parametric search
- SPICE models
- Custom electrical curves
- Environmental regulation compliance
- Customer specific screening
- Up-screening capabilities
- Special wafer diffusions
- PbSn plating options
- Package details
- Application notes
- Application and design sample kits
- Custom product and package development

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### REQUESTING PRODUCT PLATING

1. If requesting Tin/Lead plated devices, add the suffix " TIN/LEAD" to the part number when ordering (example: 2N2222A TIN/LEAD).
2. If requesting Lead (Pb) Free plated devices, add the suffix " PBFREE" to the part number when ordering (example: 2N2222A PBFREE).

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### CONTACT US

#### Corporate Headquarters & Customer Support Team

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