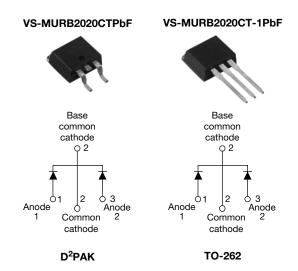


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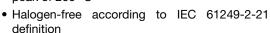
Ultrafast Rectifier, 2 x 10 A FRED Pt®

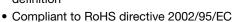


PRODUCT SUMMARY					
t _{rr}	25 ns				
I _{F(AV)}	2 x 10 A				
V_{R}	200 V				

FEATURES

- Ultrafast recovery time
- Low forward voltage drop
- Low leakage current
- 175 °C operating junction temperature
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C





• AEC-Q101 qualified





DESCRIPTION/APPLICATIONS

MUR.. series are the state of the art ultrafast recovery rectifiers specifically designed with optimized performance of forward voltage drop and ultrafast recovery time.

The planar structure and the platinum doped life time control, guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Peak repetitive reverse voltage		V _{RRM}		200	V	
Average rectified forward ourrent	per leg			10		
Average rectified forward current t	total device	I _{F(AV)}	Rated V _R , T _C = 145 °C	20	^	
Non-repetitive peak surge current per l	leg	I _{FSM}		100	Α	
Peak repetitive forward current per leg		I _{FM}	Rated V _R , square wave, 20 kHz, T _C = 145 °C	20		
Operating junction and storage temper	ratures	T _J , T _{Stg}		- 65 to 175	°C	

ELECTRICAL SPEC	ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	200	-	-			
		I _F = 8 A, T _J = 125 °C	-	-	0.85	V		
Forward voltage	V_{F}	I _F = 16 A	-	-	1.15			
		I _F = 16 A, T _J = 125 °C	-	-	1.05			
Deverse leekens eurment		$V_R = V_R$ rated	-	-	15			
Reverse leakage current	I _R	$T_J = 150 ^{\circ}\text{C}, V_R = V_R \text{ rated}$	-	-	250	μΑ		
Junction capacitance	C _T	V _R = 200 V	-	55	-	pF		
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH		

Document Number: 94083 Revision: 08-Apr-10

Vishay High Power Products

Ultrafast Rectifier, 2 x 10 A FRED Pt®



DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt =$	$50 \text{ A/}\mu\text{s}, \text{ V}_{\text{R}} = 30 \text{ V}$	-	1	35	
Reverse recovery time	t _{rr} -	$I_F = 0.5 A, I_R = 1.0$	$I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{REC} = 0.25 \text{ A}$		-	25	ns
neverse recovery time		T _J = 25 °C		-	21	-	115
		T _J = 125 °C	$I_F = 10 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 160 \text{ V}$	-	35	-	
Peak recovery current	1	T _J = 25 °C		-	1.9	-	Α
Feak recovery current	IRRM	T _J = 125 °C		-	4.8	-	A
Dovorgo rocovení oborgo	Q _{rr}	T _J = 25 °C		-	25	-	nC
Reverse recovery charge		T _J = 125 °C		-	78	-	IIC

THERMAL - MECHAN	ICAL SPE	CIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		- 65	-	175	°C
Thermal resistance, junction to case per leg	R _{thJC}		-	-	2.5	
Thermal resistance, junction to ambient per leg	R _{thJA}		-	-	50	°C/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.5	-	
Maiabt			-	2.0	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Mad to de to		Case style D ² PAK		MURB	2020CT	
Marking device		Case style TO-262		MURB2	020CT-1	

Document Number: 94083 Revision: 08-Apr-10



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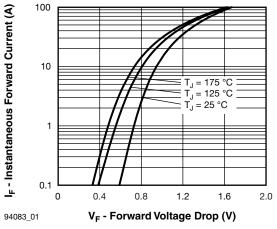


Fig. 1 - Typical Forward Voltage Drop Characteristics

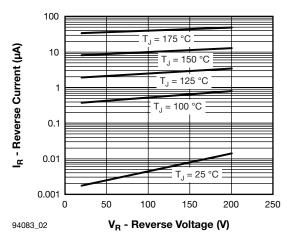


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

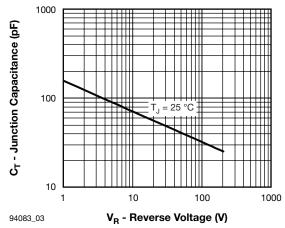


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

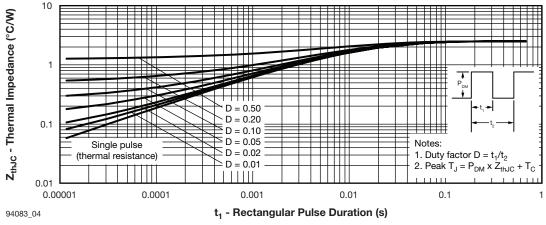


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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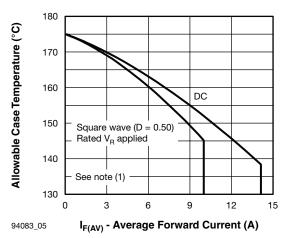


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

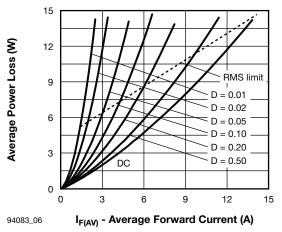


Fig. 6 - Forward Power Loss Characteristics

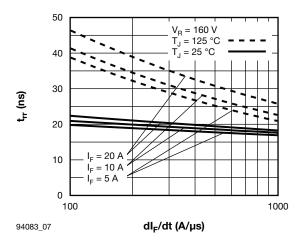


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

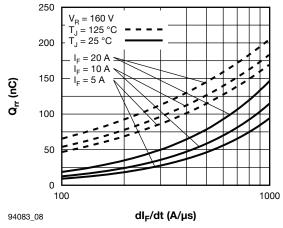


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{th,JC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \times V_{FM} \text{ at } (I_{F(AV)}/D) \text{ (see fig. 6)}; \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \times I_R \text{ (1 - D); } I_R \text{ at } V_{R1} = \text{Rated } V_R \\ \end{array}$



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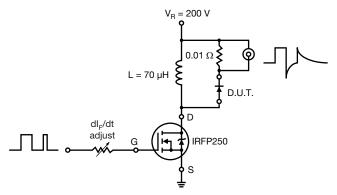
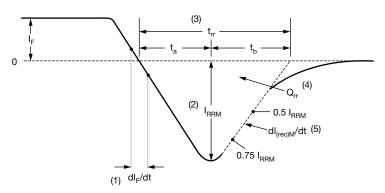


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) $t_{\rm rr}$ reverse recovery time measured from zero crossing point of negative going $I_{\rm F}$ to point where a line passing through 0.75 $I_{\rm RRM}$ and 0.50 $I_{\rm RRM}$ extrapolated to zero current.
- (4) $\rm Q_{rr}$ area under curve defined by $\rm t_{rr}$ and $\rm I_{RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dl_{(rec)M}/dt - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

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ORDERING INFORMATION TABLE

Device code

VS-	MUR	В	20	20	СТ	-1	TRL	PbF
1	2	3	4	5	6	7	8	9

1 - HPP product suffix

- Ultrafast MUR series

3 - B = D^2 PAK/TO-262

4 - Current rating (20 = 20 A)

Voltage rating (20 = 200 V)

- CT = Center tap (dual) TO-220/D²PAK/TO-262

7 - • -1 = TO-262

• None = D²PAK

None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D²PAK package)

• TRR = Tape and reel (right oriented, for D²PAK package)

9 - • PbF = Lead (Pb)-free

• P = Lead (Pb)-free (for D²PAK TRR and TRL)

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95014			
Part marking information	www.vishay.com/doc?95008			
Packaging information	www.vishay.com/doc?95032			

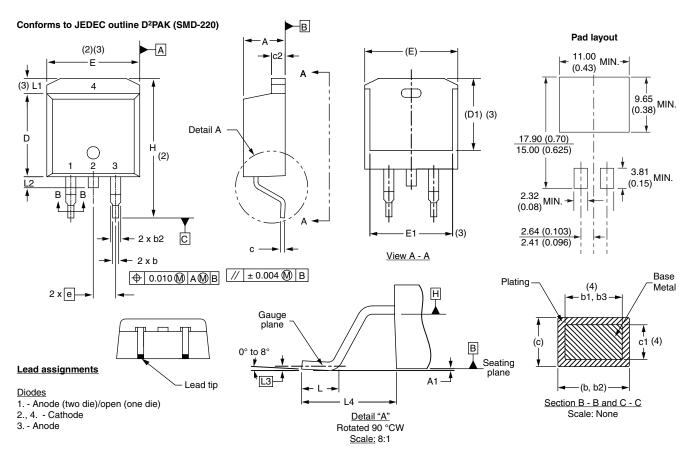
Document Number: 94083 Revision: 08-Apr-10



Vishay High Power Products

D²PAK, **TO-262**

DIMENSIONS FOR D²PAK in millimeters and inches



CVMDOL	MILLIM	MILLIMETERS		INCHES	
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.06	4.83	0.160	0.190	
A1	0.00	0.254	0.000	0.010	
b	0.51	0.99	0.020	0.039	
b1	0.51	0.89	0.020	0.035	4
b2	1.14	1.78	0.045	0.070	
b3	1.14	1.73	0.045	0.068	4
С	0.38	0.74	0.015	0.029	
c1	0.38	0.58	0.015	0.023	4
c2	1.14	1.65	0.045	0.065	
D	8.51	9.65	0.335	0.380	2

SYMBOL	MILLIM	IETERS	INC	HES	NOTES
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D1	6.86	8.00	0.270	0.315	3
E	9.65	10.67	0.380	0.420	2, 3
E1	7.90	8.80	0.311	0.346	3
е	2.54	BSC	0.100	BSC	
Н	14.61	15.88	0.575	0.625	
L	1.78	2.79	0.070	0.110	
L1	-	1.65	-	0.066	3
L2	1.27	1.78	0.050	0.070	
L3	0.25	BSC	0.010	BSC	
L4	4.78	5.28	0.188	0.208	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5 M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- $^{(3)}\,$ Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Datum A and B to be determined at datum plane H
- (6) Controlling dimension: inch

(7) Outline conforms to JEDEC outline TO-263AB

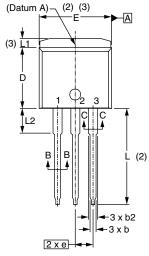
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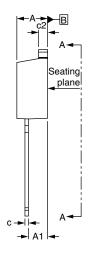
D²PAK, TO-262

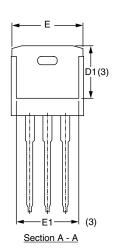


DIMENSIONS FOR TO-262 in millimeters and inches

Modified JEDEC outline TO-262 (Datum A) (2) (3)







⊕ 0.010**⋒**|A**⋒**|B

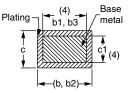
Lead assignments



Diodes

1. - Anode (two die)/open (one die) 2., 4. - Cathode

3. - Anode



Section B - B and C - C Scale: None

CVMDOL	MILLIMETERS		INC	INCHES		
SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	
Α	4.06	4.83	0.160	0.190		
A1	2.03	3.02	0.080	0.119		
b	0.51	0.99	0.020	0.039		
b1	0.51	0.89	0.020	0.035	4	
b2	1.14	1.78	0.045	0.070		
b3	1.14	1.73	0.045	0.068	4	
С	0.38	0.74	0.015	0.029		
c1	0.38	0.58	0.015	0.023	4	
c2	1.14	1.65	0.045	0.065		
D	8.51	9.65	0.335	0.380	2	
D1	6.86	8.00	0.270	0.315	3	
Е	9.65	10.67	0.380	0.420	2, 3	
E1	7.90	8.80	0.311	0.346	3	
е	2.54	BSC	0.100) BSC		
L	13.46	14.10	0.530	0.555		
L1	-	1.65	-	0.065	3	
L2	3.56	3.71	0.140	0.146		

Notes

- (1) Dimensioning and tolerancing as per ASME Y14.5M-1994
- (2) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body
- (3) Thermal pad contour optional within dimension E, L1, D1 and E1
- (4) Dimension b1 and c1 apply to base metal only
- (5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) and D1 (minimum) where dimensions derived the actual package outline



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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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