

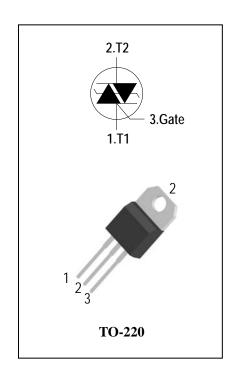
# AC Thyristor Triac power switch

#### **General Description**

Available either in through-hole or surface-mount packages, the AACT8 suitable for general purpose AC switching. They can be used as an ON/OFF function in applications such as static relays, heating regulation, induction motor starting circuits... or for phase control operation in light dimmers, motor speed controllers....

#### **Features**

- ◆ Repetitive Peak Off-State Voltage: 800Vand1000V
- ◆ R.M.S On-State Current ( I<sub>T(RMS)</sub>= 8A )
- ◆Very high immunity to false turn-on by dV/dt
- ◆Triggering in three quadrants only
- ◆Pin compatible with standard triacs
- ◆Safe clamping capability for low energy over-voltage transients
- ◆ These Devices are Pb-Free and are RoHS Compliant



# **Absolute Maximum Ratings**

Symbol	Items	Conditions		Ratings	Unit
$V_{DRM}$	Denotitive Deak Off State Valtage	T: - 25°C	AACT808	800	V
$V_{RRM}$	Repetitive Peak Off-State Voltage	Tj = 25°C	AACT810	1000	V
I <sub>T(RMS)</sub>	R.M.S On-State Current	T <sub>C</sub> = 103 °C		8	Α
I <sub>TSM</sub>	Surge On-State Current	tp=20ms(50Hz)/tp=16.7ms(60Hz)		80/84	Α
l <sup>2</sup> t	I <sup>2</sup> t for fusing	tp=10ms		32	A <sup>2</sup> s
-11/-14	Critical rate of rise of on-state F = 120 Hz Tj = 125°C			400	A /
dl/dt	current	$I_G = 2 \times I_{GT}$ , tr $\leq 100 \text{ ns}$	100	A/µs	
I <sub>GM</sub>	Peak Gate Current	tp = 20 μs Tj = 125°C		1	Α
$P_{G(AV)}$	Average Gate Power Dissipation(Tj=125°C)			0.1	W
$P_GM$	Peak Gate Power Dissipation(tp=20us,Tj=125°C)			5	W
Tj	Operating Junction Temperature			- 40 ~ 125	°C
T <sub>STG</sub>	Storage Temperature			- 40 ~ 150	°C





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## **Electrical Characteristics** (Tj = 25°C unless otherwise specified)

Symbol	Items		Conditions		AACT808/10		Unit
					S	Blank	]
I <sub>DRM</sub>	Peak Forward Reverse Blocking		V <sub>DRM</sub> = V <sub>RRM</sub> , Tj = 25°C		10		uA
I <sub>RRM</sub>	Current		$V_{DRM} = V_{RRM}$ , $Tj = 125$ °C	Max.	1.25		mA
V <sub>TM</sub>	Peak On-State Voltage		I <sub>TM</sub> = 11A, t <sub>p</sub> = 380 μs	Max.	1.55		V
$V_{GD}$	Q1-Q2-Q3	Non-Trigger Gate Voltage	$V_D = 2/3V_{DRM} R_L = 3.3 \text{ k}\Omega$ $Tj = 125^{\circ}\text{C}$	Min.	0.2		V
$V_{GT}$	Q1-Q2-Q3	Gate Trigger Voltage	V = 40V D = 220	Max.	1.5		V
I <sub>GT</sub>	Q1-Q2-Q3	Gate Trigger Current	$V_D = 12V$ , $R_L = 33\Omega$	Max.	10	35	mA
I <sub>H</sub>	Q1-Q2-Q3	Holding Current	I <sub>T</sub> = 0.1A	Max.	25	40	mA
	Q1-Q3	Latching Current	I <sub>G</sub> = 1.2 I <sub>GT</sub>	Max.	25	40	mA
IL	Q2				30	55	
dV/dt			$V_D = 2/3V_{DRM}$ gate open Tj = 125°C	Min.	600	1000	V/µs
R <sub>th(j-c)</sub>	Junction to case (AC)			Max.	1.6		°C/W
R <sub>th(j-a)</sub>	Junction to ambient			Max.	60		°C/W

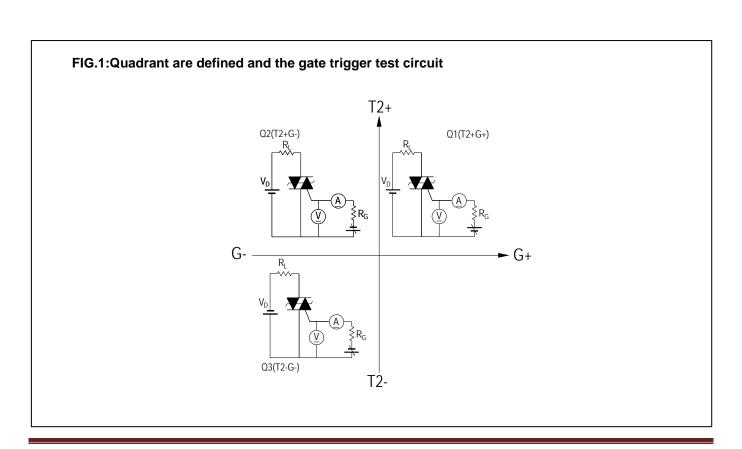




FIG.2: Maximum on-state power dissipation

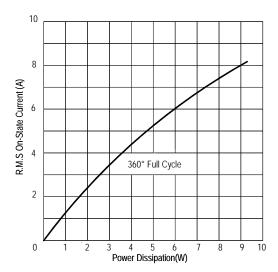


FIG.4: Maximum transient thermal impedance

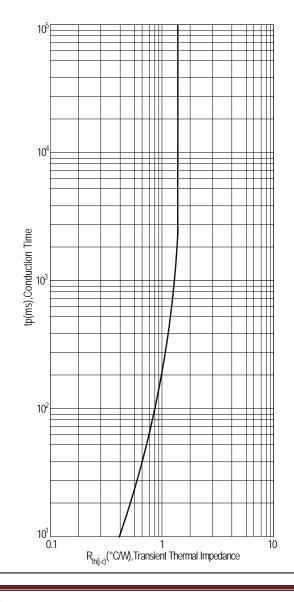


FIG.3: Typical RMS on-state current VS Allowable case Temperature

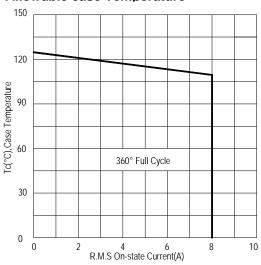


FIG.5: Rated surge on-state current (Non-Repetitive)

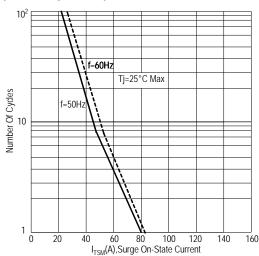


FIG.6: Gate trigger current VS Junction temperature

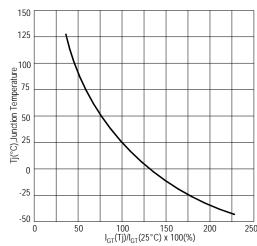




FIG.7:Holding current and Latching current VS Junction temperature

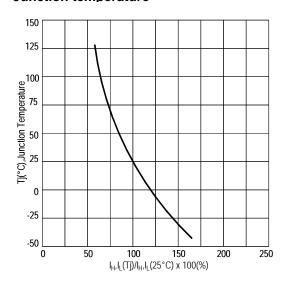


FIG.8: Gate trigger voltage VS Junction temperature

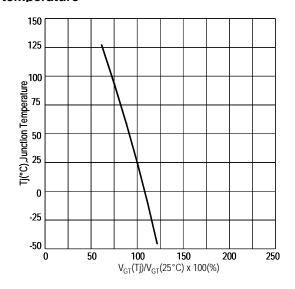
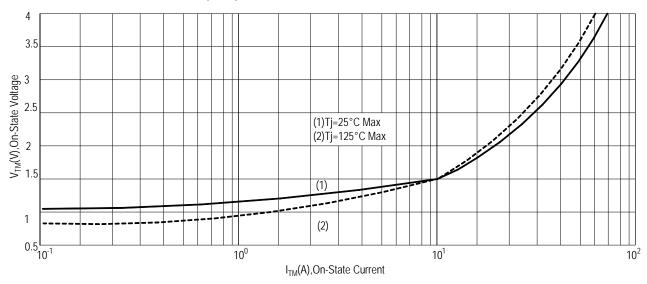


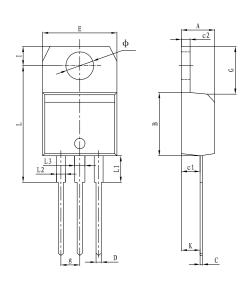
FIG.9: On-state characteristics(Max)



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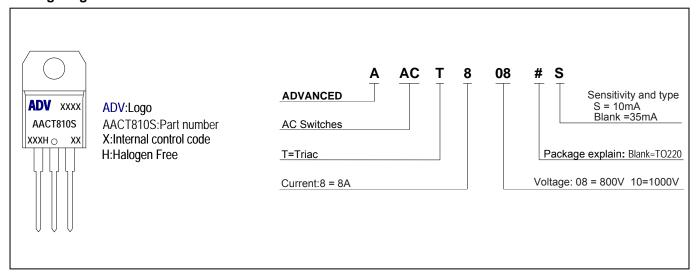


# PACKAGE MECHANICAL DATA TO-220 Package Dimension



	Dimensions		Dimensions		
Symbol	In Millimeters		In Inches		
	Min	Max	Min	Max	
А	4.40	4.60	0.173	0.181	
В	9.00	9.30	0.354	0.366	
С	0.40	0.60	0.015	0.023	
c1	2.00	2.60	0.078	0.102	
c2	1.23	1.32	0.048	0.051	
D	0.70	1.00	0.027	0.039	
E	10.00	10.40	0.393	0.409	
g	2.40	2.70	0.094	0.106	
G	6.20	6.80	0.244	0.267	
I	2.65	2.95	0.104	0.116	
L	15.80	16.80	0.622	0.661	
L1	3.75		0.147		
L2	1.14	1.70	0.044	0.066	
L3	1.14	1.70	0.044	0.066	
Ф	3.60	3.90	0.141	0.153	
K	2.60TYP		0.102TYP		

#### **Making Diagram**



### Ordering information

Part number	Package	Marking	Packing	Quantity	
AACT808#	TO-220	AACT808#	Tube	50pcs	
AACT810#	TO-220	AACT810#	Tube	50pcs	
Note:# = Gate Trigger Current Sensitivity and type					



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