

**VOLTAGE RANGE: 100--- 1000 V**

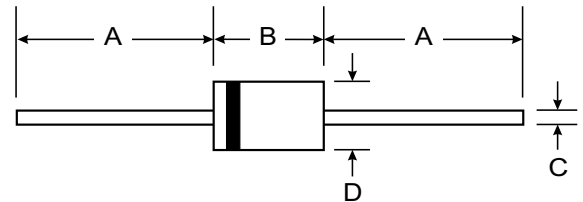
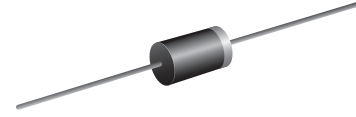
**CURRENT: 1.5 --- 0.8 A**

### Features

- Low cost
- Diffused junction
- Low leakage
- Low forward voltage drop
- Easily cleaned with freon, alcohol, Isopropand
- The plastic material carries U/L recognition 94V-0

### Mechanical Data

- Case : DO-15 Molded plastic
- Epoxy : UL94V-0 rate flame retardant
- Lead : Axial lead solderable per MIL-STD-202, Method 208 guaranteed
- Polarity : Color band denotes cathode end
- Mounting position : Any
- Weight : 0.465 gram



DO-15		
Dim	Min	Max
A	25.40	—
B	5.50	7.62
C	0.686	0.889
D	2.60	3.60
All Dimensions in mm		



### Maximum Ratings and Electrical Characteristics T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

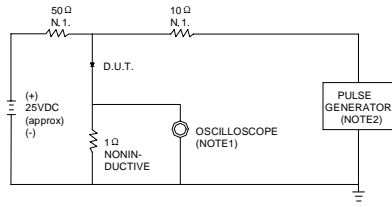
		RU2YX	RU2Y	RU2	RU2B	RU2C	UNITS
Maximum peak repetitive reverse voltage	V <sub>RRM</sub>	100	200	600	800	1000	V
Maximum RMS voltage	V <sub>RMS</sub>	70	140	420	560	700	V
Maximum DC blocking voltage	V <sub>DC</sub>	100	200	600	800	1000	V
Maximum average forward rectified current 9.5mm lead length, @T <sub>A</sub> =75°C	I <sub>F(AV)</sub>	1.5	1.0			0.8	A
Peak forward surge current 10ms single half-sine-wave superimposed on rated load @T <sub>J</sub> =125°C	I <sub>FSM</sub>	30.0	20.0				A
Maximum instantaneous forward voltage @ I <sub>F</sub> =I <sub>F(AV)</sub>	V <sub>F</sub>	0.95	1.5				V
Maximum reverse current @T <sub>A</sub> =25°C at rated DC blocking voltage @T <sub>A</sub> =100°C	I <sub>R</sub>		10.0 300.0				μ A
Maximum reverse recovery time (Note1)	t <sub>rr</sub>	50	100				ns
Typical junction capacitance (Note2)	C <sub>J</sub>	50		30			pF
Typical thermal resistance (Note3)	R <sub>θJL</sub>	15					°C/W
Operating junction temperature range	T <sub>J</sub>	- 55 ----- + 150					°C
Storage temperature range	T <sub>STG</sub>	- 55 ----- + 150					°C

NOTE: 1. Measured with I<sub>F</sub>=0.5A, I<sub>R</sub>=1A, I<sub>rr</sub>=0.25A.

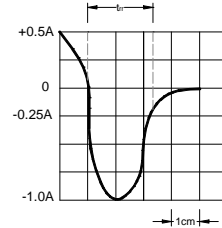
2. Measured at 1.0MHz and applied reverse voltage of 4.0V DC.

3. Thermal resistance junction ambient

**FIG.1 – TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC**

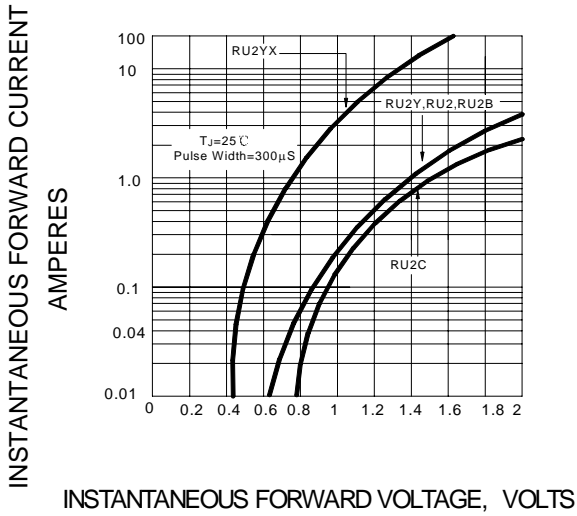


NOTES:1. RISE TIME = 7ns MAX.INPUT IMPEDANCE = 1MΩ. 22pF.  
2. RISE TIME = 10ns MAX.SOURCE IMPEDANCE = 50 Ω.

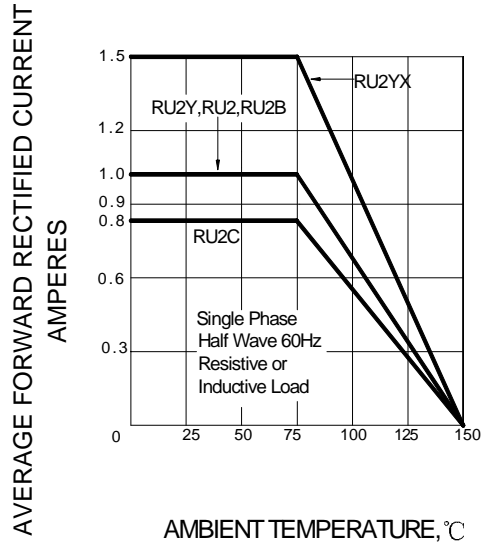


SET TIME BASE FOR 10/20 ns/cm

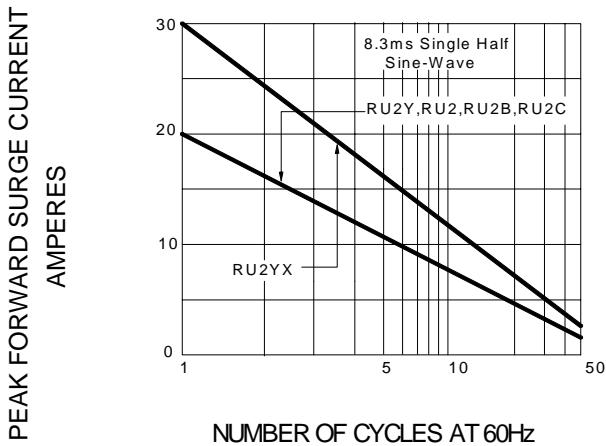
**FIG.2 – TYPICAL FORWARD CHARACTERISTIC**



**FIG.3 – FORWARD DERATING CURVE**



**FIG.4 – PEAK FORWARD SURGE CURRENT**



**FIG.5 – TYPICAL JUNCTION CAPACITANCE**

