

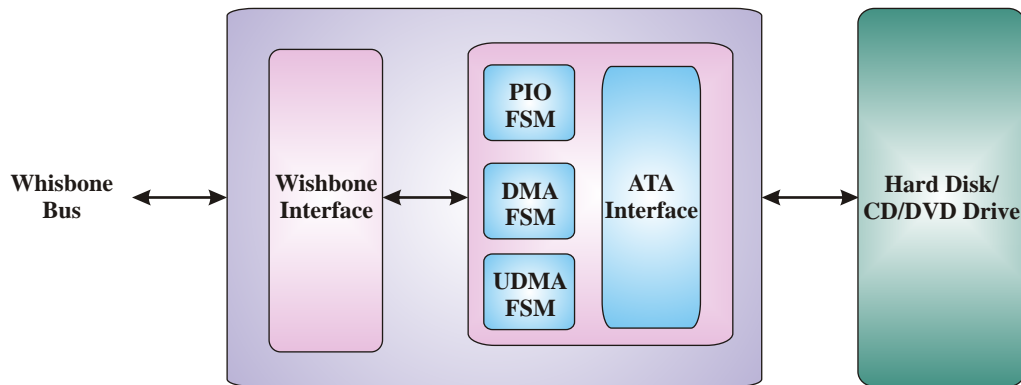
Overview

iW-ATA/ATAPI Controller core provides a simple interface towards ATA/ATAPI devices. This core can be used to control ATA/ATAPI memory devices such as hard-disk drives, CD/DVD players.

Features

- ❖ One IDE bus
- ❖ Parallel ATA/ATAPI-5 mode of operation
- ❖ ATA PIO mode 0,1,2,3, and 4
- ❖ ATAMulti Word DMA mode 0, 1 and 2
- ❖ ATAUDMA mode 0, 1, 2,3 and 4
- ❖ Wishbone interface
- ❖ 28-bit LBA mode of addressing

Block Diagram



Device Utilization Summary

Actel Family	Device	Tiles	Core Utilization	I/O cells	RAM Utilization	Frequency	Power
ProASIC3	A3P125	1708	55.60%	103	50%	100 MHz	618.867 mW
IGLOO	AGL125	1701	55.37%	103	50%	100 MHz	123.319 mW

Benefits

- ❖ Embedded applications in networking and Telecommunication Systems
- ❖ High speed, high performance peripheral applications

Deliverables

- ❖ Technical Specification
- ❖ RTL Verilog Synthesizable Code
- ❖ Comprehensive Test Environment
- ❖ Technical Support and Maintenance

About Us

iWave Systems Technologies is an embedded Hardware and Software Turnkey Design Services company, focused on providing integrated solutions for developing innovative products and systems in the areas of Communication, Consumer electronics and Multimedia. iWave offers complete turnkey solutions for systems engineering and product development.

Contact Us

India: iWave Systems Technologies Pvt. Ltd., 7/B, 29th Main, BTM Layout 2nd Stage, Bangalore 560 076. INDIA
Ph : +91-80-26683700, 26786245, Fax : +91-80-26685200
Email : mktg@iwavesystems.com, Web : www.iwavesystems.com

Japan: iWave Japan, Inc. 8F Kannai Sumiyoshi Building 3-29, Sumiyoshi-cho, Naka-ku, Yokohama Kanagawa, Japan.
Ph : +81-45-227-7626, Fax : +81-45-227-7646, Email : info@iwavejapan.co.jp, Web : www.iwavejapan.co.jp

Note : iwave reserves the right to change these specifications without notice as part of iwave's continuous effort to meet the best of breed specification. The registered trade marks are properties of their respective owners.

Supply of this core does not convey nor imply a right under the patent rights of the respective patent holders, if any, to make use or sell any product employing these patent rights. A patent license from respective patent holder may be required for any use of such patent rights, including the implementation of the core in an FPGA or an Integrated Circuit or any other device.