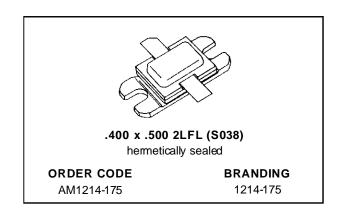


AM1214-175

RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 3:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 160 W MIN. WITH 7.3 dB GAIN

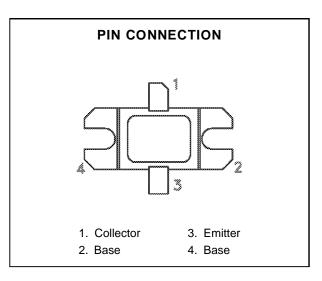


DESCRIPTION

The AM1214-175 device is a high power Class C transistor specifically designed for L-Band radar pulsed output and driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles and temperatures and is capable of withstanding 3:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

The AM1214-175 is supplied in the BIGPAC™ Hermetic Metal/Ceramic package with internal Input/Output matching structures.



ABSOLUTE MAXIMUM RATINGS $(T_{case} = 25^{\circ}C)$

Symbol	Parameter	Value	Unit
PDISS	Power Dissipation* (T _C ≤ 100°C)	330	W
Ic	Device Current*	14	А
Vcc	Collector-Supply Voltage*	45	V
TJ	Junction Temperature (Pulsed RF Operation)	250	°C
T _{STG}	Storage Temperature	- 65 to +200	°C

THERMAL DATA

_				
	R _{TH(j-c)}	Junction-Case Thermal Resistance*	0.45	°C/W

^{*}Applies only to rated RF amplifier operation

September 1992

ELECTRICAL SPECIFICATIONS $(T_{case} = 25^{\circ}C)$

STATIC

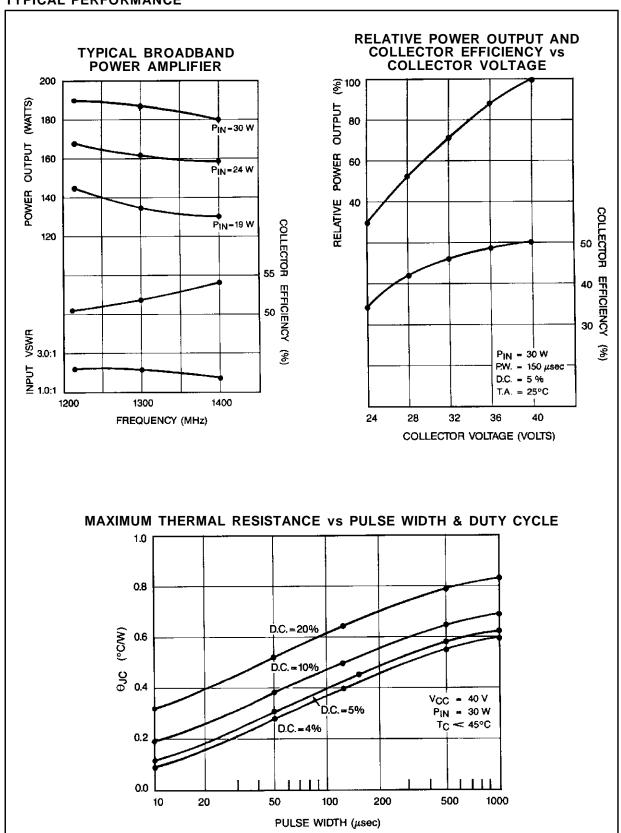
Symbol	Test Conditions		Value		
		Min.	Тур.	Max.	Unit
BV _{CBO}	$I_C = 60 \text{mA}$ $I_E = 0 \text{mA}$	65	_	_	V
BV _{EBO}	$I_E = 10mA$ $I_C = 0mA$	3.5	_	_	V
BVces	IC = 100mA	65	_	_	V
ICES	V _{CE} = 40V	_	_	25	mA
h _{FE}	$V_{CE} = 5V$ $I_{C} = 5A$	15	_	150	_

DYNAMIC

Symbol	Test Conditions		Value		Unit		
Symbol	rest Conditions			Min.	Тур.	Max.	Unit
Pout	f = 1215 — 1400MHz	$P_{IN} = 30W$	$V_{CC} = 40V$	160	180		W
ης	f = 1215 — 1400MHz	$P_{IN} = 30W$	$V_{CC} = 40V$	45	50		%
G _P	f = 1215 — 1400MHz	P _{IN} = 30W	Vcc = 40V	7.3	7.8	_	dB

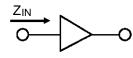
Note: Pulse Width = 150μ S Duty Cycle = 5%

TYPICAL PERFORMANCE

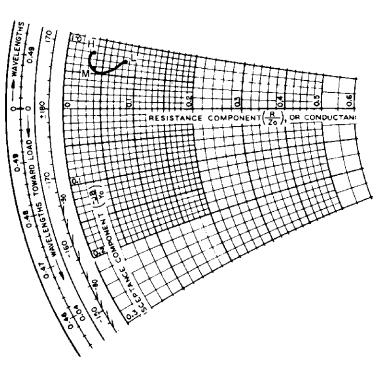


IMPEDANCE DATA



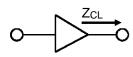


 $\begin{aligned} P_{IN} &= 30 \text{ W} \\ V_{CC} &= 40 \text{ V} \\ Z_0^* &= 50 \text{ ohms} \end{aligned}$



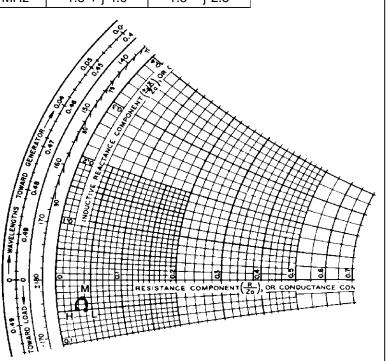
FREQ.	Z _{IN} (Ω)	Z _{CL} (Ω)
L = 1215 MHz	4.0 + j 3.5	2.0 – j 2.5
M = 1300 MHz	2.0 + j 3.0	2.0 – j 1.5
H = 1400 MHz	1.5 + j 4.0	1.5 – j 2.5

TYPICAL COLLECTOR LOAD IMPEDANCE

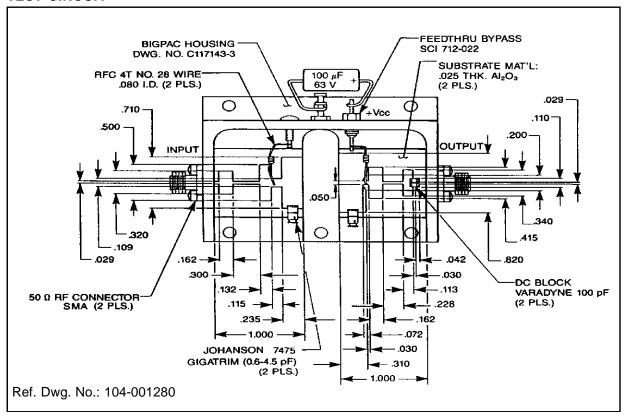


 $P_{IN} = 30 \text{ W}$ $V_{CC} = 40 \text{ V}$ $Z_0^* = 50 \text{ ohms}$

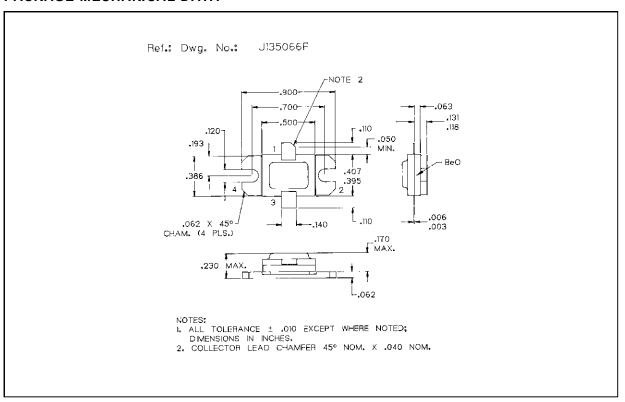
*Normalized Impedance



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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