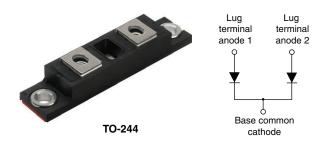
VS-HFA140NJ60CPbF

Vishay Semiconductors

HEXFRED[®] Ultrafast Soft Recovery Diode, 167 A



PRIMARY CHARACTERISTICS				
I _F (maximum)	167 A			
V _R	600 V			
I _{F(DC)} at T _C	84 A at 100 °C			
Package	TO-244			
Circuit configuration	Two diodes common cathode			

FEATURES

- Very low Q_{rr} and t_{rr}
- UL approved file E222165
- · Designed and qualified for industrial level
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

BENEFITS

- Reduced RFI and EMI
- Reduced snubbing

DESCRIPTION / APPLICATIONS

HEXFRED[®] diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems. An extensive characterization of the recovery behavior for different values of current, temperature and dl_F/dt simplifies the calculations of losses in the operating conditions. The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for power converters, motors drives and other applications where switching losses are significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS	
Cathode to anode voltage	V _R		600	V	
Continuous forward current I _F		$T_{C} = 25 \ ^{\circ}C$	167		
		T _C = 100 °C	84	А	
Single pulse forward current	I _{FSM}	Limited by junction temperature	400		
Non-repetitive avalanche energy	E _{AS}	L = 100 $\mu H,$ duty cycle limited by maximum $T_{\rm J}$	330	μJ	
Maximum power dissipation	P _D	$T_{C} = 25 \ ^{\circ}C$	310	W	
		T _C = 100 °C	132	vv	
Operating junction and storage temperature range	T _J , T _{Stg}		-55 to +150	°C	

ELECTRICAL SPECIFICATIONS PER LEG ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Cathode to anode breakdown voltage	V _{BR}	I _R = 100 μA		600	-	-	
		I _F = 70 A		-	1.37	1.89	V
Maximum forward voltage	V _{FM}	I _F = 140 A	See fig. 1	-	1.58	2.1	
		I _F = 70 A, T _J = 125 °C		-	1.29	1.54	
Maximum reverse leakage current	I _{RM}	$T_J = 125 \text{ °C}, V_R = 480 \text{ V}$	See fig. 2	-	1.2	4	mA
Junction capacitance	CT	V _R = 200 V	See fig. 3	-	140	250	pF
Series inductance	L _S	From top of terminal hole to mounting plane - 7.0 - n		nH			

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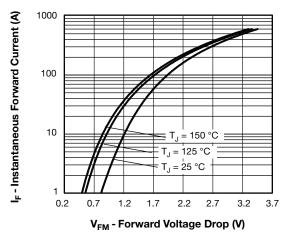
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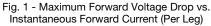
DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t =$	\approx 200 A/µs, V _R = 30 V	-	33	-	ns	
Reverse recovery time (fig. 5)	t _{rr}	T _J = 25 °C	$l_{\rm F} = 70 {\rm A}$	-	80	120		
		T _J = 125 °C		-	140	220		
Peak recovery current (fig. 6)		$T_J = 25 \ ^\circ C$		-	8.5	15		
Feak recovery current (lig. 6)	I _{RRM}	T _J = 125 °C		-	14	25	A	
Bowerse receivery charge (fig. 7)			T _J = 25 °C	dI _F /dt = 200 A/µs V _R = 200 V	-	340	900	nC
Reverse recovery charge (fig. 7)	Q _{rr}	T _J = 125 °C		-	980	2300	no	
Peak rate of recovery current (fig. 8) dI _{(rec)M} /dt	dl (dt	T _J = 25 °C		-	300	-	A/µs	
	T _J = 125 °C		_	220	-	λγμs		

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	-55	-	150	°C
Thermal resistance, junction to case	per leg	P	-	-	0.38	°C/W K/W
merma resistance, junction to case	per module	– R _{thJC}	-	-	0.19	
Typical thermal resistance, case to heatsink		R _{thCS}		0.10	-	
Weight			-	68	-	g
weight			-	2.4	-	oz.
Mounting torque ⁽¹⁾			30 (3.4)	-	40 (4.6)	
Mounting torque center hole			12 (1.4)	-	18 (2.1)	lbf · in (N · m)
Terminal torque			30 (3.4)	-	40 (4.6)	(
Vertical pull			-	-	80	lbf∙in
2" lever pull			-	-	35	חוייוטו

Note

(1) Mounting surface must be smooth, flat, free or burrs or other protrusions. Apply a thin even film or thermal grease to mounting surface. Gradually tighten each mounting bolt in 5 - 10 lbf · in steps until desired or maximum torque limits are reached





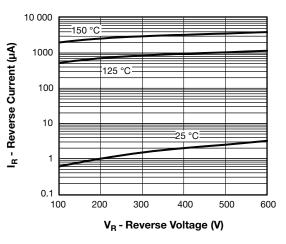


Fig. 2 - Typical Reverse Current vs. Reverse Voltage (Per Leg)

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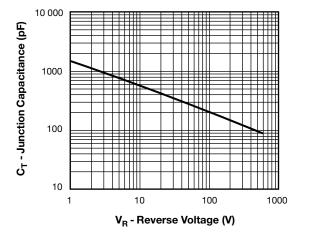


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

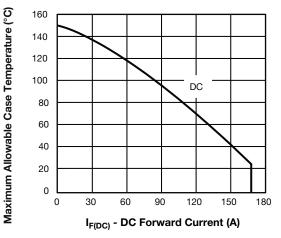


Fig. 4 - Maximum Allowable Case Temperature vs. DC Forward Current (Per Leg)

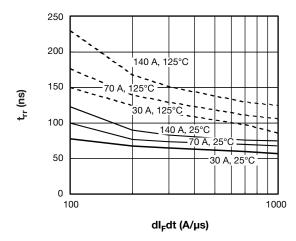


Fig. 5 - Typical Reverse Recovery Time vs. dl_F/dt (Per Leg)

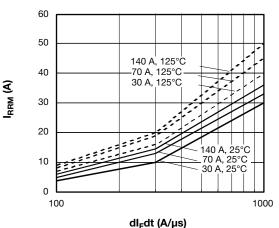


Fig. 6 - Typical Recovery Current vs. dl_F/dt (Per Leg)

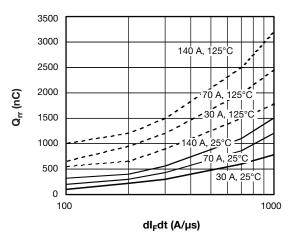


Fig. 7 - Typical Stored Charge vs. dl_F/dt (Per Leg)

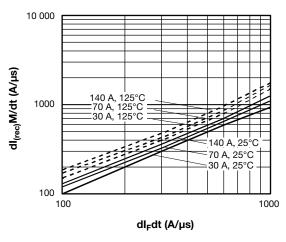


Fig. 8 - Typical dl_{(rec)M}/dt vs. dl_F/dt (Per Leg)

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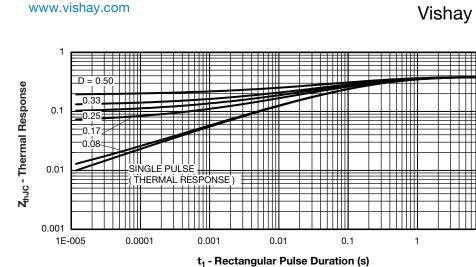


Fig. 9 - Maximum Thermal Impedance Z_{thJC} Characteristics

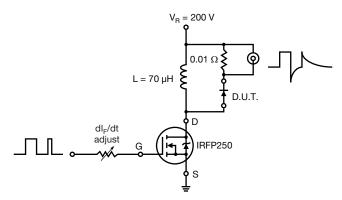


Fig. 10 - Reverse Recovery Parameter Test Circuit

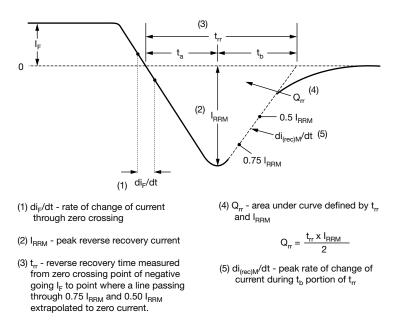


Fig. 11 - Reverse Recovery Waveform and Definitions

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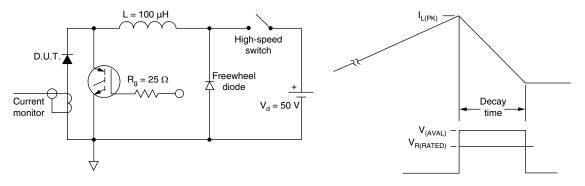
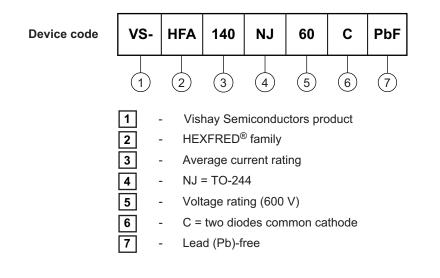


Fig. 12 - Avalanche Test Circuit and Waveforms

ORDERING INFORMATION TABLE



LINKS TO RELATED DOCUMENTS			
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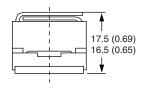


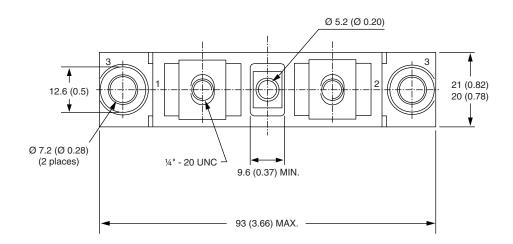
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TO-244

DIMENSIONS in millimeters (inches)









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