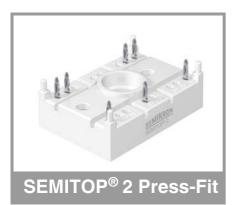
## SK20KDD12SCp



## SiC Bridge Rectifier

# Engineering Sample SK20KDD12SCp

**Target Data** 

#### **Features**

- 1200V SiC Schottky diodes
- · High frequency rectifier
- Smart pinout to ease parallel or series modules connection
- · Compact design
- One screw mounting module
- Fully compatible with other SEMITOP® Press-Fit types
- Improved thermal performance by aluminum oxide substrate
- Ultra Low inductance design
- UL recognized, file no. E63532

#### Typical Applications\*

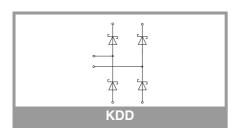
- · Solar inverter
- UPS
- Power Supply

Absolute Maximum Ratings					
Symbol	Conditions		Values	Unit	
Diode 1	•				
$V_{RRM}$	T <sub>j</sub> = 25 °C		1200	V	
I <sub>F</sub>	T <sub>j</sub> = 175 °C	$T_s = 25 ^{\circ}\text{C}$ $T_s = 70 ^{\circ}\text{C}$	18	Α	
		T <sub>s</sub> = 70 °C	14	Α	
I <sub>Fnom</sub>			10	Α	
I <sub>FSM</sub>	8.3 ms, sin 180°,		33	Α	
i <sup>2</sup> t	8.3 ms, sin 180°, T <sub>j</sub> = 150 °C		5	A <sup>2</sup> s	
Tj			-40 175	°C	

Absolute Maximum Ratings					
Symbol	Conditions	Values	Unit		
Module					
I <sub>t(RMS)</sub>	T <sub>terminal</sub> = 100 °C, T <sub>S</sub> = 60°C, per pin	40	Α		
T <sub>stg</sub>		-40 125	°C		
V <sub>isol</sub>	AC, sinusoidal, t = 1 min	2500	V		

Characteristics						
Symbol	Conditions		min.	typ.	max.	Unit
Diode 1						
$V_{F}$	I <sub>F</sub> = 10 A	T <sub>j</sub> = 25 °C		1.40	1.60	V
	chiplevel	T <sub>j</sub> = 150 °C		1.80	2.10	V
V <sub>F0</sub>	chiplevel	T <sub>j</sub> = 25 °C		0.95	1.05	V
		T <sub>j</sub> = 150 °C		0.80	0.90	V
r <sub>F</sub>	chiplevel	T <sub>j</sub> = 25 °C		45	55	mΩ
		T <sub>j</sub> = 150 °C		100	120	mΩ
C <sub>j</sub>	V <sub>R</sub> = 800 V, f = 1 MHz, T <sub>j</sub> = 25 °C			0.042		nF
Q <sub>c</sub>	$V_R = 800 \text{ V}, \text{ di/dt}_{off} = 500 \text{ A/}\mu\text{s},$ $T_i = 25 \text{ °C}$			0.034		μС
R <sub>th(j-s)</sub>	per Diode			2.65		K/W

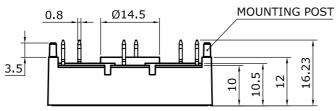
Characteristics						
Symbol	Conditions	min.	typ.	max.	Unit	
Module						
Ms	to heatsink	1.8		2	Nm	
w	weight		19		g	

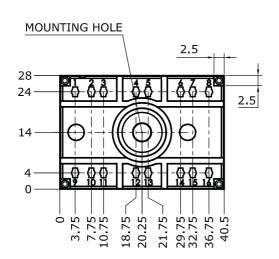


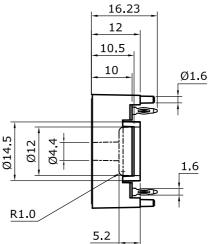
## SK20KDD12SCp

Dimensions: mm

Tolerance system: ISO 2768-m







Suggested drilled hole diameter for terminal pins in the circuit board:

minimum: 1.575 mmtypical: 1.6 mm

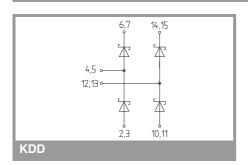
• maximum: 1.625 mm

Suggested hole diameter for the mounting post in the circuit board:

• 2 mm

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#### SEMITOP 2 Press-Fit



### SK20KDD12SCp

This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

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