

# **DATASHEET**

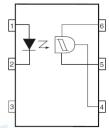
# 6 PIN DIP SCHMITT TRIGGER PHOTOCOUPLER H11LX Series



#### Features:

- High data rate, 1MHz typical (NRZ)
- Free from latch up and oscillation throughout voltage and temperature ranges.
- Microprocessor compatible drive
- Logic compatible output sinks 16mA at 0.4V maximum
- Guaranteed on/off threshold hysteresis
- Wide supply voltage capability, compatible with all popular logic systems
- High isolation voltage between input and output (Viso=5000 V rms)
- Compact dual-in-line package
- •The product itself will remain within RoHS compliant version
- •Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

### Schematic



#### Pin Configuration

- 1. Anode
- 2. Cathode
- 3. No Connection
- $4. V_{O}$
- 5. GND
- 6. V<sub>CC</sub>

#### Truth Table

Input	Output
Н	L
L	Н

#### **Description**

The H11LX series of devices each consist of a GaAs infrared emitting diode optically coupled a high speed integrated circuit detector. The output detector incorporates a Schmitt trigger, which provides hysteresis for noise immunity and pulse shaping.

The devices are in a 6-pin DIP package and available in wide-lead spacing and SMD option.

#### **Applications**

- Logic to logic isolator
- Programmable current level sensor
- Line receiver eliminate noise and transient problems
- AC to TTL conversion square wave shaping
- Digital programming of power supplies
- Interfaces computers with peripherals



## **Absolute Maximum Ratings (Ta=25°C)**

	Parameter	Symbol	Rating	Unit
	Forward current	I <sub>F</sub>	60	mA
Input	Reverse voltage	$V_R$	6	V
	Power dissipation	$P_{D}$	120	mW
Output	V <sub>45</sub> Allowed Range	Vo	0 to 16	V
	V <sub>65</sub> Allowed Range	V <sub>CC</sub>	3 to 16	V
	Output Current	Io	50	mA
	Power dissipation	P <sub>D</sub>	150	mW
Total power	r dissipation	P <sub>tot</sub>	250	mW
Isolation voltage		V <sub>iso</sub>	5000	V rms
Operating temperature		T <sub>opr</sub>	-55~+100	°C
Storage temperature		T <sub>stg</sub>	-55~+125	°C
Soldering temperature *2		T <sub>sol</sub>	260	°C

#### Notes:

<sup>\*1</sup> AC for 1 minute, R.H.=  $40 \sim 60\%$  R.H. In this test, pins 1, 2 & 3 are shorted together, and pins 4, 5 & 6 are shorted together.

<sup>\*2</sup> For 10 seconds



#### Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	$V_{F}$	-	1.15	1.5	V	$I_F = 10mA$
Reverse Current	I <sub>R</sub>	-	-	10	μA	V <sub>R</sub> = 5V
Input capacitance	CJ	-	-	100	pF	V=0, f=1MHz

Output

Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition	
Operation Voltage Range	$V_{CC}$	3	-	15	V		
Supply Current	I <sub>CC(off)</sub>	-	1.6	5	mA	I <sub>F</sub> =0mA, Vcc=5V	
Output Current, High	$I_{OH}$	-	-	100	μΑ	I <sub>F</sub> =0mA, Vcc=Vo=15V	
Isolation Resistance	R <sub>ISO</sub>	10 <sup>11</sup>	-	-	Ω	V <sub>I-O</sub> =500VDC	

#### **Transfer Characteristics**

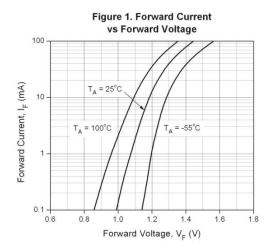
Parameter	Symbol	Min	Тур.	Max.	Unit	Condition	
Supply Current	I <sub>CC(on)</sub>	-	1.6	5	mA	I <sub>F</sub> =10mA, Vcc=5V	
Output Voltage .low	V <sub>OL</sub>	P		0.4	V	Vcc=5V, $I_F=I_{Fon}(max.)$ , $R_L=270\Omega$	
Turn on H11L1			-	1.6	_		
Threshold H11L2	I <sub>Fon</sub>	-	-	10	mA	Vcc=5V, $R_L$ =270 $\Omega$	
Current <sup>1</sup> H11L3		-	-	5			
Turn off Threshold Current	I <sub>Foff</sub>	-	1	-	mA	Vcc=5V, $R_L$ =270 $\Omega$	
Hysteresis Ratio	I <sub>Foff</sub> /I <sub>Fon</sub>	0.5	-	0.9		Vcc=5V, $R_L$ =270 $\Omega$	
Turn on Time	t <sub>on</sub>	-	-	4	μS		
Fall Time	t <sub>r</sub>	-	0.1	-	μS	Vcc=5V,	
Turn off Time	t <sub>off</sub>	-	-	4	μS	$I_{F}=I_{Fon}, \ R_{L}=270\Omega$	
Rise Time	t <sub>r</sub>	-	0.1	-	μS		
Data Rate		-	1	-	MHz		

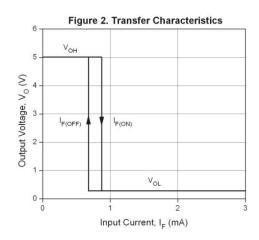
<sup>\*</sup> Typical values at T<sub>a</sub> = 25°C

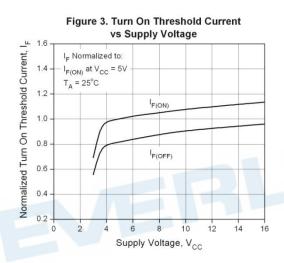
<sup>&</sup>lt;sup>1</sup>. Max. I<sub>F(ON)</sub> is the maximum current required to trigger the output. For examples, a 1.6mA maximum trigger current would require the LED to be driven at a current greater than 1.6mA to guarantee the device will turn on. A 10% guard band is recommended to account for degradation of the LED over its lifetime. The maximum allowable LED drive current is 60mA.

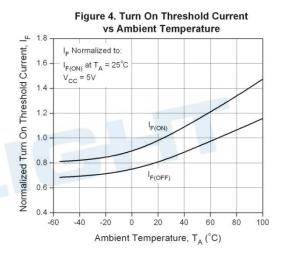


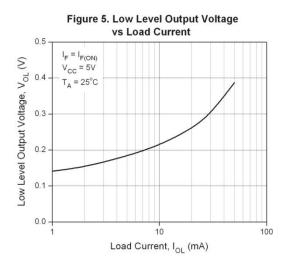
## **Typical Electro-Optical Characteristics Curves**

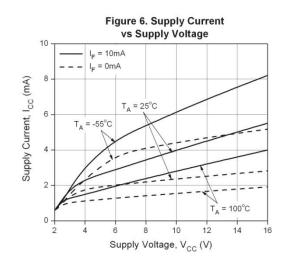














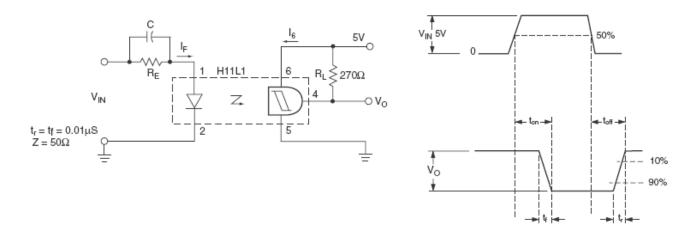


Figure 7. Switching Time Test Circuit & Waveforms

#### **Order Information**

#### **Part Number**

# H11LXY(Z)-V

Note

 $\overline{X}$  = Part No. for 1, 2 or 3

Y = Lead form option (S, S1, M or none)Z = Tape and reel option (TA, TB or none).

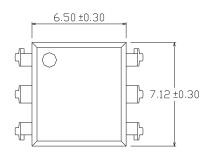
V = VDE (optional)

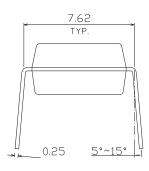
Option	Description	Packing quantity
None	Standard DIP-6	65 units per tube
М	Wide lead bend (0.4 inch spacing)	65 units per tube
S + TA	Surface mount lead form + TA tape & reel option	1000 units per reel
S + TB	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 + TA	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 + TB	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel

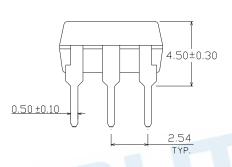


# Package Dimension (Dimensions in mm)

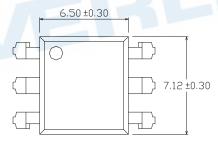
#### **Standard DIP Type**

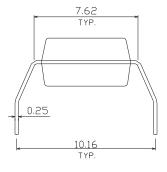


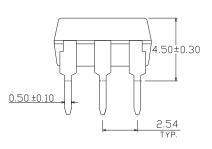




#### **Option M Type**

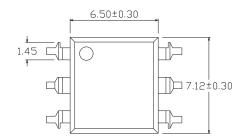


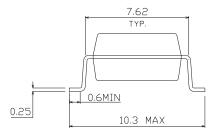


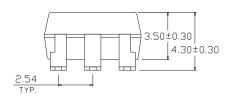




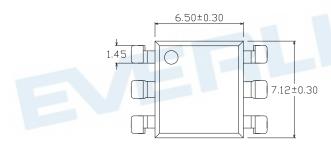
#### **Option S Type**

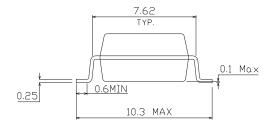


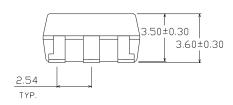




#### **Option S1 Type**

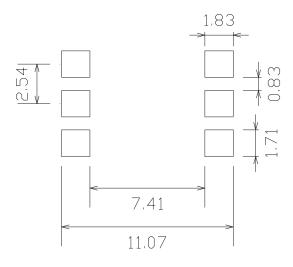








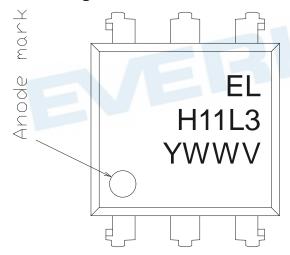
#### Recommended pad layout for surface mount leadform



#### **Notes**

Suggested pad dimension is just for reference only. Please modify the pad dimension based on individual need.

#### **Device Marking**



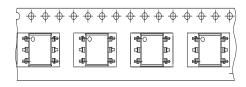
#### **Notes**

EL denotes Everlight
H11L3 denotes Device Number
Y denotes 1 digit Year code
WW denotes 2 digit Week code
V denotes VDE (optional)



**Tape & Reel Packing Specifications** 

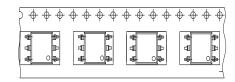
#### **Option TA**



Direction of feed from reel



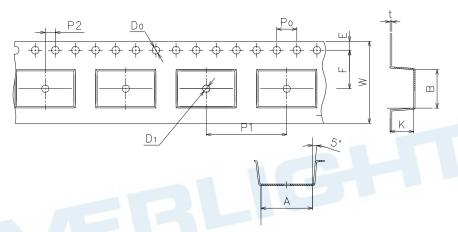
#### **Option TB**



Direction of feed from reel



#### **Tape dimensions**



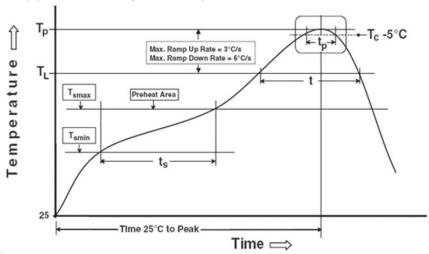
Dimension No.	Α	В	Do	D1	E	F
Dimension(mm)	10.8±0.1	7.55±0.1	1.5±0.1	1.5+0.1/-0	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	w	К
Dimension(mm)	4.0±0.15	12±0.1	2.0±0.1	0.35±0.03	16.0±0.2	4.5±0.1



#### **Precautions for Use**

#### 1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

#### **Preheat**

Temperature min (T<sub>smin</sub>)

Temperature max (T<sub>smax</sub>)

Time ( $T_{smin}$  to  $T_{smax}$ ) ( $t_s$ )

Average ramp-up rate (T<sub>smax</sub> to T<sub>p</sub>)

Other

Liquidus Temperature (T<sub>L</sub>)

Time above Liquidus Temperature (t L)

Peak Temperature (T<sub>P</sub>)

Time within 5 °C of Actual Peak Temperature: T<sub>P</sub> - 5°C

Ramp- Down Rate from Peak Temperature

Time 25°C to peak temperature

Reflow times

Reference: IPC/JEDEC J-STD-020D

150 °C

200°C

60-120 seconds

3 °C/second max

217 °C

60-100 sec

260°C

30 s

6°C /second max.

8 minutes max.

3 times

# DATASHEET 6 PIN DIP Schmitt Trigger PHOTOCOUPLER H11LX Series



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