

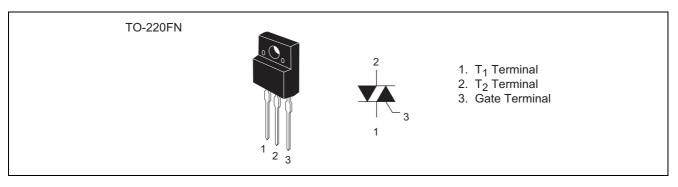
Triac Low Power Use

> REJ03G0312-0200 Rev.2.00 Nov.09.2004

### Features

- $I_{T(RMS)}$ : 3 A
- V<sub>DRM</sub> : 600 V
- $I_{FGT I}$ ,  $I_{RGT}$  I,  $I_{RGT III}$ : 15 mA (10 mA)<sup>Note3</sup>
- Insulated Type
- Planar Passivation Type
- UL Recognized : Yellow Card No. E223904 File No. E80271

### Outline



# Applications

Electric rice cooker, electric pot, and controller for other heater

## **Maximum Ratings**

| Parameter  | Symbol           | Voltage class | Unit |  |
|--|------------------|---------------|------|--|
| Falanielei   | Symbol           | Onit          |      |  |
| Repetitive peak off-state voltage <sup>Note1</sup>     | V <sub>DRM</sub> | 600           | V    |  |
| Non-repetitive peak off-state voltage <sup>Note1</sup> | V <sub>DSM</sub> | 720           | V    |  |

### BCR3KM-12

| Parameter                      | Symbol              | Ratings      | Unit             | Conditions  |  |  |
|--------------------------------|---------------------|--------------|------------------|---|--|--|
| RMS on-state current           | I <sub>T(RMS)</sub> | 3.0          | A                | Commercial frequency, sine full wave $360^{\circ}$ conduction, Tc = $111^{\circ}$ C |  |  |
| Surge on-state current         | I <sub>TSM</sub>    | 30           | A                | 60Hz sinewave 1 full cycle, peak value, non-repetitive                              |  |  |
| I <sup>2</sup> t for fusing    | l <sup>2</sup> t    | 3.7          | A <sup>2</sup> s | Value corresponding to 1 cycle of half<br>wave 60Hz, surge on-state current         |  |  |
| Peak gate power dissipation    | P <sub>GM</sub>     | 3            | W                |   |  |  |
| Average gate power dissipation | P <sub>G(AV)</sub>  | 0.3          | W                |   |  |  |
| Peak gate voltage              | V <sub>GM</sub>     | 6            | V                |   |  |  |
| Peak gate current              | I <sub>GM</sub>     | 0.5          | А                |   |  |  |
| Junction temperature           | Tj                  | - 40 to +125 | °C               |   |  |  |
| Storage temperature            | Tstg                | - 40 to +125 | °C               |   |  |  |
| Mass                           | _                   | 2.0          | g                |   |  |  |
| Isolation voltage              | V <sub>iso</sub>    | 2000         | V                | Ta = 25°C, AC 1 minute,<br>T <sub>1</sub> ·T <sub>2</sub> ·G terminal to case       |  |  |

Notes: 1. Gate open.

## **Electrical Characteristics**

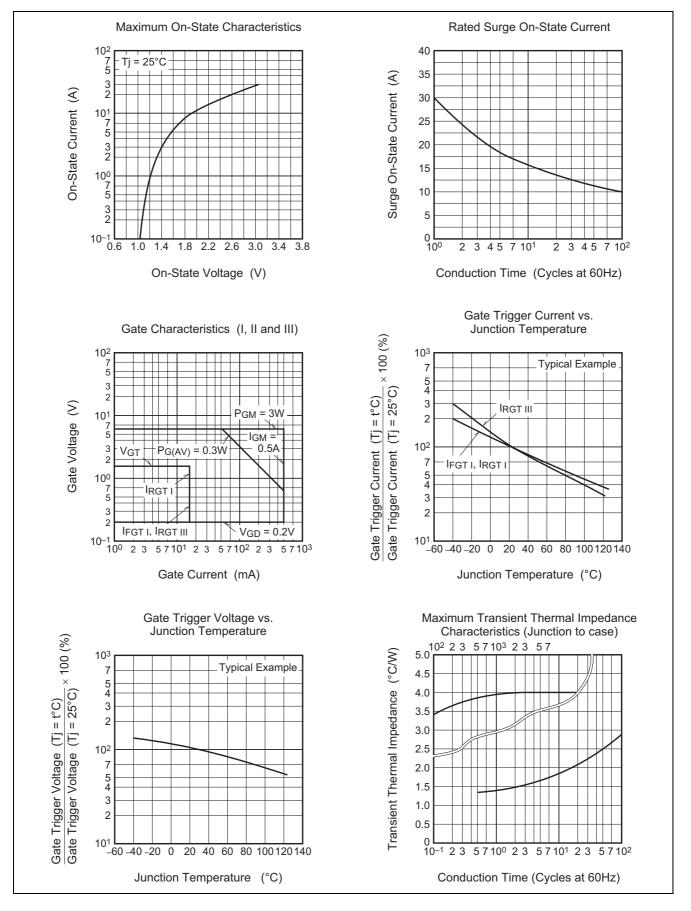
| Parameter                             |      | Symbol               | Min. | Тур. | Max.                | Unit | Test conditions  |
|---------------------------------------|------|----------------------|------|------|---------------------|------|--|
| Repetitive peak off-state cur         | rent | I <sub>DRM</sub>     |      | —    | 2.0                 | mA   | Tj = 125°C, V <sub>DRM</sub> applied                                 |
| On-state voltage                      |      | V <sub>TM</sub>      | _    | —    | 1.5                 | V    | $Tc = 25^{\circ}C$ , $I_{TM} = 4.5 A$ ,<br>Instantaneous measurement |
| Gate trigger voltage <sup>Note2</sup> | Ι    | $V_{FGT I}$          |      | —    | 1.5                 | V    | $Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$                       |
|                                       | II   | V <sub>rgt i</sub>   |      | —    | 1.5                 | V    | $R_G = 330 \ \Omega$   |
|                                       | III  | V <sub>RGT III</sub> |      | —    | 1.5                 | V    |  |
| Gate trigger current <sup>Note2</sup> | Ι    | I <sub>FGT I</sub>   |      | —    | 15 <sup>Note3</sup> | mA   | $Tj = 25^{\circ}C, V_D = 6 V, R_L = 6 \Omega,$                       |
|                                       | II   | I <sub>RGT I</sub>   |      | —    | 15 <sup>Note3</sup> | mA   | $R_G = 330 \Omega$   |
|                                       | III  | I <sub>RGT III</sub> |      | —    | 15 <sup>Note3</sup> | mA   |  |
| Gate non-trigger voltage              |      | $V_{GD}$             | 0.2  | —    | —                   | V    | $Tj = 125^{\circ}C, V_D = 1/2V_{DRM}$                                |
| Thermal resistance                    |      | R <sub>th(j-c)</sub> | —    | —    | 4.0                 | °C/W | Junction to case <sup>Note4</sup>                                    |
| Thermal resistance                    |      | R <sub>th(j-a)</sub> | _    | _    | 50                  | °C/W | Junction to ambient  |

Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

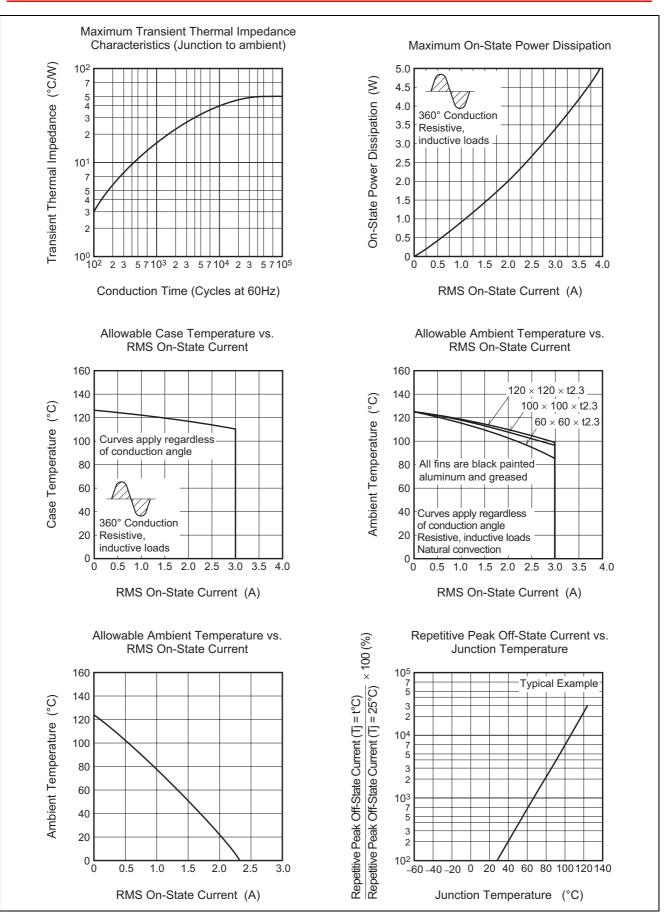
3. High sensitivity (I\_{GT} \leq 10 \text{ mA}) is also available. (I\_{GT} item: 1)

4. The contact thermal resistance  $R_{th\,(c\text{-}f)}$  in case of greasing is 0.5°C/W.

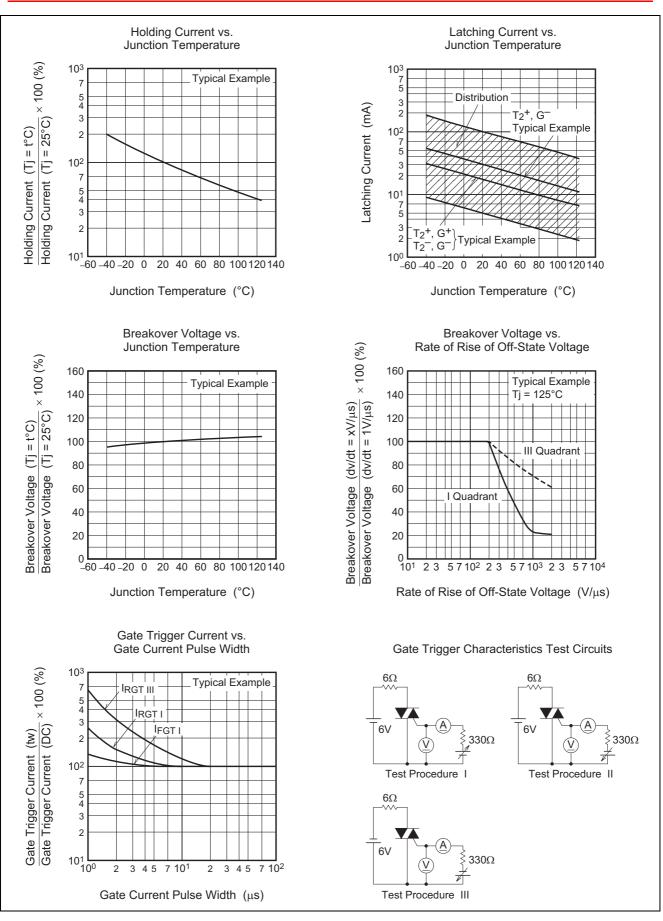
### **Performance Curves**



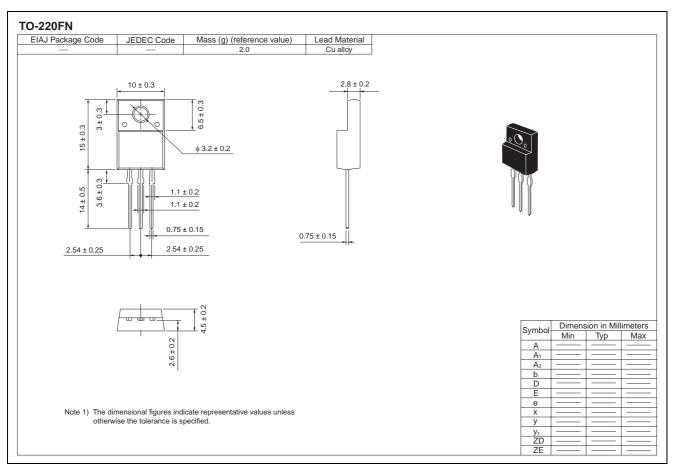








# **Package Dimensions**



## **Order Code**

| Standard packing        | Quantity                | Standard order code               | Standard order<br>code example                   |
|-------------------------|-------------------------|-----------------------------------|--|
| Plastic Magazine (Tube) | 50                      | Type name +RA                     | BCR3KM-12RA                                      |
| Plastic Magazine (Tube) | 50                      | Type name +RA – Lead forming code | BCR3KM-12RA-A8                                   |
|                         | Plastic Magazine (Tube) | Plastic Magazine (Tube)50         | Plastic Magazine (Tube)     50     Type name +RA |

Note : Please confirm the specification about the shipping in detail.

# Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

Keep safety first in your circuit designs! 1. Renesas Technology Corp. puts the maximum effort into making semiconductor products better and more reliable, but there is always the possibility that trouble may occur with them. Trouble with semiconductors may lead to personal injury, fire or property damage. Remember to give due consideration to safety when making your circuit designs, with appropriate measures such as (i) placement of substitutive, auxiliary circuits, (ii) use of nonflammable material or (iii) prevention against any malfunction or mishap.

### Notes regarding these materials

- Notes regarding these materials
  These materials are intended as a reference to assist our customers in the selection of the Renesas Technology Corp. product best suited to the customer's application; they do not convey any license under any intellectual property rights, or any other rights, belonging to Renesas Technology Corp. or a third party.
  Renesas Technology Corp. assumes no responsibility for any damage, or infringement of any third-party's rights, originating in the use of any product data, diagrams, charts, programs, algorithms, or circuit application examples contained in these materials.
  All information contained in these materials, including product data, diagrams, charts, programs and algorithms represents information on products at the time of publication of these materials, and are subject to change by Renesas Technology Corp. without notice due to product improvements or other reasons. It is therefore recommended that customers contact Renesas Technology Corp. van a authorized Renesas Technology Corp. product distributor for the latest product information before purchasing a product listed herein.
  The information before purchasing a product listed herein.
  The information described here may contain technical inaccuracies or typographical errors.
  Renesas Technology Corp. assumes no responsibility for any damage, ilability, or other loss rising from these inaccuracies or errors.
  Please also pay attention to information published by Renesas Technology Corp. by various means, including the Renesas Technology Corp. Semiconductor home page (http://www.renesas.com).
  When using any or all of the information contained in these materials, including product data, diagrams, charts, programs, and algorithms, please be sure to evaluate all information actual system before making a final decision on the applicability of the information and products. Renesas Technology Corp. assumes no responsibility for any damage or manufactured f

- use. 6. The prior written approval of Renesas Technology Corp. is necessary to reprint or reproduce in whole or in part these materials. 7. If these products or technologies are subject to the Japanese export control restrictions, they must be exported under a license from the Japanese government and cannot be imported into a country other than the approved destination. Any diversion or reexport contrary to the export control laws and regulations of Japan and/or the country of destination is prohibited. 8. Please contact Renesas Technology Corp. for further details on these materials or the products contained therein.



### **RENESAS SALES OFFICES**

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc. 450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology (Shanghai) Co., Ltd. Unit2607 Ruijing Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd. 1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

http://www.renesas.com