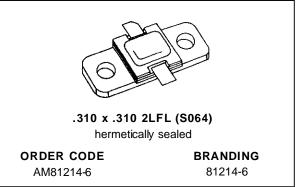


AM81214-006

RF & MICROWAVE TRANSISTORS L-BAND RADAR APPLICATIONS

- REFRACTORY/GOLD METALLIZATION
- EMITTER SITE BALLASTED
- 5:1 VSWR CAPABILITY
- LOW THERMAL RESISTANCE
- INPUT/OUTPUT MATCHING
- OVERLAY GEOMETRY
- METAL/CERAMIC HERMETIC PACKAGE
- P_{OUT} = 5.5 W MIN. WITH 10 dB GAIN

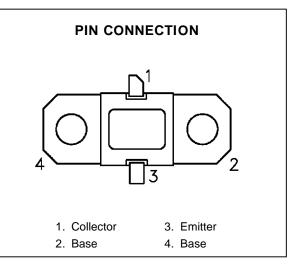


DESCRIPTION

The AM81214-006 device is a high power Class C transistor specifically designed for L-Band Radar pulsed driver applications.

This device is capable of operation over a wide range of pulse widths, duty cycles, and temperatures and is capable of withstanding 5:1 output VSWR at rated RF conditions. Low RF thermal resistance and computerized automatic wire bonding techniques ensure high reliability and product consistency.

AM81214-006 is supplied in the grounded IM-PAC[™] Hermetic Metal/Ceramic package with internal input/output matching structures.



Symbol	Parameter	Value	Unit	
Pdiss	Power Dissipation* $(T_C \le 100^{\circ}C)$	16.7	W	
lc	Device Current*	0.82	A	
Vcc	Collector-Supply Voltage*	32	V	
TJ	Junction Temperature (Pulsed RF Operation)	250	°C	
T _{STG}	Storage Temperature	- 65 to +200	°C	

ABSOLUTE MAXIMUM RATINGS ($T_{case} = 25^{\circ}C$)

THERMAL DATA

RTH(j-c)	Junction-Case Thermal Resistance*	9.0	°C/W		

*Applies only to rated RF amplifier operation

ELECTRICAL SPECIFICATIONS ($T_{case} = 25^{\circ}C$)

STATIC

Symbol	Test Conditions	Value			Unit		
			Min.	Тур.	Max.	Unit	
BV _{CBO}	$I_C = 1 \text{ mA}$	$I_E = 0 \text{ mA}$		48	_		V
BV _{CER}	$I_{C} = 5 \text{ mA}$	$R_{BE} = 10\Omega$		48	_		V
BV _{EBO}	$I_E = 1 \text{ mA}$	$I_C = 0 \text{ mA}$		3.5	_		V
I _{CES}	$V_{BE} = 0 V$	$V_{CE} = 28 V$			_	500	μA
h _{FE}	$V_{CE} = 5 V$	$I_C = 500 \text{ mA}$		15	_	300	_

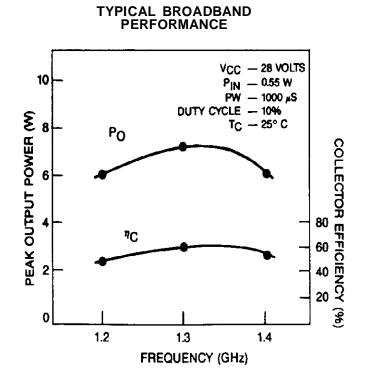
DYNAMIC

Symbol	Test Conditions			Value			
	Test Conditions			Min.	Тур.	Max.	Unit
Роит	f = 1.2 — 1.4 GHz	$P_{IN} = 0.5 \text{ W}$	$V_{CC} = 28 V$		5.5	6.2	W
ηc	f = 1.2 — 1.4 GHz	$P_{IN} = 0.5 \text{ W}$	$V_{CC} = 28 V$	47	52	—	%
GP	f = 1.2 — 1.4 GHz	$P_{IN} = 0.5 W$	$V_{CC} = 28 V$	10	10.5	_	dB

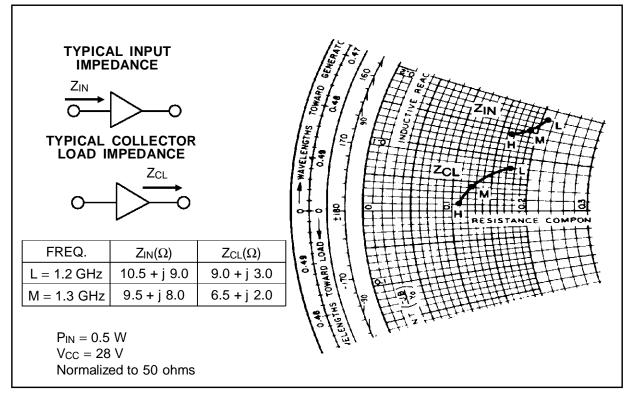
Note: Pulse Width = $1000 \mu S$

Duty Cycle = 10%

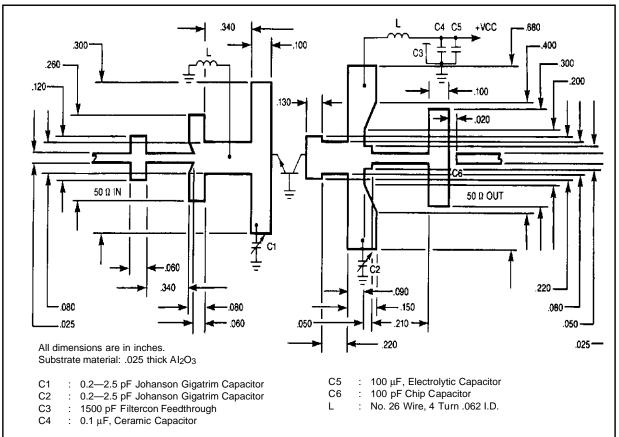
TYPICAL PERFORMANCE



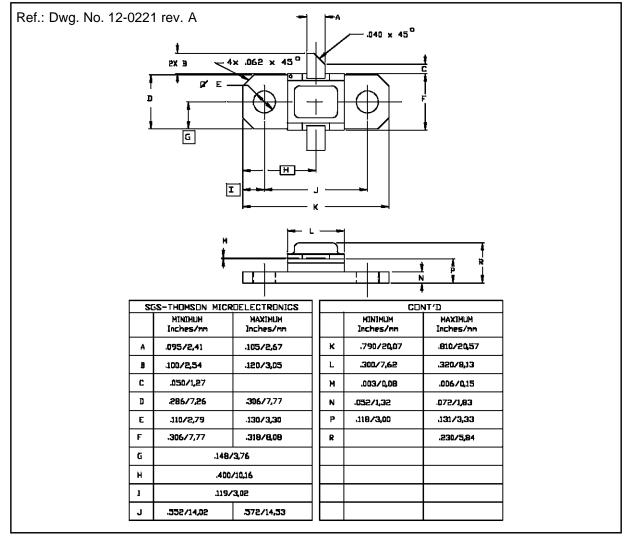
IMPEDANCE DATA



TEST CIRCUIT



PACKAGE MECHANICAL DATA



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