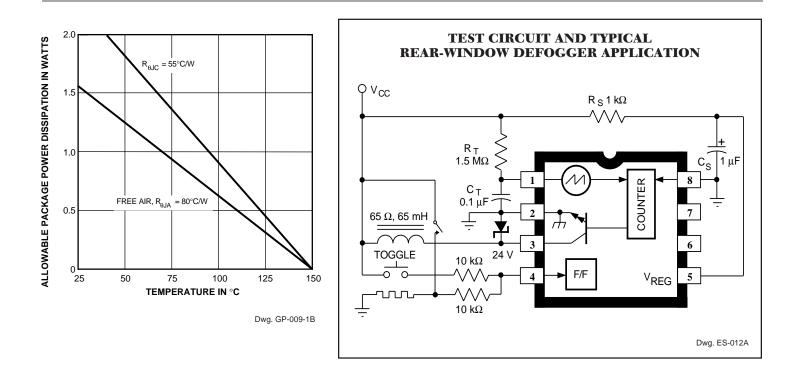




## 2436 COUNTDOWN POWER TIMER



## **ELECTRICAL SPECIFICATIONS** at $T_A = -40^{\circ}C$ to $+85^{\circ}C$ , $V_{CC} = 12$ V (unless otherwise specified).

			Limits		
Characteristic	Test Conditions	Min.	Max.	Units	
Regulator Voltage	I <sub>REG</sub> = 12 mA, Output Off	7.0	9.0	V	
Output Saturation Voltage	$I_{OUT} = 400 \text{ mA}, T_A = +25^{\circ}\text{C}$	—	2.5	V	
	$I_{OUT} = 250 \text{ mA}, T_A = +25^{\circ}\text{C}$	—	1.35	V	
Output Leakage Current	$V_{OUT} = 28 \text{ V}, V_{CC} = 12 \text{ V}$	—	100	μΑ	
	$V_{OUT}$ = 22 V, $V_{CC}$ = Open Circuit	—	100	μΑ	
Input Threshold Voltage	10 k $\Omega$ Series Resistor	1.0	5.0	V	
Oscillator Tolerance	$T_A = +25^{\circ}C$	—	±3.0	%	
	$T_A = -40^{\circ}C$ to $+85^{\circ}C$	_	±6.0	%	
Divider Count (V <sub>CC</sub> = 10 V to 16 V)	Initial Timeout	4064	4064	_	
	Subsequent Timeouts	2032	2032	—	



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## **CIRCUIT DESCRIPTION**

**OSC.** An external resistor in the range of 200 k $\Omega$  to 2 M $\Omega$  and an external capacitor in the range of 0.001  $\mu$ F to 1  $\mu$ F determine the frequency of the internal oscillator. The period of oscillation is nominally R<sub>T</sub>C<sub>T</sub> with the overall output time period (after the digital countdown) of

 $t = 4064 R_T C_T$ 

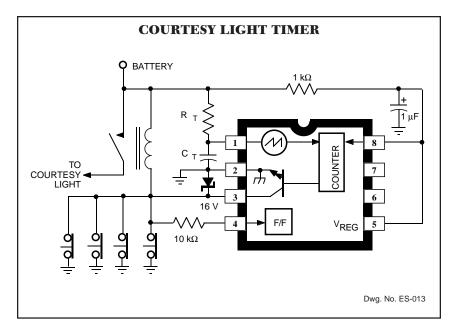
where t is in seconds. See also MODE SELECT.

**OUTPUT.** The output is an open-collector of a Darlington-connected transistor. The output is ON (low) during the timing period. An external Zener diode is used to protect the output against inductive-load switching transients and automotive "load dump".

**TOGGLE.** A push-button, momentary-action switch at this input toggles the timer from the OFF to the ON state. The oscillator and countdown circuitry are started on the rising edge of the input pulse. Internal de-bounce circuitry is included.

**SUPPLY.** The timer requires a supply current applied to this pin through a current-limiting resistor ( $R_S$ ). An internal 8 volt Zener diode shunt regulator provides a stable supply to the device over wide supply voltage variations. Capacitor  $C_S$  is used to provide decoupling.

**MODE SELECT.** With MODE SELECT connected to GROUND, the first activation will run for the preset time delay. All activations after the first will time out at half of the initial preset time. This sequence is reset each time the supply is interrupted. With MODE SELECT connected to SUPPLY ( $V_{REG}$ ), the timer will repeat the preset time delay each time it is activated.



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