



Maximun Ratings and Electrical Characteristics at 25 °C

SYMBOL	PARAMETER	CONDITIONS	Value	Unit
I _{T(RMS)}	On-State Current	180 ° Conduction Angel, T _c = 115 °C (H) T _c = 90 °C (W)	25	А
I _{T(AV)}	Average On-State Current	Half Cycle, $\Theta = 180^{\circ}$, $T_c = 115 \circ C$ (H) $T_c = 90 \circ C$ (W)	16	А
I _{TSM}	Non-repetitive On-State Current	Half Cycle, 60 Hz	330	А
I _{TSM}	Non-repetitive On-State Current	Half Cycle, 50 Hz	300	А
l²t	Fusing Current	tp = 10 ms, Half Cycle	450	A ² s
I _{GM}	Peak Gate Current	20 μs max.	4	А
Р _{бм}	Peak Gate Dissipation	20 μs max.	5	W
P _{G(AV)}	Gate Dissipation	20 ms max.	1	W
T	Operating Temperature		(-40 to + 150)	°⊂C
T _{stg}	Storage Temperature		(-40 to + 150)	°⊂C
T _{sld}	Soldering Temperature	10 ms max.	260	°⊂C
V _{RGM}	Reverse Gate Voltage		5	V
V _{iso}	R.M.S. isolation voltage 50/60 Hz sinusoidal waveform	(W)	2.500	Vac



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SYMBOL			Voltage	
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V _{drm} / V _{rrm}	Repetitive Peak Off State Voltage	600	800	V

Electrical Characteristics at Tamb = 25 °C

SVMBOI		CONDITIONS		SENSITIVITY	Lloit
	FANAIVIETEN			14	Unit
1	Cata Trigger Current	V = 12 V = R = 220	MIN	4	m۸
GT	Gale mgger Current	$v_{\rm D} = 12 v_{\rm DC}, n_{\rm L} = 3322$	MAX	40	ШA
V _{GT}	Gate Trigger Voltage	$V_{_{D}}$ = 12 $V_{_{DC}}$, $R_{_{L}}$ = 33 Ω	MAX	1.3	V
V_{gd}	Gate Non Trigger Voltage	$V_{_{D}} = V_{_{DRM}}, R_{_{L}} = 3.3 k\Omega, Tj = 125 \ ^{o}C$	MIN	0.5	V
I _H	Holding Current	I _τ = 500 mA	MAX	80	mA
I _L	Latching Current	$I_{g} = 1.2 I_{gT}$	MAX	90	mA
dV / dt	Critical Rate of Voltage Rise	$V_{D} = 0.67 \times V_{DRM}$, Gate open, Tj= 150 °C	MIN	200	V/µs
dl / dt	Critical Rate of Current Rise	$I_{G} = 2 \times I_{GT}$, tr ≤ 100ns, f = 60Hz, Tj= 125 ${}^{Q}C$	MIN	50	A/μs
V _{TM}	On-State Voltage	at I _T = 50 Amp, tp = 380 μ s, Tj= 25 $^{\circ}$ C	MAX	1.55	V
V _{t0}	Threshold Voltage	Tj= 125 ^⁰ C	MAX	0.77	V
r _d	Dynamic resistance	Tj= 125 ⁰C	MAX	14	mΩ
I _{drm /} I _{rrm}	Off-State Leakage Current	$ \begin{array}{ll} V_{_{D}} = V_{_{DRM}}, & Tj = 150 \ ^{\circ}C \\ V_{_{R}} = V_{_{RRM}}, & Tj = 25 \ ^{\circ}C \end{array} $	MAX MAX	4 10	mΑ μΑ
R _{th(j-c)}	Thermal Resistance Junc- tion-Case for DC	for AC 360 ^o conduction angle (H) (W)		1 1.7	ºC/W

Part Number Information





Ordering information

PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (g)
HS2514MH 00TUC	TU	TUBE	1000	2.30
PREFERRED P/N	PACKAGE CODE	DELIVERY MODE	BASE QUANTITY	UNIT WEIGHT (a)
1				- (3)

Package Outline Dimensions: (mm) TO-220AB





Package Outline Dimensions: (mm) TO-220F





Rating and Characteristics (Ta 25 °C unless otherwise noted)



Fig. 3: Relative variation of thermal impedance junction to case versus pulse duration.



Fig. 5: Non repetitive surge peak on-state current versus number of cycles.



Fig. 2: Average and D.C. on-state current versus case temperature.



Fig. 4: Relative variation of gate trigger current, holding and latching current versus junction temperature.



Fig. 6: Non repetitive surge peak on-state current for a sinusoidal pulse with width: tp < 10 ms, and corresponding value of $l^{2}t$.





Fig. 7: On-state characteristics (maximum values).





Revision History

DATE	REVISION	DESCRIPTION OF CHANGES
10-Dec-2019	0	Original Data Sheet

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