MOSFETs Silicon N-Channel MOS (U-MOSⅢ-H)

# TK50F15J1

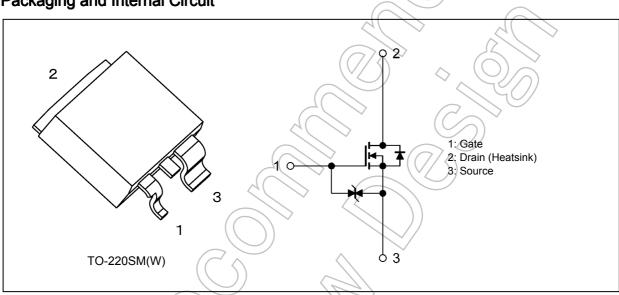
### 1. Applications

- Switching Voltage Regulators
- DC-DC Converters

### 2. Features

- (1) Low drain-source on-resistance:  $R_{DS(ON)} = 22 \text{ m}\Omega \text{ (typ.)} (V_{GS} = 10 \text{ V})$
- (2) Low leakage current:  $I_{\rm DSS}$  = 10  $\mu A$  (max) (V\_{\rm DS} = 150 V)
- (3) Enhancement mode:  $V_{th}$  = 2.0 to 4.0 V ( $V_{DS}$  = 10 V,  $I_D$  = 1 mA)

## 3. Packaging and Internal Circuit



## 4. Absolute Maximum Ratings (Note) ( $T_a = 25$ °C unless otherwise specified)

Characteristics	$(\mathcal{S})$	Symbol	Rating	Unit
Drain-source voltage	V <sub>DSS</sub>	150	V	
Gate-source voltage	$\rangle$	V <sub>GSS</sub>	±20	
Drain current (DC)	(Note 1)	Ι <sub>D</sub>	50	Α
Drain current (pulsed)	(Note 1)	I <sub>DP</sub>	150	
Power dissipation $(T_c = 25 \text{ °C})$		PD	300	W
Single-pulse avalanche energy	(Note 2)	E <sub>AS</sub>	273	mJ
Avalanche current		I <sub>AR</sub>	35	Α
Channel temperature	(Note 3)	T <sub>ch</sub>	175	°C
Storage temperature	(Note 3)	T <sub>stg</sub>	-55 to 175	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### 5. Thermal Characteristics

Characteristics		Max	Unit
Channel-to-case thermal resistance	R <sub>th(ch-c)</sub>	0.5	°C/W

Note 1: Ensure that the channel temperature does not exceed 175 °C.

Note 2: V\_DD = 50 V, T\_ch = 25 °C (initial), L = 33  $\mu$ H, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 35 A

Note 3: The definitions of the absolute maximum channel and storage temperatures are qualified per AEC-Q101.

Note: This transistor is sensitive to electrostatic discharge and should be handled with care.

#### 6. Electrical Characteristics

### 6.1. Static Characteristics (T<sub>a</sub> = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current	I <sub>GSS</sub>	$V_{GS}$ = ±16 V, $V_{DS}$ = 0 V	_	_	±10	μA
Drain cut-off current	I <sub>DSS</sub>	V <sub>DS</sub> = 150 V, V <sub>GS</sub> = 0 V	$\langle \rangle$		10	
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	150		_	V
Drain-source breakdown voltage (Note 4)	V <sub>(BR)DSX</sub>	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = -20 V	95	$\langle \gamma \rangle$	_	
Gate threshold voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	2.0	2_	4.0	
Drain-source on-resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A	$\gamma$	22	30	mΩ

Note 4: If a reverse bias is applied between gate and source, this device enters V<sub>(BR)DSX</sub> mode. Note that the drainsource breakdown voltage is lowered in this mode.

### 6.2. Dynamic Characteristics (Ta = 25 °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Input capacitance	C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-((	4300	$\geq -$	pF
Reverse transfer capacitance	C <sub>rss</sub>			210	) —	
Output capacitance	C <sub>oss</sub>		$\mathcal{A}$	640	_	
Switching time (rise time)	t <sub>r</sub>	See Fig. 6.2.1	$\langle \langle \rangle$	7	—	ns
Switching time (turn-on time)	t <sub>on</sub>		>)	30	—	
Switching time (fall time)	t <sub>f</sub>		$\sim$	15	—	
Switching time (turn-off time)	t <sub>off</sub>		))_	85	_	
	4					

VGS 
$$V_{DD} \approx 75 V$$
  
V<sub>DD</sub>  $\approx 0 V_{OUT}$   
RG  $R_{C}$   
V<sub>DD</sub>  $R_{C} \approx 0 V_{OUT}$   
V<sub>DD</sub>  $V_{DD} \approx 75 V$   
V<sub>GS</sub>  $= 0 V/10 V$   
I<sub>D</sub>  $= 25 A$   
R<sub>L</sub>  $\approx 3 \Omega$   
R<sub>G</sub>  $= 4.7 \Omega$   
Duty  $\leq 1 \%$ , t<sub>w</sub>  $= 10 \mu s$ 

Fig. 6.2.1 Switching Time Test Circuit

## 6.3. Gate Charge Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Total gate charge (gate-source plus gate-drain)	Qg	V <sub>DD</sub> ≈ 120 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 50 A	_	75	—	nC
Gate-source charge	Q <sub>gs</sub>		_	50	_	
Gate-drain charge	Q <sub>gd</sub>			25	_	

## 6.4. Source-Drain Characteristics ( $T_a = 25$ °C unless otherwise specified)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Reverse drain current (DC)	(Note 5)	I <sub>DR</sub>		_	_	50	Α
Reverse drain current (pulsed)	(Note 5)	I <sub>DRP</sub>		_	_	150	
Diode forward voltage		V <sub>DSF</sub>	I <sub>DR</sub> = 50 A, V <sub>GS</sub> = 0 V	_	_	-1.5	V
Reverse recovery time		t <sub>rr</sub>	I <sub>DR</sub> = 50 A, V <sub>GS</sub> = 0 V	_	95	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-dI <sub>DR</sub> /dt = 100 A/μs		450		nC

Note 5: Ensure that the channel temperature does not exceed 175 °C.

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## TOSHIBA 7. Marking (Note)

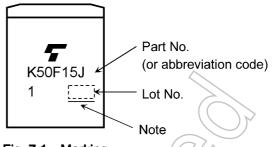


Fig. 7.1 Marking

Note: A line under a Lot No. identifies the indication of product Labels. Not underlined: [[Pb]]/INCLUDES > MCV

Underlined: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

Please contact your TOSHIBA sales representative for details as to environmental matters such as the RoHS compatibility of Product.

The RoHS is the Directive 2011/65/EU of the European Parliament and of the Council of 8 June 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.

### 8. Moisture-Proof Packing

This device is packed in a moisture-proof laminated aluminum bag.

#### 8.1. Precautions for Transportation and Storage (Note)

- (1) Avoid excessive vibration during transportation.
- (2) Do not toss or drop the packed devices to avoid ripping of the bag.
- (3) After opening the moisture-proof bag, the devices should be assembled within two weeks in an environment of 5°C to 30°C and RH70% or below. Perform reflow at most twice.
- (4) The moisture-proof bag may be stored unopened for up to 24 months at 5°C to 30°C and RH90% or below.
- (5) If, upon opening the bag, the moisture indicator card shows humidity of 30% or above (the color of the 30% dot has changed from blue to pink) or the expiration date has passed, the devices should be baked as follows:

Baking conditions: 125°C for 48 hours.

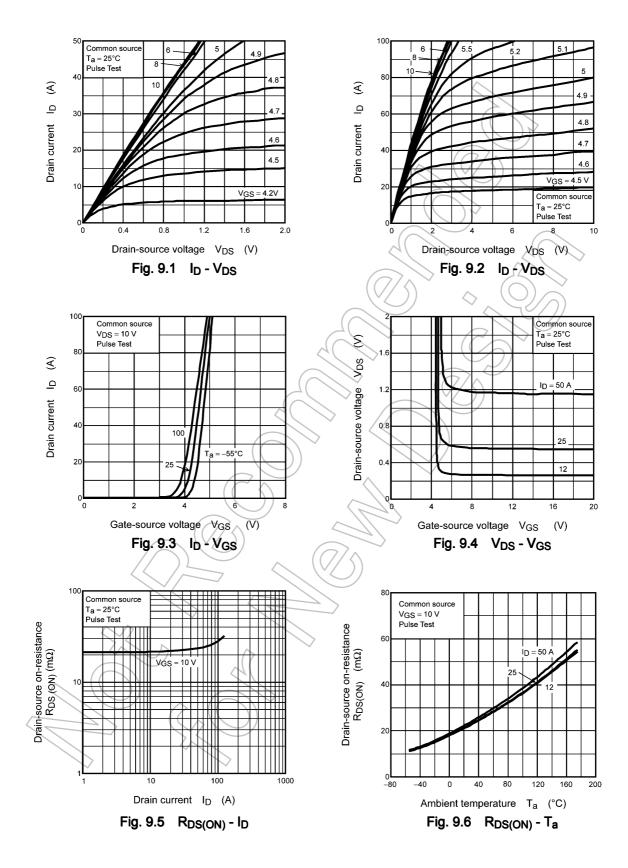
Note: Since the tape materials are not heat-proof, devices should be placed on either heat-proof trays or aluminum magazines when baking.

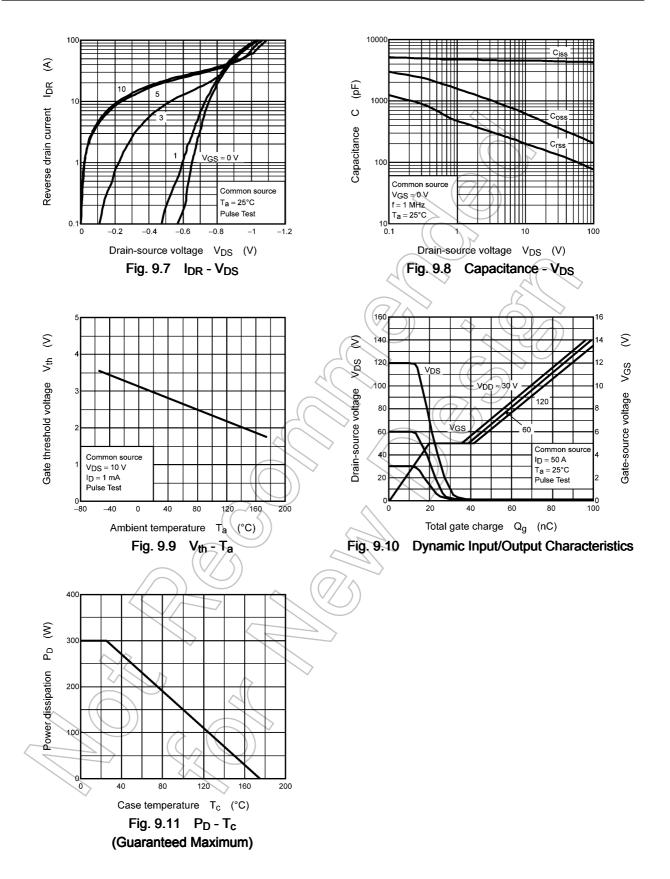


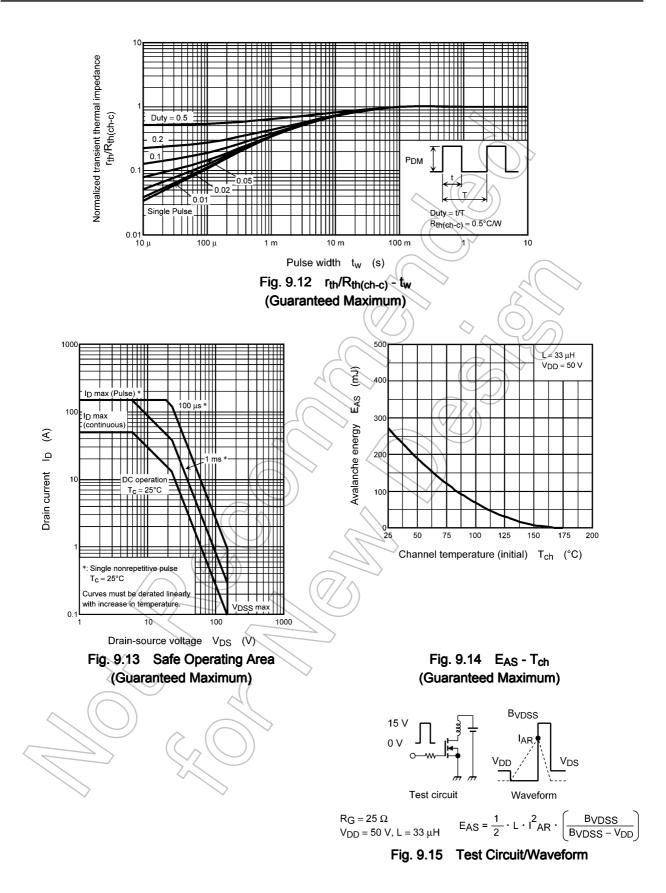
The humidity indicator shows an approximate ambient humidity at 25°C. If the ambient humidity is below 30%, the color of all the indicator dots is blue. If, upon opening the bag, the color of the 30% dot has changed from blue to pink, the devices should be baked before assembly.

Fig. 8.1.1 Humidity Indicator

### 9. Characteristics Curves (Note)



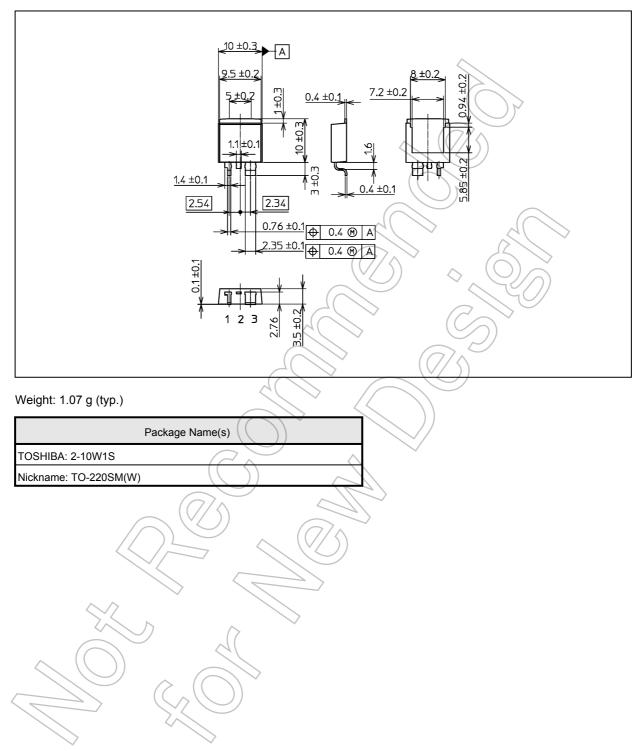




Note: The above characteristics curves are presented for reference only and not guaranteed by production test, unless otherwise noted.

### Package Dimensions

Unit: mm



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