AN7511

1-W BTL audio power amplifier

■ Overview

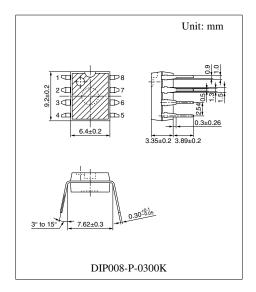
The AN7511 is an audio power amplifier IC with 1-ch output. The BTL (Balanced Transformer-Less) method can provide fewer external parts and more easy design for applications.

■ Features

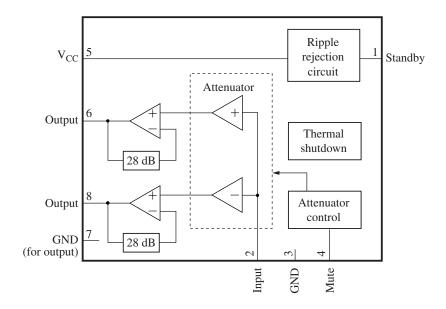
- ullet 1-W output (8 Ω) with supply voltage of 5 V
- On-chip standby function
- On-chip muting function

Applications

• Televisions, radios, and personal computers



■ Block Diagram



■ Pin Descriptions

Pin No.	Description		
1	Standby (standby state if this pin is open.)		
2	Input		
3	Ground (for input)		
4	Muting (muting on if this pin is open.)		
5	Supply voltage		
6	+ Output		
7	Ground (for output ch.1)		
8	– Output		

■ Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage *2	V _{CC}	14	V
Supply current	I_{CC}	1.0	A
Power dissipation *3	P_{D}	541	mW
Operating ambient temperature *1	T_{opr}	-25 to +70	°C
Storage temperature *1	T_{stg}	-55 to +150	°C

Note) *1: Except for the operating ambient temperature and storage temperature, all ratings are for $T_a = 25$ °C.

■ Recommended Operating Range

Parameter	Symbol	Range	Unit
Supply voltage	V _{CC}	3.5 to 13.5	V

\blacksquare Electrical Characteristics at V_{CC} = 5.0 V, R_L = 8 $\Omega,$ f = 1 kHz, T_a = 25°C \pm 2°C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Quiescent circuit current	I_{CQ}	$V_{IN} = 0 \text{ mV}$	_	30	60	mA
Standby current	I _{STB}	$V_{IN} = 0 \text{ mV}$	_	1	10	μΑ
Output noise voltage *	V _{NO}	$R_g = 10 \text{ k}\Omega$	_	0.14	0.4	mV[rms]
Voltage gain	G_{V}	$P_{O} = 0.25 \text{ W}$	32	34	36	dB
Total harmonic distortion	THD	$P_{O} = 0.25 \text{ W}$	_	0.05	0.5	%
Maximum output power	P _{O1}	THD = 10%	0.8	1.1	_	W
Ripple rejection ratio *	RR	$R_g = 10 \text{ k}\Omega, V_R = 0.5 \text{ V[rms]}, f_R = 120 \text{ Hz}$	30	50	_	dB
Output offset voltage	V _{OFF}	$R_g = 10 \text{ k}\Omega$	-300	0	300	mV
Muting effect *	MT	$P_{O} = 0.25 \text{ W}$	70	86	_	dB

Note) *: In measuring, the filter for the range of 15 Hz to 30 kHz (12 dB/OCT) is used.

^{*2:} At no signal

^{*3:} The power dissipation shown is the value for $T_a = 70^{\circ}C$.

■ Terminal Equivalent Circuits

Pin No.	Pin name	Equivalent circuit	Voltage
1	Standby pin	$V_{CC} \circ \overbrace{Q}_{Q} = \underbrace{Q}_{Q} = $	5 V
2	Input pin	V_{CC} \sim	
3	GND	3	0 V
4	Muting pin	V_{CC} Q_{C} $Q_{$	_

■ Terminal Equivalent Circuits (continued)

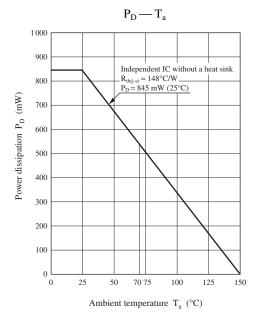
Pin No.	Pin name	Equivalent circuit	Voltage
5	V _{CC}	_	5.0 V
6	+ Output pin	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	2.15 V
		$\frac{1}{2}$	
7	GND	7	0 V
8	– Output pin	1/2 V _{CC} → W 20 kΩ 8 1/7 V _{CC} → W 20 kΩ	2.15 V

■ Usage Notes

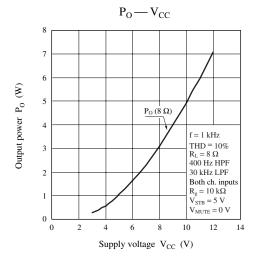
- \bullet Please avoid the short circuit to V_{CC} , ground, or load short circuit.
- Please connect the cooling fin with the GND potential.
- The thermal shutdown circuit operates at about $T_j = 150^{\circ}$ C. However, the thermal shutdown circuit is reset automatically if the temperature drops.
- \bullet Please carefully design the heat radiation especially when you take out high power at high V_{CC} .
- Please connect only the ground of signal with the signal GND of the amplifier in the previous stage.

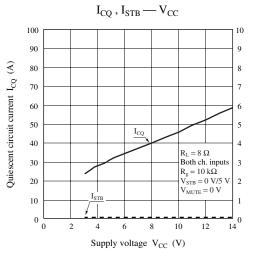
■ Technical Data

1. Package power dissipation



2. Main characteristics

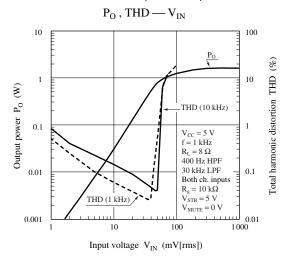


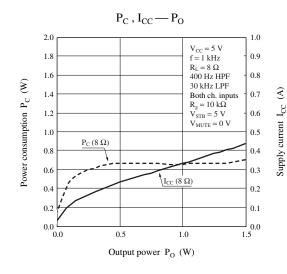


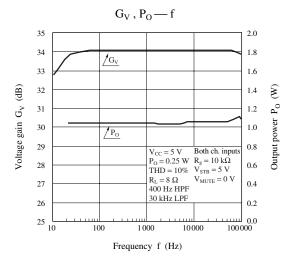
Standby current I_{STB} (µA)

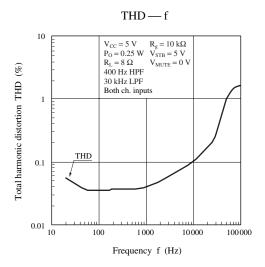
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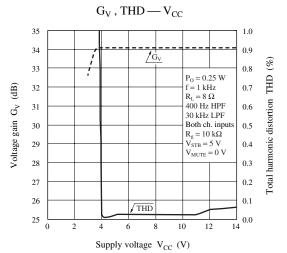
2. Main characteristics (continued)

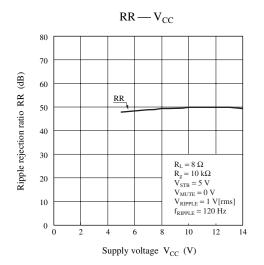






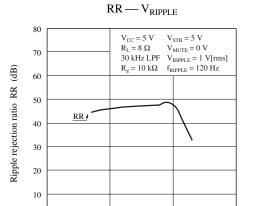


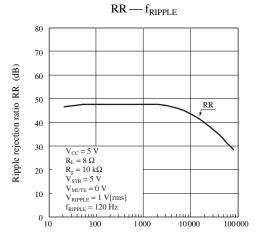




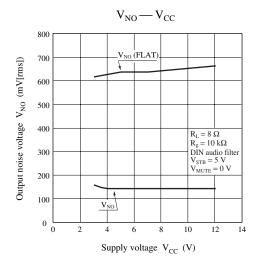
■ Technical Data (continued)

2. Main characteristics (continued)





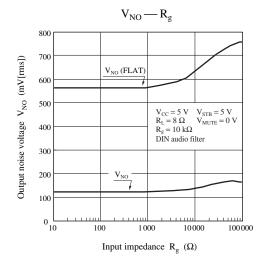
Power supply ripple frequency f_{RIPPLE} (Hz)

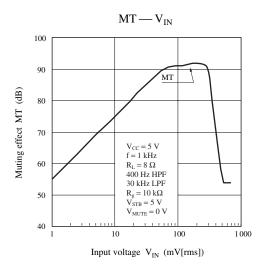


100 90 Muting effect MT (dB) MT \ 80 70 $P_{O} = 0.25 \text{ W}$ f = 1 kHz60 $R_L = 8 \Omega$ 400 Hz HPF $30~\mathrm{kHz}~\mathrm{LPF}$ $R_g=10\;\mathrm{k}\Omega$ 50 $V_{STB} = 5 \text{ V}$ $V_{MUTE} = 0 V$ 40 12

Supply voltage V_{CC} (V)

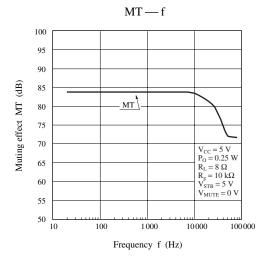
 $MT - V_{CC}$

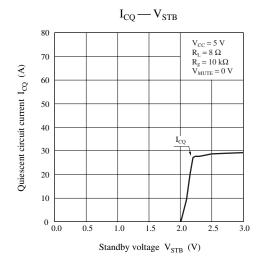




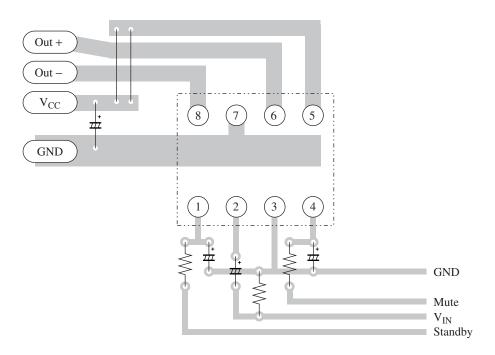
■ Technical Data (continued)

2. Main characteristics (continued)

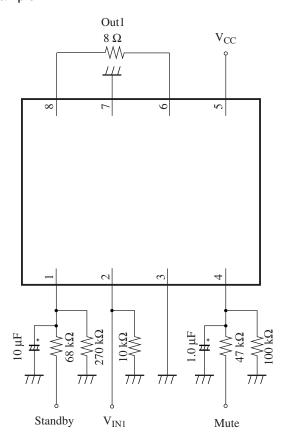




3. Example of PCB pattern



■ Application Circuit Example



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