## ADS25A60G/80G

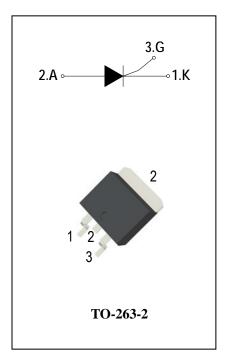
## ADV SCRs

### **General Description**

The 25A SCR series of silicon controlled rectifiers, with high ability to withstand the shock loading of large current, provide high dv/dt rate with strong resistance to electromagnetic interference. They are especially recommended for use on solid state relay, motorcycle, power charger, T-tools etc.

#### Features

- ◆ Repetitive Peak Off-State Voltage : 600V and 800V
- ◆ R.M.S On-State Current ( IT(RMS)= 25A )
- ♦ These are Pb-Free Devices



#### **Absolute Maximum Ratings**

| Symbol              | Items                                     | Conditions  |           | Ratings    | Unit             |
|---------------------|---|---|-----------|------------|------------------|
| $V_{\text{DRM}}$    | Repetitive Peak Off-State Voltage         | Ti-25°C   | ADS25A60G | 600        | V                |
| V <sub>RRM</sub>    | Repetitive peak reverse voltage           | Tj=25°C   | ADS25A80G | 800        | V                |
| I <sub>T(AV)</sub>  | Average On-State Current                  | Half Sine Wave , Tc = 100°C                                   |           | 16         | А                |
| I <sub>T(RMS)</sub> | R.M.S On-State Current                    | Half Sine Wave , Tc = 100°C                                   |           | 20         | А                |
| I <sub>TSM</sub>    | Surge On-State Current                    | 1/2 Cycle, Sine Wave Non-Repetitive,<br>tp=10ms(50Hz)Tj =25°C |           | 300        | А                |
| l <sup>2</sup> t    | I <sup>2</sup> t for Fusing               | Tj =25°C,tp =10ms   |           | 450        | A <sup>2</sup> S |
| $P_{GM}$            | Forward Peak Gate Power Dissipation       | Tj =125°C, Pulse Width $\leq 20\mu_S$                         |           | 5          | W                |
| P <sub>G(AV)</sub>  | Forward Average Gate Power<br>Dissipation | Tj =25°C, tp =10ms  |           | 1          | W                |
| I <sub>GM</sub>     | Peak Gate Current                         | Tj =125°C, Pulse Width $\leq 20\mu s$                         |           | 4          | А                |
| Tj                  | Operating Junction Temperature            |   |           | - 40 ~ 125 | °C               |
| T <sub>STG</sub>    | Storage Temperature                       |   |           | - 40 ~ 150 | °C               |



## ADS25A60G/80G

### Electrical Characteristics (Tj = 25°C unless otherwise specified)

| Symbol               | Items   | Conditions   |      | ADS25A60G/80G |       | Unit |
|----------------------|---|--|------|---------------|-------|------|
|                      |   |  |      |               | Blank |      |
| I <sub>DRM</sub>     | Peak Forward Reverse  | V <sub>DRM</sub> = V <sub>RRM</sub><br>Tj = 25°C             |      | 10            |       | uA   |
| I <sub>RRM</sub>     | Blocking Current  | V <sub>DRM</sub> = V <sub>RRM</sub><br>Tj = 125°C            | Max. |               |       | mA   |
| V <sub>TM</sub>      | Peak On-State Voltage   | I <sub>TM</sub> = 50A, t <sub>P</sub> = 380 μs               | Max. | 1.6           |       | V    |
| $V_{GD}$             | Non-Trigger Gate Voltage  | $V_D = V_{DRM}$ $R_L = 3.3 \text{ k}\Omega$<br>Tj = 125°C    | Min. | 0.2           |       | V    |
| $V_{\text{GT}}$      | Gate Trigger Voltage  |  | Max. | 1.3           |       | V    |
| I <sub>GT</sub>      | Gate Trigger Current  | $V_D = 12V$ , $R_L = 33\Omega$                               | Max. | 15            | 30    | mA   |
| Ι <sub>Η</sub>       | Holding Current   | I <sub>T</sub> = 0.5A  | Max. | 30            | 40    | mA   |
| ΙL                   | Latching Current  | I <sub>G</sub> = 1.2 I <sub>GT</sub>                         | Max. | 50            | 50    | mA   |
| dV/dt                | Critical Rate of Rise of<br>Off-State Voltage                     | V <sub>D</sub> = 2/3V <sub>DRM</sub> gate open<br>Tj = 125°C | Min. | 1000          | 1500  | V/µs |
| R <sub>th(j-c)</sub> | Junction to case (AC)   |  | Max. | 1.0           |       | °C/W |
| R <sub>th(j-a)</sub> | Junction to ambient(Copper surface under tab:S=1cm <sup>2</sup> ) |  | Max. | 45            |       | °C/W |

## <u>ADV</u>

ADS25A60G/80G

FIG.1: Maximum average power dissipation (Single phase half wave) 360° θ 16 Average On-State Current (A) 12 8 θ=180° 4 0 3 6 9 12 15 18 21 24 27 30 Power Dissipation(W)

FIG.3: Gate trigger current VS Junction temperature

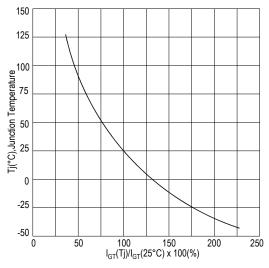
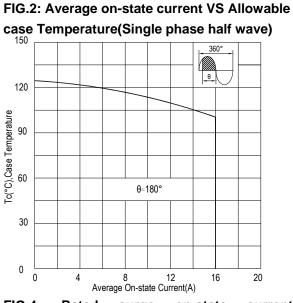
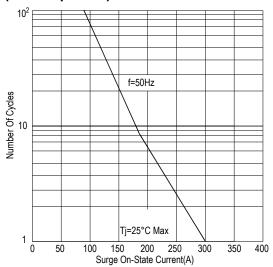
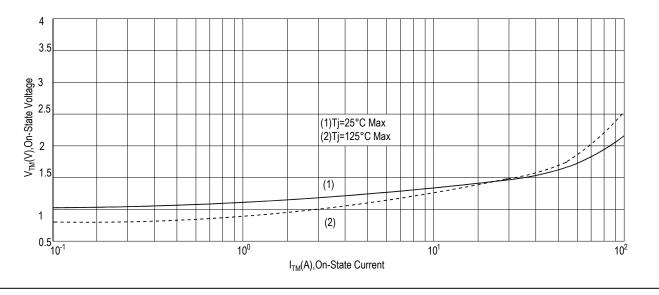


FIG.5: On-state characteristics(Max)



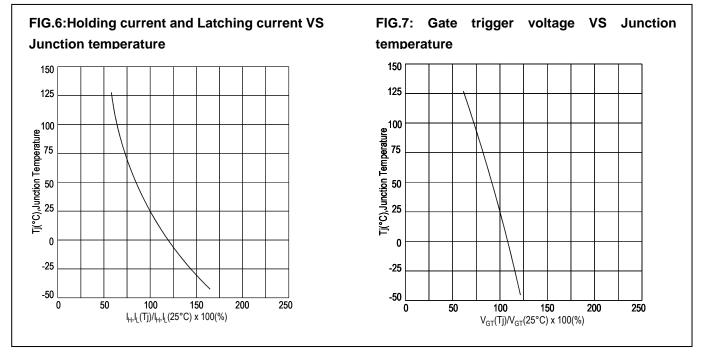






## <u>ADV</u>

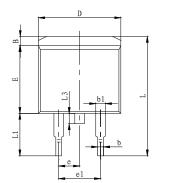
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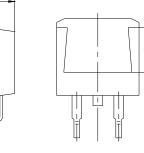
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### PACKAGE MECHANICAL DATA **TO-263-2 Package Dimension**



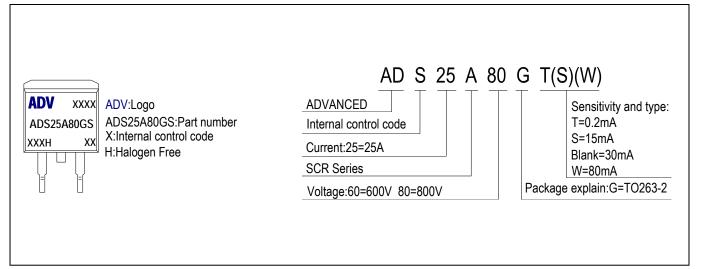
A1

2



| Symb       | Dimensions     |        | Dimensions |       |  |
|------------|----------------|--------|------------|-------|--|
| Symb<br>ol | In Millimeters |        | In Inches  |       |  |
| 01         | Min            | Max    | Min        | Max   |  |
| А          | 4.470          | 4.670  | 0.176      | 0.184 |  |
| A1         | 0.000          | 0.150  | 0.000      | 0.006 |  |
| В          | 1.170          | 1.370  | 0.046      | 0.054 |  |
| b          | 0.710          | 0.910  | 0.028      | 0.036 |  |
| b1         | 1.170          | 1.370  | 0.046      | 0.054 |  |
| с          | 0.310          | 0.530  | 0.012      | 0.021 |  |
| c1         | 1.170          | 1.370  | 0.046      | 0.054 |  |
| D          | 10.010         | 10.310 | 0.394      | 0.406 |  |
| E          | 8.500          | 8.900  | 0.335      | 0.350 |  |
| е          | 2.540 TYP      |        | 0.100 TYP  |       |  |
| e1         | 4.980          | 5.180  | 0.196      | 0.204 |  |
| L          | 15.050         | 15.450 | 0.593      | 0.608 |  |
| L1         | 5.080          | 5.480  | 0.200      | 0.216 |  |
| L2         | 2.340          | 2.740  | 0.092      | 0.108 |  |
| L3         | 1.300          | 1.700  | 0.051      | 0.067 |  |
| V          | 5.600 REF      |        | 0.220 REF  |       |  |

#### **Making Diagram**



#### **Ordering information**

| Part number  | Package  | Marking    | Packing       | Quantity |  |
|--|----------|------------|---------------|----------|--|
| ADS25A60G#   | TO-263-2 | ADS25A60G# | Tube          | 50pcs    |  |
| ADS25A60G#   |          |            | Embossed tape | 800pcs   |  |
| 40000  | TO-263-2 | ADS25A80G# | Tube          | 50pcs    |  |
| ADS25A80G#   |          |            | Embossed tape | 800pcs   |  |
| Note:# = Gate Trigger Current Sensitivity and type |          |            |               |          |  |

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## ADS25A60G/80G

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