

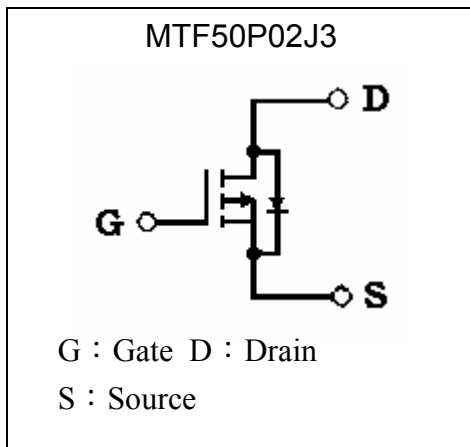
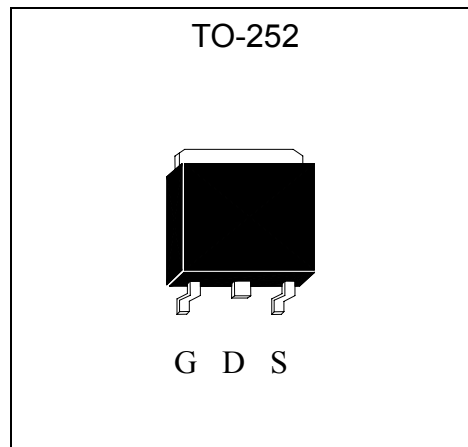
P-Channel Logic Level Enhancement Mode Power MOSFET

MTF50P02J3

BV_{DSS}	-20V
I_D	-10A
$R_{DSON(MAX)}$	58m Ω

Features

- Low Gate Charge
- Simple Drive Requirement
- RoHS compliant & Halogen-free package

Equivalent Circuit

Outline

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	-20	V
Gate-Source Voltage	V_{GS}	± 12	
Continuous Drain Current @ $T_C=25^\circ\text{C}$	I_D	-10	A
Continuous Drain Current @ $T_C=100^\circ\text{C}$		-6.5	
Pulsed Drain Current *1	I_{DM}	-40	
Total Power Dissipation @ $T_C=25^\circ\text{C}$	P_d	30	W
Total Power Dissipation @ $T_C=100^\circ\text{C}$		10	
Operating Junction and Storage Temperature Range	T_j, T_{stg}	-55~+175	$^\circ\text{C}$

Note : *1. Pulse width limited by maximum junction temperature



Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	5	°C/W
Thermal Resistance, Junction-to-ambient, max	$R_{th,j-a}$	110	

Characteristics (Tc=25°C, unless otherwise specified)

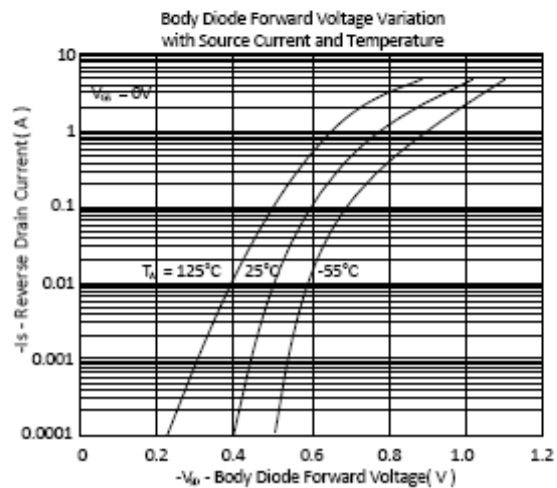
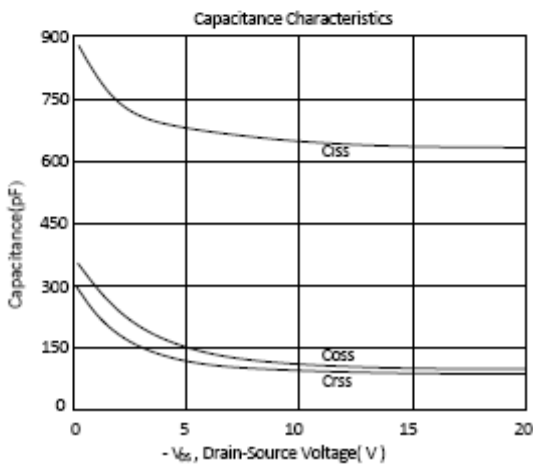
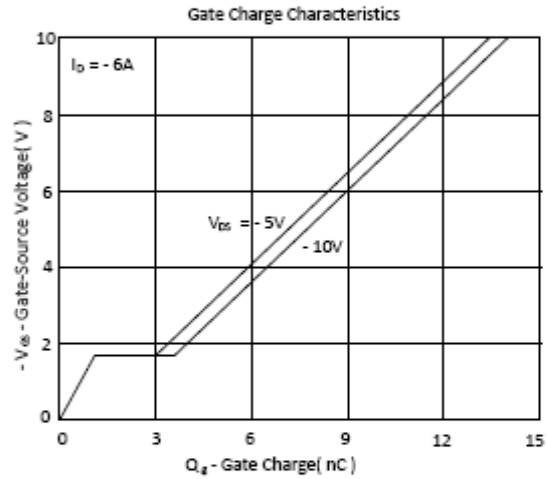
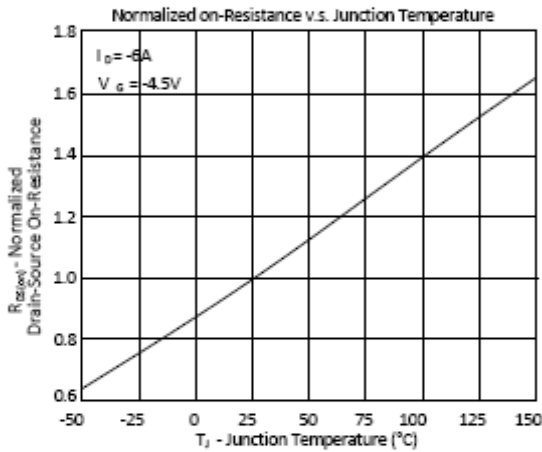
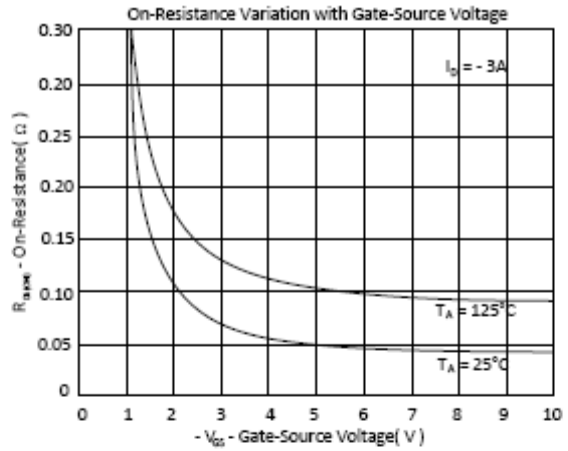
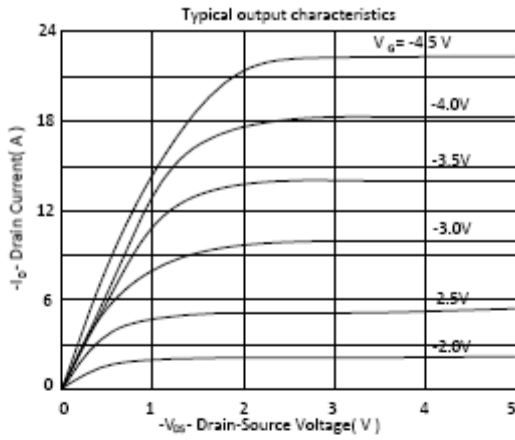
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	-20	-	-	V	$V_{GS}=0, I_D=-250\mu A$
$V_{GS(th)}$	-0.45	-0.8	-1.2	V	$V_{DS}=V_{GS}, I_D=-250\mu A$
$G_{FS} *1$	-	9	-	S	$V_{DS}=-5V, I_D=-5A$
I_{GSS}	-	-	±100	nA	$V_{GS}=\pm 12, V_{DS}=0$
I_{DSS}	-	-	-1	μA	$V_{DS}=-16V, V_{GS}=0$
	-	-	-25	μA	$V_{DS}=-16V, V_{GS}=0, T_j=125^\circ C$
$I_{D(ON)} *1$	-10	-	-	A	$V_{DS}=-5V, V_{GS}=-4.5V$
$R_{DS(ON)} *1$	-	51	58	mΩ	$V_{GS}=-4.5V, I_D=-6A$
	-	75	90	mΩ	$V_{GS}=-2.5V, I_D=-3A$
Dynamic					
$Q_g *1, 2$	-	7	-	nC	$I_D=-6A, V_{DS}=-10V, V_{GS}=-4.5V$
$Q_{gs} *1, 2$	-	1.3	-		
$Q_{gd} *1, 2$	-	2.5	-		
$t_{d(ON)} *1, 2$	-	18	-	ns	$V_{DS}=-10V, I_D=-1A, V_{GS}=-4.5V, R_G=6\Omega$
$t_r *1, 2$	-	35	-		
$t_{d(OFF)} *1, 2$	-	40	-		
$t_f *1, 2$	-	35	-		
C_{iss}	-	653	-	pF	$V_{GS}=0V, V_{DS}=-10V, f=1MHz$
C_{oss}	-	102	-		
C_{rss}	-	87	-		
R_g	-	7.5	-	Ω	$V_{GS}=15mV, V_{DS}=0, f=1MHz$
Source-Drain Diode					
$I_S *1$	-	-	-10	A	
$I_{SM} *3$	-	-	-40		
$V_{SD} *1$	-	-	-1.3	V	$I_F=I_S, V_{GS}=0V$

Note : *1.Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%
 *2.Independent of operating temperature
 *3.Pulse width limited by maximum junction temperature.

Ordering Information

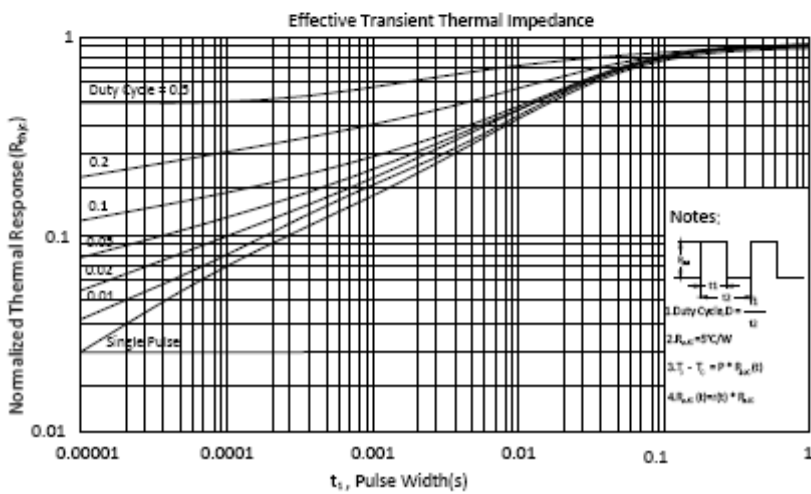
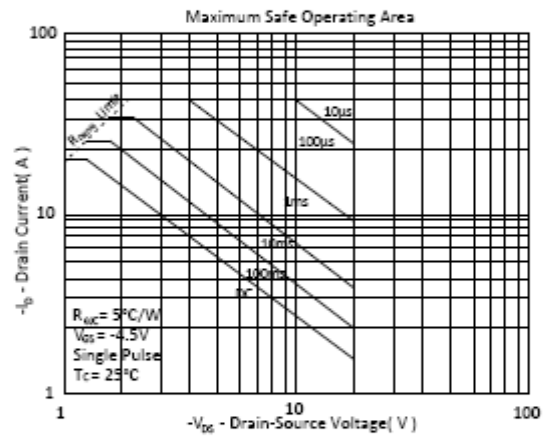
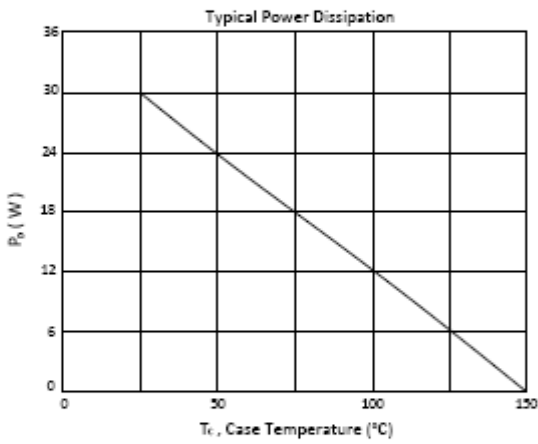
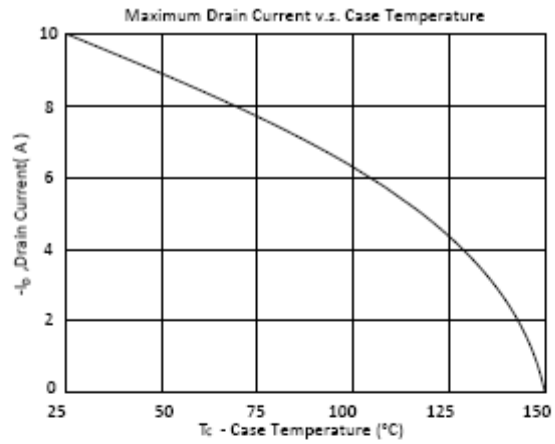
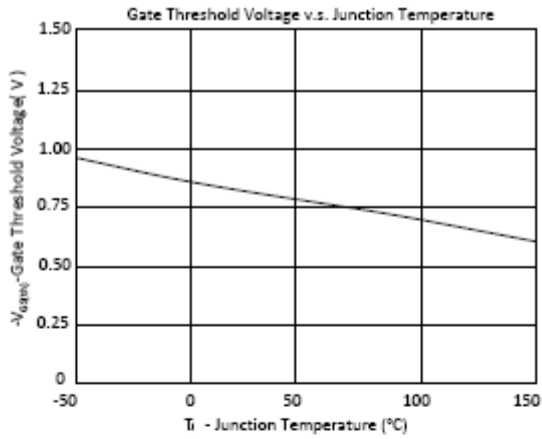
Device	Package	Shipping	Marking
MTF50P02J3	TO-252 (RoHS compliant and Halogen-freepackage)	2500 pcs / Tape & Reel	F50P02

Characteristic Curves

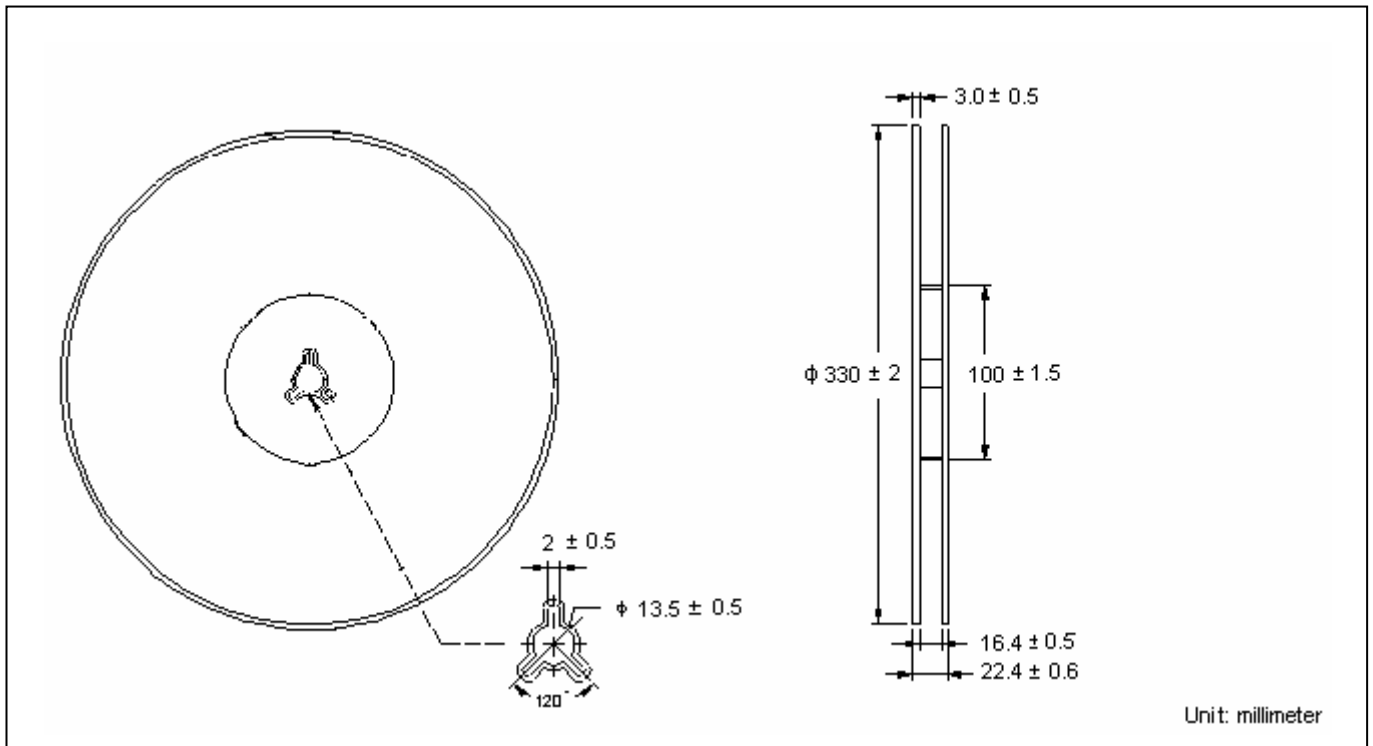




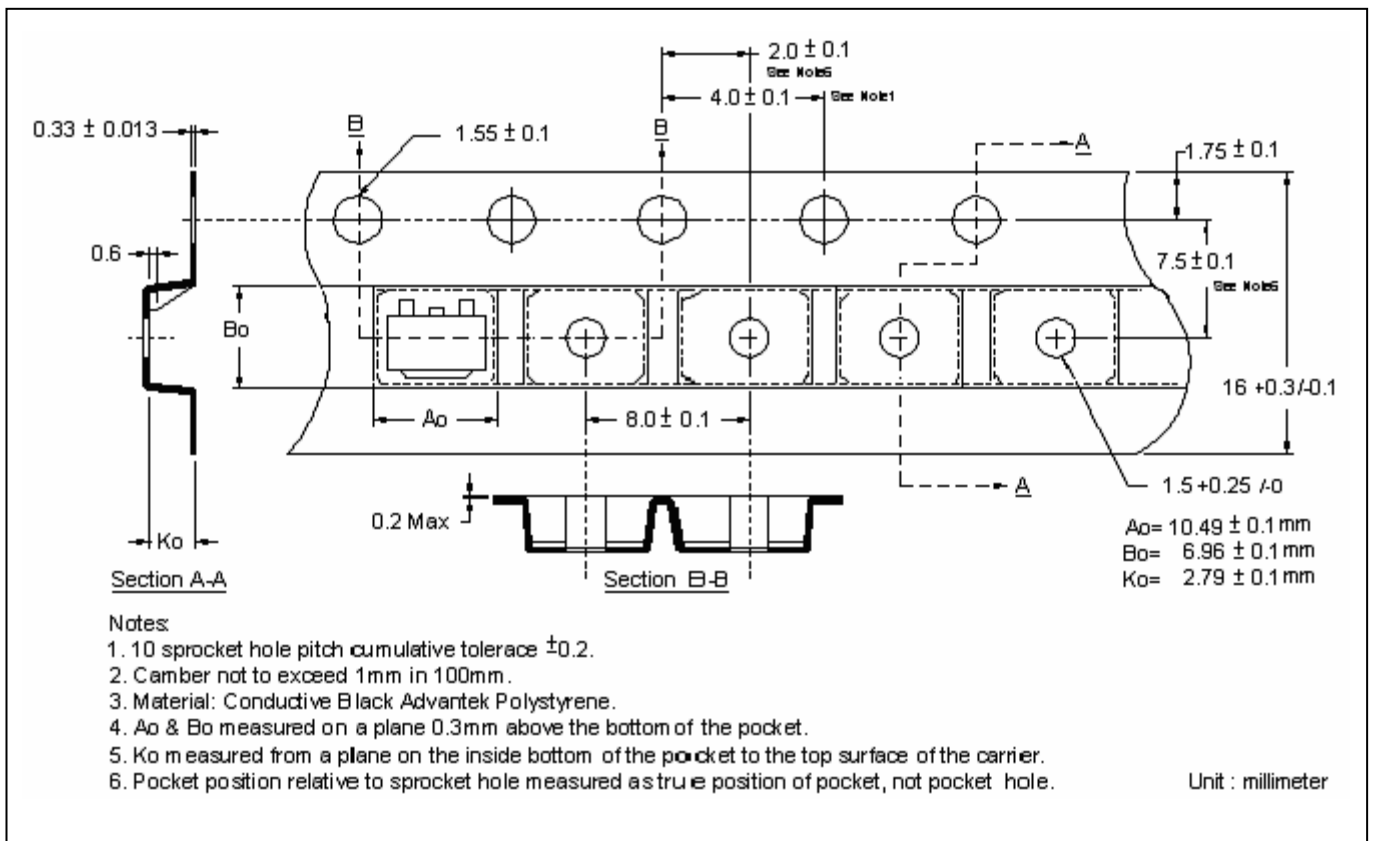
Characteristic Curves(Cont.)



Reel Dimension



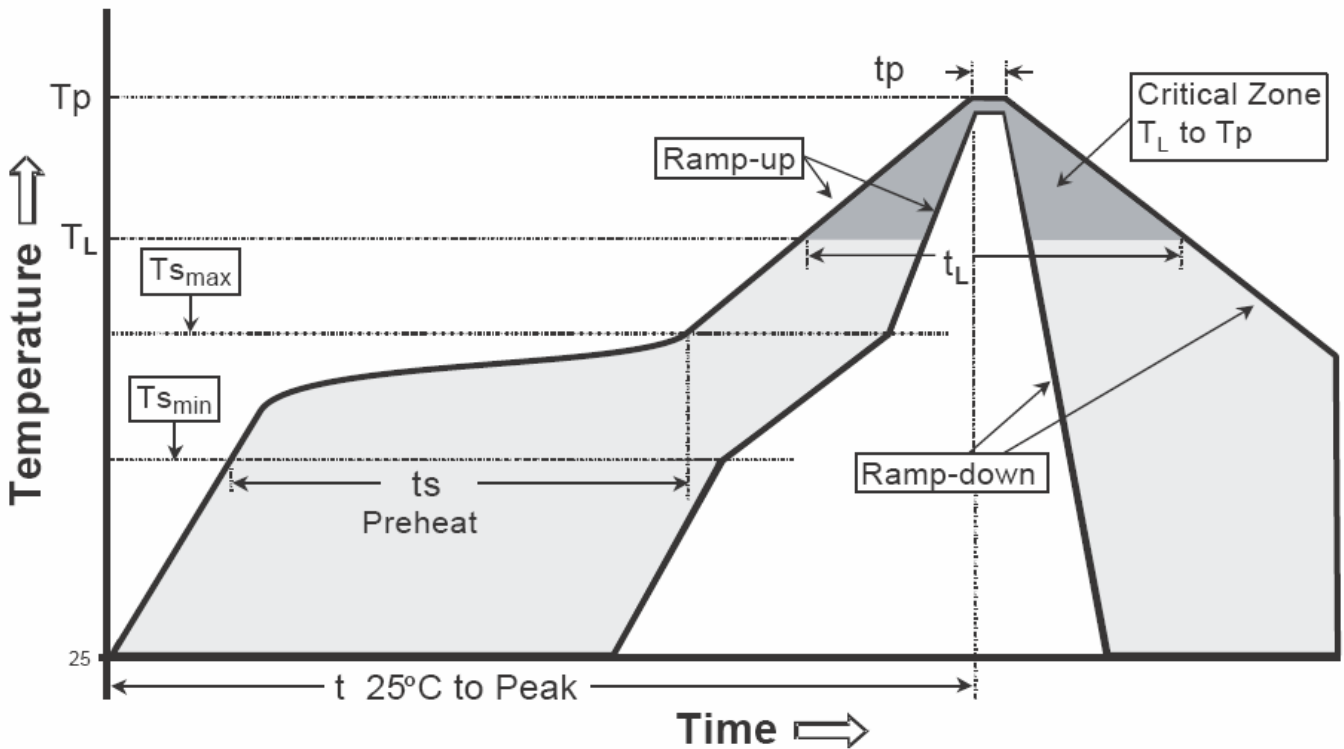
Carrier Tape Dimension



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

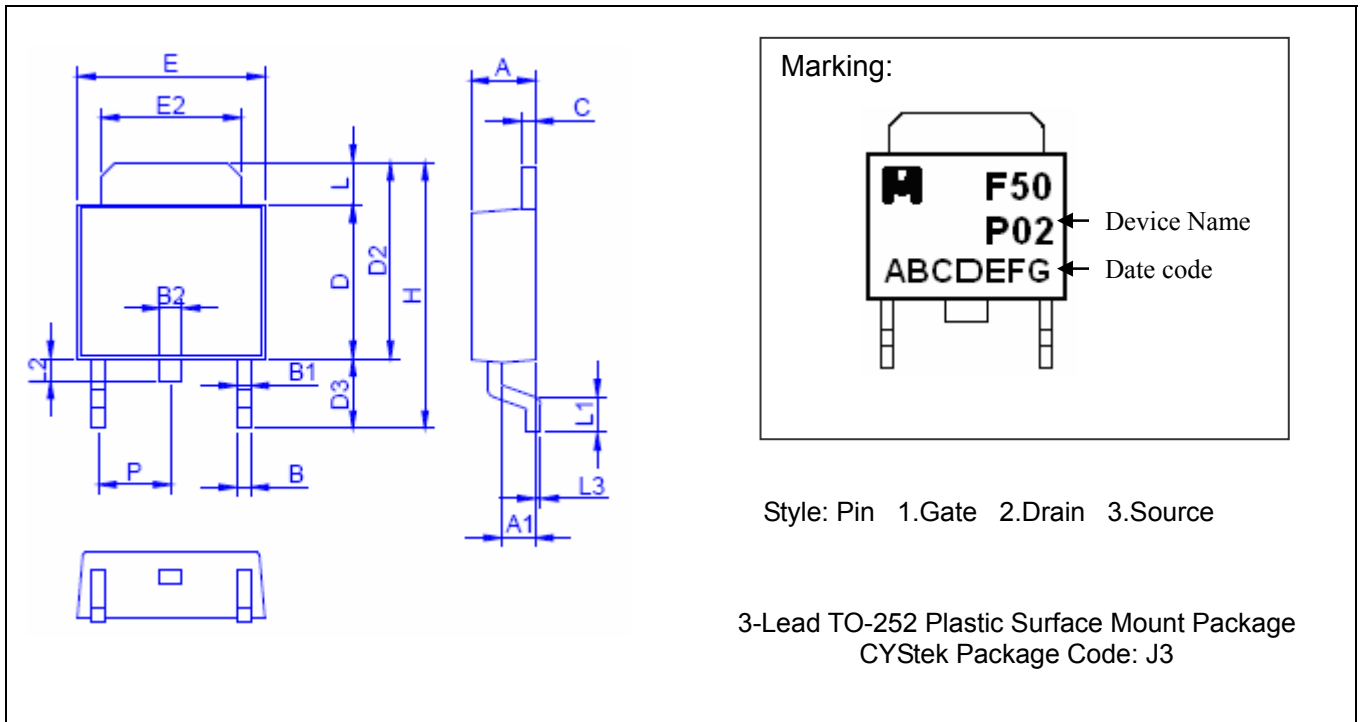
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(Ts min)	100°C	150°C
-Temperature Max(Ts max)	150°C	200°C
-Time(ts min to ts max)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (TL)	183°C	217°C
- Time (tL)	60-150 seconds	60-150 seconds
Peak Temperature(TP)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-252 Dimension



Marking:

Style: Pin 1.Gate 2.Drain 3.Source

3-Lead TO-252 Plastic Surface Mount Package
 CYStek Package Code: J3

DIM	Inches		Millimeters		DIM	Inches		Millimeters	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.0827	0.0984	2.10	2.50	E	0.2520	0.2638	6.40	6.70
A1	0.0374	0.0512	0.95	1.30	E2	0.1890	0.2146	4.80	5.45
B	0.0118	0.0335	0.30	0.85	H	0.3622	0.3996	9.20	10.15
B1	0.0157	0.0370	0.40	0.94	L	0.0350	0.0669	0.89	1.70
B2	0.0236	0.0394	0.60	1.00	L1	0.0354	0.0650	0.90	1.65
C	0.0157	0.0236	0.40	0.60	L2	0.0197	0.0433	0.50	1.10
D	0.2087	0.2441	5.30	6.20	L3	0.0000	0.0118	0.00	0.30
D2	0.2638	0.2874	6.70	7.30	P	0.0827	0.0984	2.10	2.50
D3	0.0866	0.1181	2.20	3.00					

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead : Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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