



SS52BF THRU SS520BF

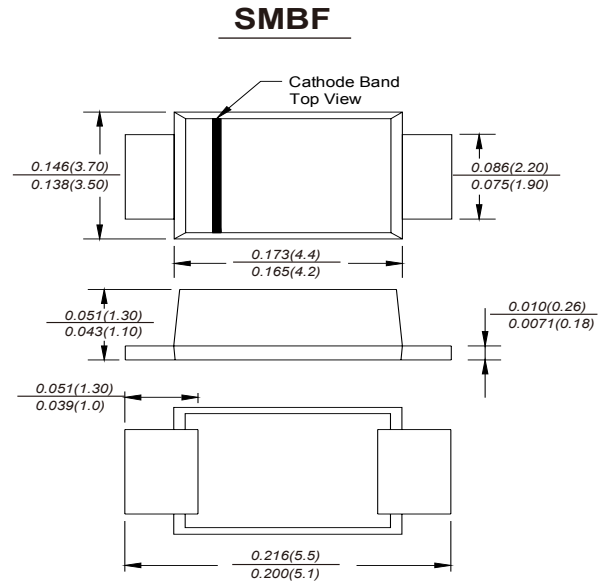
Surface Mount Schottky Barrier Rectifiers

FEATURES

- The plastic package carries Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- Ultra fast switching for high efficiency
- Low reverse leakage
- Built-in strain relief, ideal for automated placement
- High forward surge current capability
- High temperature soldering guaranteed
- 260°C/10 seconds at terminals
- Glass passivated chip junction

MECHANICAL DATA

- Case:** SMBF molded plastic body over passivated chip
- Terminals:** Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity:** Color band denotes cathode end
- Mounting Position:** Any



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

Ratings at 25°C ambient temperature unless otherwise specified.
Single phase half-wave 60Hz, resistive or inductive load, for capacitive load current derate by 20%.

| Parameter | Symbols | SS52BF | SS54BF | SS56BF | SS58BF | SS510BF | SS512BF | SS515BF | SS520BF | Units |
|---|-----------------|------------|--------|--------|--------|---------|---------|---------|---------|-------|
| Maximum Repetitive Peak Reverse Voltage | V_{RRM} | 20 | 40 | 60 | 80 | 100 | 120 | 150 | 200 | V |
| Maximum RMS voltage | V_{RMS} | 14 | 28 | 42 | 56 | 70 | 84 | 105 | 140 | V |
| Maximum DC Blocking Voltage | V_{DC} | 20 | 40 | 60 | 80 | 100 | 120 | 150 | 200 | V |
| Maximum Average Forward Rectified Current | $I_{F(AV)}$ | 5.0 | | | | | | | | A |
| Peak Forward Surge Current, 8.3ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method) | I_{FSM} | 150 | | | | | | | | A |
| Max Instantaneous Forward Voltage at 5 A | V_F | 0.55 | 0.70 | | 0.85 | | | | V | |
| Maximum DC Reverse Current $T_a = 25^\circ\text{C}$ at Rated DC Reverse Voltage $T_a = 100^\circ\text{C}$ | I_R | 1.0 | | | | 50 | | | | mA |
| Typical Junction Capacitance ⁽¹⁾ | C_j | 800 | 500 | | | | | pF | | |
| Typical Thermal Resistance ⁽²⁾ | $R_{\theta JA}$ | 45 | | | | | | | | °C/W |
| Operating Junction Temperature Range | T_j | -55 ~ +150 | | | | | | | | °C |
| Storage Temperature Range | T_{stg} | -55 ~ +150 | | | | | | | | °C |

(1) Measured at 1 MHz and applied reverse voltage of 4 V D.C
(2) P.C.B. mounted with 2.0" X 2.0" (5 X 5 cm) copper pad areas.



Fig.1 Forward Current Derating Curve

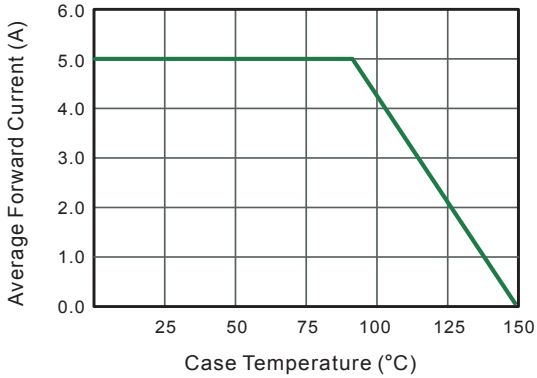


Fig.2 Typical Reverse Characteristics

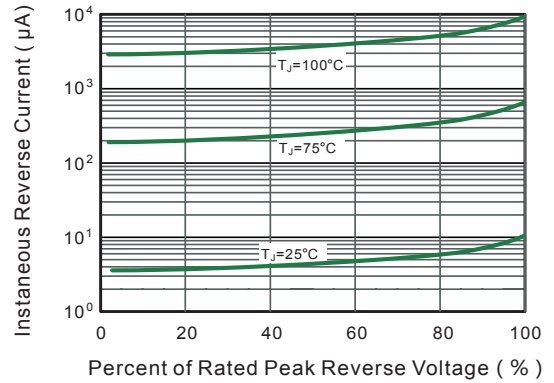


Fig.3 Typical Forward Characteristic

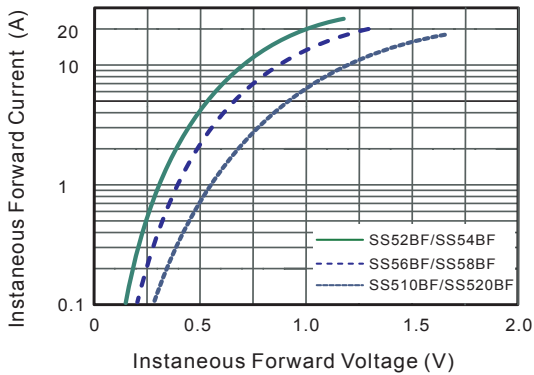


Fig.4 Typical Junction Capacitance

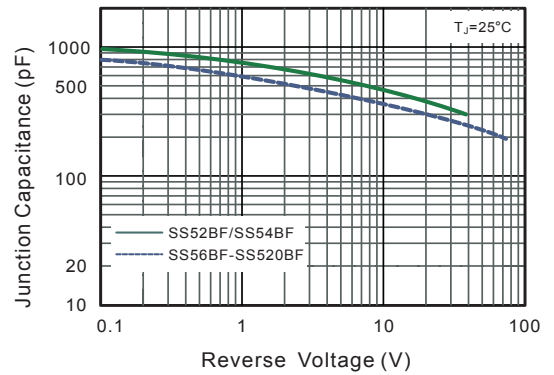


Fig.5 Maximum Non-Repetitive Peak Forward Surge Current

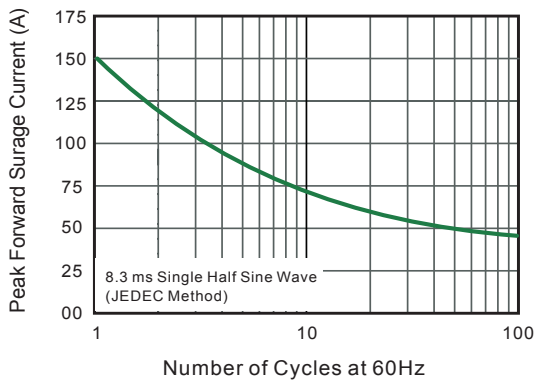


Fig.6- Typical Transient Thermal Impedance

